SHEET INDEX

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	B2CF	1																								
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	B3AB	1																								
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	B3AD	1																								
	B3CA	1																								
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	B4AD	1																								
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	B4CC	1																								
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	B5AB	1																								
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	B5CB	1																								
	B5CC	1																								
	B5CD	1																								
	B5GA	1																								
	B5GB	1																								
	B5GC	1																								
	B5GD	1																								
	B5GE	1																								
	B5GF	1																								
	B5GG	1																								
	B5GH	1																								
	B5GJ	1																								
	B5GK	1																								
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FS 6 DIAGNOSTIC ACCESS	B6AA	1																								
	B6AB	1																								
	B6AC	1																								
	B6AD	1																								
	B6AE	1																								
	B6CA	1																								
	B6CB	1																								
	B6CC	1																								
	B6CD	1																								
	B6GA	1																								
	B6GB	1																								
	B6GC	1																								
	B6GD	1																								

SUPPORTING INFORMATION

CATEGORY	NO.
CIRCUIT PACK SCHEMATIC	CPS-*
EQUIPMENT DRAWING	J1C052C
DC-DC CONVERTER (J87389F)	SD-82161-02
DC-DC CONVERTER (J87421A2, L2)	SD-82327-01
DC-DC CONVERTER (J87422B)	SD-82328-02

* - SCHEMATICS OF ALL FA FB AND FC - CODED CIRCUIT PACKS USED IN THIS SD ARE SHOWN ON DRAWINGS NUMBERED WITH A CPS PREFIX FOLLOWED BY THE CODE OF THE PACK. (e.g. CPS-FA1060).

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING ONLY THE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

NOTICE— [illegible]

COMMON SYSTEMS

MAIN STORE CONTROLLER AND MEMORY
CIRCUIT

BELL LABORATORIES

SD-1C902-03-A1

SHEET INDEX

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	B7AD	1																								
	B7CA	1																								
	B7CB	1																								
	B7CC	1																								
	B7CD	1																								
	B7CE	1																								
	B7CF	1																								
	B7CG	1																								
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CAD 1 UNIT SYMBOL	GB2	1																								
	GB3	1																								
P/0 CAD 2	GB4	1																								
	GB5	1																								
	GB6	1																								
P/0 CAD 2, CAD 3,4	GB7	1																								

MAIN STORE CONTROLLER AND MEMORY ②

SD-1C902-03-A2

BELL TELEPHONE LABORATORIES
INCORPORATED

6S

DESIGNATION MNEMONICS INDEX

MNEMONIC	FS/SYM	DEFINITION
+24V	10/4	+24 VOLT FRAME POWER
-48TMASC	10/4	-48 VOLT RETURN ON MAIN STORE CONTROLLER
-48TMEM(A-L)	9/1	-48 VOLT RETURN ON MEMORY MODULE
-48VMASC	10/4	-48 VOLT (POWER) ON MAIN STORE CONTROLLER
-48VMEM(A-L)	9/1	-48 VOLT (POWER) ON MEMORY MODULE
-5V	7/2	-5 VOLT (MEMORY POWER)
ADPARER0	5/5	ADDRESS PARITY ERROR (WITHIN MAIN STORE CONTROLLER)
AD150	2/6	ADDRESS 15 (TO FA1065 FOR WRITE PROTECT DECODING)
AD160	2/1	ADDRESS 16 (TO FA1065 FOR WRITE PROTECT DECODING)
APER0(A,B)	7/1,15	ADDRESS PARITY ERROR
AP17.81(A,B)	2/17	ADDRESS PARITY 17 THROUGH 8 (PARITY OVER ADDRESS BITS 8-17 TO FANOUT BOARD FROM MAIN STORE CONTROLLER)
AP7.01(A,B)	2/1	ADDRESS PARITY 7 THROUGH 1 (PARITY OVER ADDRESS BITS 1-7 TO FANOUT BOARD FROM MAIN STORE CONTROLLER)
ARRE01	3/3	A REFRESH REQUEST (PRIMARY INDICATOR OF 'REFRESH CYCLE PENDING' FUNCTION)
ARTIM1	3/1	A REFRESH TIME (PRIMARY INDICATOR OF 'REFRESH CYCLE REQUESTED' FUNCTION)
ARTIM1B	5/2	A REFRESH TIME (PRIMARY INDICATOR OF 'REFRESH CYCLE REQUESTED' FUNCTION)
ASEL0	1/1	A SELECTED (PRIMARY INDICATOR OF 'THIS STORE SELECTED' FUNCTION)
A(00-15)1(A,B)	2/6-13	ADDRESS (00-15) (FROM MAIN STORE CONTROLLER TO FANOUT BOARD)
A(16,17)1(A,B)	2/1,14	ADDRESS (16,17) (FROM MAIN STORE CONTROLLER TO FANOUT BOARD)
BIT0010	1/1	BIT 0 (BIT 0 ACTIVE ON TIMING SHIFT REGISTER OF FA1071)
BIT4B0	1/1	BIT 4 (BIT 4 ACTIVE ON TIMING SHIFT REGISTER OF FA1071)
BIT5B0	1/1	BIT 5 (BIT 5 ACTIVE ON TIMING SHIFT REGISTER OF FA1071)
BLDWR0	1/2	BLIND WRITE (DECODED 2/4 STORE COMMAND)
BLNLD0	1/3	BLOCK NORMAL LOAD
BLWTOP0	5/3	BLIND WRITE OPTION
BSEL0	1/5	B SELECTED (SECONDARY INDICATOR OF 'THIS STORE SELECTED' FUNCTION)
BUSRD0(A-C)	1/2	BUS READ (A-C)
CEA(0-2)1(A,B)	7/1,15	CHIP ENABLE ADDRESS (0-2) (FANOUT BOARD TO MEMORY PLANE)
CHSAD0	2/15	CHECK STORE ADDRESS
CHSCM1	1/1	CHECK STORE COMPLETE (USED IN CHECK CIRCUITRY)
CLER0	6/3	CLEAR ERROR
CLINIT1	6/1	CLEAR INITIALIZE
CLOCK0(A,B)	1/20	CLOCK (OF MAIN STORE CONTROLLER)
CNLD0	1/1	COMMAND NORMAL LOAD
COMDE40	6/2	COMMAND DECODED EA (DECODED SERIAL I/O COMMAND TO LOAD ERROR REGISTER ON FA1063)
COMER0	5/2	COMMAND ERROR (ERROR ON 2/4 STORE COMMAND DECODING
COMPRD0(A-C)	5/4	COMPLEMENT READ (A-C)
COMWRT0(A-C)	1/3	COMPLEMENT WRITE (A-C)
CS0(A,B)	1/1	CHIP SELECT (A,B) (TO FANOUT BOARD FROM MAIN STORE CONTROLLER)
CVFA1	9/2	CONVERTER FUSE ALARM
DPH0	2/1	DATA PARITY HIGH (FROM FA1060 FOR PARITY CHECK)
DPL0	2/1	DATA PARITY LOW (FROM FA1060 FOR PARITY CHECK)
DPWER0	5/6	DATA PARITY WIRE ERROR
DSTB0	1/1	DATA STROBE
D(00-23)0	2/2-13	DATA BIT (00-23) (FROM FA1060 FOR PARITY CHECK)
ENGOXMT0	6/2	ENGAGE OUTWARD TRANSMIT (ENABLE OUTWARD SERIAL I/O TRANSMISSION)
ENSERC1	1/1	ENABLE STORE ERROR C
ERBPH1	6/3	ERROR REGISTER BIT PARITY HIGH
ERBPL1	6/3	REGISTER REGISTER BIT PARITY LOW
ERB(00-15)1	6/13-20	ERROR REGISTER BITS 0-15
FBIT090	6/2	FORCE BIT 9 (IN TIMING SHIFT REGISTER OF FA1071, TO ACTIVE)
FBIT110	6/3	FORCE BIT 11 (IN TIMING SHIFT REGISTER OF FA1071, TO ACTIVE)
FCHKER(A,B)0	6/3	FORCE CHECK ERROR (FORCE NORMAL SELECT ERROR CHECK ON FA1066 TO ERROR CONDITION)
FCLB110	6/3	FORCE CLEAR BIT 11 (IN TIMING SHIFT REGISTER OF FA1071)
FCLW(PB,P)0	6/2	FORCE CLEAR WRITE PROTECT (REGISTERS ON FA1060)
FDISRUN0	6/3	FORCE DISABLE RUN
FFOBRM0	6/2	FORCE FANOUT BOARD REFRESH
FLDSHT00	6/3	FORCE LOAD SHIFTER 0 (LOAD ACTIVE BIT INTO LOW ORDER POSITION OF FA1071 TIMING SHIFT REGISTER)
FLDXERB0	6/3	FORCE LOAD EXTRA ERROR
FLDXER0	6/3	FORCE LOAD EXTRA ERROR
FNOTIM0	6/3	FORCE NO TIMING (TO MEMORY)
FNRREQ0	6/2	FORCE NO REFRESH REQUEST
FNWRTMD0	6/3	FORCE 'NO WRITE' MODE
FOBATER0	5/3	FANOUT BOARD A TIMING ERROR
FOBBTER0	5/3	FANOUT BOARD B TIMING ERROR
FOBMT0	6/3	FANOUT BOARD MAINTENANCE
FOBMT1(A,B)	6/6	FANOUT BOARD MAINTENANCE
FRREQA0	6/3	FORCE REFRESH REQUEST A (PRIMARY)
FRREQB0	6/3	FORCE REFRESH REQUEST B (DUPLICATE)
FSELA0	6/2	FORCE SELECT A (PRIMARY)
FSELB0	6/3	FORCE SELECT B (SECONDARY)
FSTNM0	6/3	FORCE STORE TO NORMAL MODE
FSTOPCK0	6/2	FORCE STOP CLOCK (MAIN CLOCK IN MAIN STORE CONTROLLER)
FSTRM0	6/3	FORCE STORE TO REFRESH MODE
FSTSBY0	6/3	FORCE SET STORE BUSY
F0.10TM0	6/2	FORCE 0 THROUGH 10 TIMING (INDICATOR OF ACTIVE BIT IN FA1071 TIMING SHIFT REGISTER)
GCAS0(A,B)	7/1,15	GATED COLUMN ADDRESS STROBE
GRAS0(A,B)	7/1,15	GATED ROW ADDRESS STROBE
GRCA(0-6)0(A,B)	7/1,15	GATED ROW-COLUMN ADDRESS
GRDA	9/1	GROUND
GRW0(A,B)	7/1,15	GATED READ/WRITE
ICHDPARC	5/8	ISOLATION CHECK DATA PARITY
ICHD(16-19)0	5/9-12	ISOLATION CHECK DATA (16-19)
ICHEPAR0	6/22	ISOLATION CHECK EXTENDED PARITY
ICHTIM0	6/21	ISOLATION CHECK TIMING
INITBBB0	6/3	INITIALIZE
INITBB0	6/3	INITIALIZE
INITB0	6/3	INITIALIZE
INIT0	6/3	INITIALIZE
IOB(04-11)0	6/2	I/O BIT (4-11)
IOCHOR(N,P)1	6/1	I/O CHANNEL ONE RECEIVE (NEGATIVE, POSITIVE)
IOCHOS(N,P)1	6/1	I/O CHANNEL ONE SEND (NEGATIVE, POSITIVE)
IOCHZR(N,P)1	6/1	I/O CHANNEL ZERO RECEIVE (NEGATIVE, POSITIVE)
IOCHZS(N,P)1	6/1	I/O CHANNEL ZERO SEND (NEGATIVE, POSITIVE)
IODECCH0	5/7	I/O DECODER CHECK
IODIN0	6/1	I/O DATA INPUT
IODOUT0	6/2	I/O DATA OUTPUT
IOENCH00	1/5	I/O ENABLE CHANNEL ONE
IORESET0	6/1	I/O RESET
IOSHFT0	6/1	I/O SHIFT
ISOCHBB0	6/3	ISOLATION CHECK
ISOCHB0	6/3	ISOLATION CHECK
ISOCH0	6/3	ISOLATION CHECK
LADPH0	2/17	LATCHED ADDRESS PARITY HIGH
LAD170	2/14	LATCHED ADDRESS 17
LDDCFWT0	6/2	LOAD DECODER CHECK. FORCE WRITE (MODE)
LDIODCH0	6/7	LOAD I/O ORDER DECODER CHECK
LDSPARE0	1/2	LOAD SPARE READ
MADPH1	2/15	MODIFIED ADDRESS PARITY HIGH
MODEER0	5/2	MODE ERROR
MODSEL(0-7)0	4/1	MOD SELECT (0-7)
MSEL(A,B)	7/1,15	MOD SELECTED (HARD-WIRED ADDRESS OUTPUT ON FANOUT BOARD)
MSEL(0,1)0	7/1,15	MOD SELECTED (0,1)
NLD0	1/1	NORMAL LOAD
NOCS0	1/2	NO CHIP SELECT
NO2DPH(I,O)0	2/18	NUMBER 2 DATA PARITY HIGH (INPUT, OUTPUT)
NO2DPL(I,O)0	2/18	NUMBER 2 DATA PARITY LOW (INPUT, OUTPUT)
NPA0	10/8	NEGATIVE POWER ALARM (POWER ALARM STATUS AND RESET FOR CONVERTERS)
NSELER0	5/5	NORMAL SELECT ERROR
OSPAT0	10/8	START POWER ALARM TEST (INTERNAL POWER ALARM SIGNAL)
PAI1	9/2	POWER ALARM INPUT
PAI21	10/8	POWER ALARM INPUT
PAD1	10/8	POWER ALARM INPUT
PAT0	9/2	POWER ALARM TEST
PA1	9/1	POWER ALARM
PHER0	5/3	PARITY HIGH ERROR
PINIT0	1/20	POWER INITIALIZE
PINIT1	10/9	POWER INITIALIZE
PLER0	5/2	PARITY LOW ERROR
RADCL0	6/2	REFRESH ADDRESS CLEAR
RADCNT0	3/2	REFRESH ADDRESS COUNTER
RB(0-3)CAR0	3/6-9	REFRESH ADDRESS (0-3) CARRY
RB(4,5)CAR0	3/12,13	REFRESH ADDRESS (4,5) CARRY
RDDPH0(A,B)	7/2,16	READ DATA PARITY HIGH
RDDPL0(A,B)	7/2,16	READ DATA PARITY LOW
RDD(00-23)0 (A,B)	7/3-28	READ DATA (00-23)
RD(0-5)CAR0	3/9-12	REFRESH CHECK (0-5) CARRY

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
C2

ISSUE
1

BELL LABORATORIES
SD-1C902-03
A3

PRINTED IN U. S. A.

DESIGNATION MNEMONICS INDEX

MNEMONIC	FS/SYM	DEFINITION
READADB0	6/3	READ ADDRESS (ONTO DATA BUS)
READAD0	6/3	READ ADDRESS (ONTO DATA BUS)
READ0(A-D)	1/2	READ
REFMD0(A,B)	1/1	REFRESH MODE
REFMODE0	1/1	REFRESH MODE
REF0(A,B)	7/1,15	REFRESH
RSELER0	5/5	REFRESH SELECT ERROR
RSEL(0-7)B1	5/5	REFRESH SELECT
RSEL(0,1)	7/1,15	REFRESH SELECT
RW0(A,B)	1/2	READ/WRITE
SAC(I,0)0	1/3	STORE AUTOMATIC CORRECTION (INPUT, OUTPUT)
SADD(18,19)0	2/16	STORE ADDRESS (18,19)
SAD(18,19) (0,1)	2/15,16	STORE ADDRESS (18,19)
SAPH(I,0)0	2/17	STORE ADDRESS PARITY HIGH (INPUT, OUTPUT)
SAPL(I,0)0	2/17	STORE ADDRESS PARITY LOW (INPUT, OUTPUT)
SA(00-19)(I,0)0	2/1-16	STORE ADDRESS (00-19) (INPUT, OUTPUT)
SBY(I,0)0	1/3	STORE BUSY (INPUT, OUTPUT)
SBY0	1/1	STORE BUSY
SCM(B,C)0	1/3,4	STORE COMPLETE
SCMER0	5/3	STORE COMPLETE ERROR
SCM(I,0)0	1/3	STORE COMPLETE (INPUT, OUTPUT)
SCW(I,0)0	1/3	STORE COMPLEMENT WRITE (INPUT, OUTPUT)
SC(0-3)(I,0)0	1/2	STORE COMMAND (0-3) (INPUT, OUTPUT)
SDEPH(I,0)0	2/18	STORE DATA EXTENDED PARITY HIGH (INPUT, OUTPUT)
SDPH(I,0)0	2/1	STORE DATA PARITY HIGH (INPUT, OUTPUT)
SDPL(I,0)0	2/1	STORE DATA PARITY LOW (INPUT, OUTPUT)
SD(00-23)(I,0)0	2/2-13	STORE DATA (00-23) (INPUT, OUTPUT)
SELER0	5/1	SELECT ERROR
SELRC0(A,B)	7/1,15	SELECTED ROW-COLUMN
SELRW0(A,B)	7/1,15	SELECTED READ/WRITE (SANITY INDICATOR FROM FANOUT BOARD)
SEL0(G,1)	1/1,5	SELECT (0,2)
SEL1(0,1)	1/1,5	SELECT (1,3)
SERA(I,0)0	5/4	STORE ERROR A (INPUT, OUTPUT)
SERB(I,0)0	5/3	STORE ERROR B (INPUT, OUTPUT)
SERC(B,C)0	1/4	STORE ERROR C
SERC(I,0)0	5/4	STORE ERROR C (INPUT, OUTPUT)
SGO(A,B)1	1/3,4	STORE GO
SGO(I,0)0	1/1,3	STORE GO (INPUT, OUTPUT)
SGO(0,1)	1/1	STORE GO
SHBIT090	1/1	SHIFTER BIT 9 (AN FA1071 MAIN TIMING GENERATOR SHIFT REGISTER)
SHBIT110	1/1	SHIFTER BIT 11 (AN FA1071 MAIN TIMING GENERATOR SHIFT REGISTER)
SHIFTER0	5/1	SHIFT ERROR (ON FA1071 MAIN TIMING GENERATOR SHIFT REGISTER)
SMS(I,0)0	1/5	STORE MAINTENANCE STATE (INPUT, OUTPUT)
SPARE(00-23)	7/1-28	SPARE
SREQ(I,0)0	1/20	STORE REQUEST (INPUT, OUTPUT)
STA001	10/1	START A
STA011	9/2	START A
STB01	10/9	START B
STB1	10/9	START B
S9RTN	9/1	S9 RETURN (GROUND)
TERROR0	5/13	TIED ERROR (ESSENTIALLY ERROR A)
TINWP0	4/4	TIED IN WRITE PROTECTED (REGION OF MEMORY)
TLI1	10/7	TERMINATION LOOP INPUT
UNSLER0(A,B)	7/1,15	UNSELECTED ERROR
WPAD(12-14)0	2/7-9	WRITE PROTECT ADDRESS (12-14)
WPRRDB0	1/2	WRITE-PROTECT READ
WPRRD0	1/2	WRITE-PROTECT READ
WPSEG(0-7)0	4/2	WRITE-PROTECT SEGMENT (0-7)
WPWRTB0	1/2	WRITE-PROTECT WRITE
WPWRT0	1/2	WRITE-PROTECT WRITE
WRITE0	1/2	WRITE (COMMAND)
WRTDPH0(A,B)	2/1	WRITE DATA PARITY HIGH
WRTDPL0(A,B)	2/1	WRITE DATA PARITY LOW
WRTD(00-23)0 (A,B)	2/2-13	WRITE DATA (00-23)
WRTMD0(A-C)	1/3	WRITE MODE
WTDPHER0	5/4	WRITE DATA PARITY HIGH ERROR
WTDPLER0	5/4	WRITE DATA PARITY LOW ERROR
WTR0	1/5	WRITE
XBKSCM1	5/1	ON BOARD INTERCONNECT, BLOCK STORE COMPLETE
XBLB4CS0	6/4	ON BOARD INTERCONNECT, BLOCK CHIP SELECT AT BIT 4 TIME
XBLDWR0	1/5	ON BOARD INTERCONNECT, BLIND WRITE
XBUSRDB(0,1)	1/19	ON BOARD INTERCONNECT, BUS READ
XBUSRD0(A-H, J-N)	2/6-18	ON BOARD INTERCONNECT, BUS READ
XBUSRD1(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, BUS READ
XCAAB0	6/3	ON BOARD INTERCONNECT, AA (DIAGNOSTIC 4/8 CODE)
XCBIT031	1/1	ON BOARD INTERCONNECT, BIT 3 (MAIN TIMING SHIFT REGISTER)
XCBIT5B1	1/1	ON BOARD INTERCONNECT, BIT 5 (MAIN TIMING SHIFT REGISTER)
XCBUSRD(0,1)	1/19	ON BOARD INTERCONNECT, BUS READ
XCB1B0	6/3	ON BOARD INTERCONNECT, CLOCK
XCB2BB0	6/3	ON BOARD INTERCONNECT, B2 (DIAGNOSTIC 4/8 CODE)
XCCCB0	6/3	ON BOARD INTERCONNECT, CC (DIAGNOSTIC 4/8 CODE)
XCCK1B0	1/1	ON BOARD INTERCONNECT, CLOCK
XCCOMAB0	1/2	ON BOARD INTERCONNECT, COMMAND A
XCC(00,01)	1/2	ON BOARD INTERCONNECT, COMMAND 0
XCC11	1/2	ON BOARD INTERCONNECT, COMMAND 1
XCC(20,21)	1/2	ON BOARD INTERCONNECT, COMMAND 2
XCC30	1/2	ON BOARD INTERCONNECT, COMMAND 3
XCC6B0	6/3	ON BOARD INTERCONNECT, C6 (DIAGNOSTIC 4/8 CODE)
XCC9B0	6/3	ON BOARD INTERCONNECT, C9 (DIAGNOSTIC 4/8 CODE)
XCD4B0	6/3	ON BOARD INTERCONNECT, D4 (DIAGNOSTIC 4/8 CODE)
XCE4B0	6/3	ON BOARD INTERCONNECT, E4 (DIAGNOSTIC 4/8 CODE)
XCE8BB0	6/3	ON BOARD INTERCONNECT, E8 (DIAGNOSTIC 4/8 CODE)
XCF0BB0	6/3	ON BOARD INTERCONNECT, F0 (DIAGNOSTIC 4/8 CODE)
XCID040	1/1	ON BOARD INTERCONNECT, IDLE 0-4 (SHIFT REGISTER BITS)
XCINIT0	6/5	ON BOARD INTERCONNECT, INITIALIZE
XCINIT0(A,B)	6/4	ON BOARD INTERCONNECT, INITIALIZE
XCINIT1(A-H, J-N)	6/8-20	ON BOARD INTERCONNECT, INITIALIZE
XCNLDB(0,1)	1/1	ON BOARD INTERCONNECT, NORMAL LOAD
XCNLDB1(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, NORMAL LOAD
XCNLD0(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, NORMAL LOAD
XCNOTWC1	1/2	ON BOARD INTERCONNECT, NOT A WRITE COMMAND
XCNRD1(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, NORMAL READ
XCOMWT0(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, COMPLEMENT WRITE
XCREF0(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, REFRESH
XCREF1(A-H, J-N)	1/6,18	ON BOARD INTERCONNECT REFRESH
XCRMBB0	5/1	ON BOARD INTERCONNECT, REFRESH MODE
XCRMB(0,1)	1/5,1	ON BOARD INTERCONNECT, REFRESH MODE
XCRM1	1/1	ON BOARD INTERCONNECT, REFRESH MODE
XCRREQ0	3/3	ON BOARD INTERCONNECT, REFRESH REQUEST
XCSD01B(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, STORE DATA 0
XCSD01C(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, STORE DATA
XCSD11B(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, STORE DATA 1
XCSD11C(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, STORE DATA
XCSEL0(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, CHIP SELECT
XCSEL1	1/1	ON BOARD INTERCONNECT, CHIP SELECT
XCSEL1(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, CHIP SELECT
XCTAD00(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, TIED ADDRESS 0
XCTAD10(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, TIED ADDRESS 1
XCTCLR00	3/2	ON BOARD INTERCONNECT, TIED CLEAR REFRESH REQUEST
XCTCL590	6/7	ON BOARD INTERCONNECT, TIED CLEAR 5 AND 9 (STAGES OF)
XCTINWP1	5/3	ON BOARD INTERCONNECT, TIED IN WRITE PROTECTED (AREA)
XCTS0(F,G)0	5/5	ON BOARD INTERCONNECT, TIED STORE COMMAND (F,G) (FORCE COMMAND CHECK INTO ERROR CONDITION)
XCTSEL0	1/1	ON BOARD INTERCONNECT, TIED SELECT
XC1BB0	6/3	ON BOARD INTERCONNECT, 1B (DIAGNOSTIC 4/8 CODE)
XC17B0	6/3	ON BOARD INTERCONNECT, 17 (DIAGNOSTIC 4/8 CODE)
XC2BB0	6/3	ON BOARD INTERCONNECT, 2B (DIAGNOSTIC 4/8 CODE)
XC33B0	6/3	ON BOARD INTERCONNECT, 33 (DIAGNOSTIC 4/8 CODE)
XC36B0	6/3	ON BOARD INTERCONNECT, 36 (DIAGNOSTIC 4/8 CODE)
XC39B0	6/3	ON BOARD INTERCONNECT, 39 (DIAGNOSTIC 4/8 CODE)
XC4DB0	6/3	ON BOARD INTERCONNECT, 4D (DIAGNOSTIC 4/8 CODE)

MAIN STORE CONTROLLER AND MEMORY

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DESIGNATION MNEMONICS INDEX

MNEMONIC	FS/SYM	DEFINITION
XC4EB0	6/3	ON BOARD INTERCONNECT, 4E (DIAGNOSTIC 4/8 CODE)
XC55B0	6/3	ON BOARD INTERCONNECT, 55 (DIAGNOSTIC 4/8 CODE)
XC6CB0	6/3	ON BOARD INTERCONNECT, 6C (DIAGNOSTIC 4/8 CODE)
XC63B0	6/3	ON BOARD INTERCONNECT, 63 (DIAGNOSTIC 4/8 CODE)
XC69B0	6/3	ON BOARD INTERCONNECT, 69 (DIAGNOSTIC 4/8 CODE)
XC7RRE01	3/3	ON BOARD INTERCONNECT, 7 REFRESH REQUEST (DELAYS)
XC71B0	6/3	ON BOARD INTERCONNECT, 71 (DIAGNOSTIC 4/8 CODE)
XC72B0	6/3	ON BOARD INTERCONNECT, 72 (DIAGNOSTIC 4/8 CODE)
XC8DB0	6/3	ON BOARD INTERCONNECT, 8D (DIAGNOSTIC 4/8 CODE)
XC8EB0	6/3	ON BOARD INTERCONNECT, 8E (DIAGNOSTIC 4/8 CODE)
XC9CB0	6/3	ON BOARD INTERCONNECT, 9C (DIAGNOSTIC 4/8 CODE)
XC93B0	6/3	ON BOARD INTERCONNECT, 93 (DIAGNOSTIC 4/8 CODE)
XC96B0	6/3	ON BOARD INTERCONNECT, 96 (DIAGNOSTIC 4/8 CODE)
XERCOL1(A-H, J-N)	4/3-15	ON BOARD INTERCONNECT, ERROR COLLECT
XFBIT090	6/4	ON BOARD INTERCONNECT, FORCE BIT 9
XFBIT110	6/4	ON BOARD INTERCONNECT, FORCE BIT 11
XFCLRE00	6/7	ON BOARD INTERCONNECT, FORCE CLEAR REQUEST (REFRESH)
XFLSB000	6/4	ON BOARD INTERCONNECT, FORCE LOAD SHIFT BIT 0
XFSTPCK0	6/1	ON BOARD INTERCONNECT, FORCE STOP CLOCK
XFSTRM0	6/4	ON BOARD INTERCONNECT, FORCE STORE REFRESH MODE
XFSTRR00	6/4	ON BOARD INTERCONNECT, FORCE STORE REFRESH REQUEST (ACTIVE)
XFSTSBY0	6/4	ON BOARD INTERCONNECT, FORCE STORE BUSY (ACTIVE)
XF110	6/6	ON BOARD INTERCONNECT, FORCE 11 (CHECK CIRCUIT DTD FAILURE MODE)
XINITB0	6/7	ON BOARD INTERCONNECT, INITIALIZE
XINITB0(A-D)	6/4	ON BOARD INTERCONNECT, INITIALIZE
XINIT0	6/1	ON BOARD INTERCONNECT, INITIALIZE
XISOCK0(A-H, J-N)	6/8-20	ON BOARD INTERCONNECT, ISOLATION CHECK
XLBT13A1	1/1	ON BOARD INTERCONNECT, LATCHED BIT 13
XLDSPR0	1/5	ON BOARD INTERCONNECT, LOAD SPARE READ (STORE COMMAND)
XLDWP0	1/5	ON BOARD INTERCONNECT, LOAD WRITE PROTECT
XLSAC0	1/3	ON BOARD INTERCONNECT, LATCHED STORE AUTOMATIC CORRECTION
XLSCW0	1/3	ON BOARD INTERCONNECT, LATCHED STORE COMPLEMENT WRITE
XNFOBRM0	5/1	ON BOARD INTERCONNECT, NO FAN-OUT-BOARD REFRESH MODE
XNITBC0	6/3	ON BOARD INTERCONNECT, INITIALIZE
XNIT0A	6/6	ON BOARD INTERCONNECT, INITIALIZE
XNIT0A(A-H, J-N)	2/1-3	ON BOARD INTERCONNECT, INITIALIZE
XNIT0(B-D)A	6/6	ON BOARD INTERCONNECT, INITIALIZE
XNLDB1	1/5	ON BOARD INTERCONNECT, NORMAL LOAD
XNLD0	1/5	ON BOARD INTERCONNECT, NORMAL LOAD
XRDSTB0(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, READ STROBE
XRDSTB1(A-H, J-N)	1/6-18	ON BOARD INTERCONNECT, READ STROBE
XRD1	1/1	ON BOARD INTERCONNECT, READ
XREF0	3/4	ON BOARD INTERCONNECT, REFRESH
XRM0	1/5	ON BOARD INTERCONNECT, REFRESH MODE
XRM0(A,B)	1/2	ON BOARD INTERCONNECT, REFRESH MODE
XRM1	1/5	ON BOARD INTERCONNECT, REFRESH MODE
XSDPW0	2/18	ON BOARD INTERCONNECT, STORE DATA PARITY W
XSDPW1(A,B)	2/18	ON BOARD INTERCONNECT, STORE DATA PARITY W
XSDPX1B	2/18	ON BOARD INTERCONNECT, STORE DATA PARITY X
XSDPY21B	2/18	ON BOARD INTERCONNECT, STORE DATA PARITY Y2
XSEL(0,1)	1/19	ON BOARD INTERCONNECT, SELECTED
XSEL0A	1/3	ON BOARD INTERCONNECT, SELECTED
XSOCKB1(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, ISOLATION CHECK
XSTRWP0	1/5	ON BOARD INTERCONNECT, STORE READ (OF) WRITE PROTECT (REGISTERS)
XTBIT001	1/1	ON BOARD INTERCONNECT, TIED BIT 0 (MAIN TIMING SHIFT REGISTER)
XTBKSCM0	5/1	ON BOARD INTERCONNECT, TIED BLOCK STORE COMPLETE
XTCLCOM0	1/2	ON BOARD INTERCONNECT, TIED CLEAR COMMAND REGISTERS
XTCLER0(A-H, J-N)	5/9-20	ON BOARD INTERCONNECT, TIED CLEAR ERROR
XTCLRM0	1/1	ON BOARD INTERCONNECT, TIED CLEAR REFRESH MODE
XTCLRRQ0	1/1	ON BOARD INTERCONNECT TIED CLEAR REFRESH MODE
XTCLSB10	1/1	ON BOARD INTERCONNECT, TIED CLEAR STORE BUSY
XTCLSCM0	1/1	ON BOARD INTERCONNECT, TIED CLEAR STORE COMPLETE
XTCL590	6/6	ON BOARD INTERCONNECT, TIED CLEAR 5 & 9 (STAGES OF REFRESH COUNTER)
XTDB120	6/4	ON BOARD INTERCONNECT, TIED DISABLE BIT 12, (MAIN TIMING SHIFT REGISTER)
XTFOBRM0	1/1	ON BOARD INTERCONNECT, TIED FAN-OUT BOARD (TO) REFRESH MODE
XTNOCS0	1/1	ON BOARD INTERCONNECT, TIED NO CHIP SELECT
XTPWHEV0	2/18	ON BOARD INTERCONNECT, TIED PARITY W HIGH (EVEN)
XTPWHOD0	2/18	ON BOARD INTERCONNECT, TIED PARITY W HIGH (ODD)
XTPWLEV0	2/18	ON BOARD INTERCONNECT, TIED PARITY W LOW (EVEN)
XTPWLOD0	2/18	ON BOARD INTERCONNECT, TIED PARITY W LOW (ODD)
XTRED00(A-H, J-N)	4/3-15	ON BOARD INTERCONNECT, TIED READ ERROR DATA
XTRED01(A-H, J-N)	2/1-3	ON BOARD INTERCONNECT, TIED READ ERROR DATA
XTRED10(A-H, J-N)	4/3-15	ON BOARD INTERCONNECT, TIED READ ERROR DATA
XTRED11(A-H, J-N)	2/1-13	ON BOARD INTERCONNECT, TIED READ ERROR DATA
XTRTTIM0	1/1	ON BOARD INTERCONNECT, TIED RESET TIMING
XTSC31(B-D)	1/2	ON BOARD INTERCONNECT, TIED STORE COMMAND 3
XTSER00(A-H, J-N)	5/8-20	ON BOARD INTERCONNECT, TIED STORE ERROR REGISTER
XTSER10(A-H, J-N)	5/8-20	ON BOARD INTERCONNECT, TIED STORE ERROR REGISTER
XTWRTPK1	1/3	ON BOARD INTERCONNECT, TIED WRITE PARITY CHECK
XWR0	1/5	ON BOARD INTERCONNECT, WRITE
XXINIT0	6/21	ON BOARD INTERCONNECT, INITIALIZE
XXNLDB1	1/20	ON BOARD INTERCONNECT, NORMAL LOAD
XXNLD0	1/20	ON BOARD INTERCONNECT, NORMAL LOAD
0.10TIM0	1/1	0 THROUGH 10 TIMING (MAIN TIMING SHIFT REGISTER)
0GRDM00(A,B)	7/1,15	GROUND
12F046	10/5	+12 VOLT REFERENCE
12T046	10/5	+12 VOLT RETURN (FOR REFERENCE)
12V	7/2	+12 VOLT (MEMORY POWER)
3F046(A-D)	10/5,6	+3 VOLT REFERENCE
3G046(A-D)	10/5,6	+3 VOLT REFERENCE RETURN
3T046(A-D)	1/1-3,7	+3 VOLT RETURN (TO STORE CONTROLLER)
3T046(AA-AG)	10/1	+3 VOLT RETURN (TO STORE CONTROLLER)
3T046(BA-BG)	10/2	+3 VOLT RETURN (TO STORE CONTROLLER)
3T046(CA-CG)	10/3	+3 VOLT RETURN (TO STORE CONTROLLER)
3T046(DA-DG)	10/4	+3 VOLT RETURN (TO STORE CONTROLLER)
3VG08(08-16)T	1/6-12	GROUND
3VG0812(A,B)	1/1	GROUND
3VG0818T	1/1	GROUND
3VG0819T	2/17	GROUND
3VG0821C	1/2	GROUND
3VG0822B	6/1	GROUND
3VG0822T	6/2	GROUND
3VG0824B	1/3	GROUND
3VG0824T	1/3	GROUND
3V046(A-D)	1/1	+3 VOLT (POWER TO STORE CONTROLLER)
3V046(AA-AG,AS)	10/1	+3 VOLT (POWER TO STORE CONTROLLER)
3V046(BA-BG,BS)	10/2	+3 VOLT (POWER TO STORE CONTROLLER)
3V046(CA-CG,CS)	10/3	+3 VOLT (POWER TO STORE CONTROLLER)
3V046(DA-DG,DS)	10/4	+3 VOLT (POWER TO STORE CONTROLLER)
5T	9/2	+5V RETURN (TO MEMORY)
5T039A	9/3	+5V RETURN (TO TERMINATIONS)
5V	7/1	+5V (MEMORY POWER)
5V039A	9/4	+5V (TO TERMINATIONS)

MAIN STORE CONTROLLER AND MEMORY

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APPARATUS INDEX

CIRCUIT PACKS

EQUIP LOC	APP NO.	FIGURE SH NO.
03-02	2	C2
03-04	2	C2
03-05	2	C2
03-06	2	C2
03-07	2	C2
03-08	2	C2
03-09	2	C2
03-10	2	C2
03-11	2	C2
03-12	3	C2
03-13	3	C2
03-14	3	C2
03-16	3	C2
03-17	2	C2
03-18	4	C3
03-19	5	C3
03-20	5	C3
03-21	5	C3
03-22	5	C3
03-23	4	C3
03-24	4	C3
03-25	4	C3
03-26	4	C3
03-27	4	C3
03-28	4	C3
03-29	4	C3
03-31	4	C3
03-33	4	C3
08-01	1	C1
08-02	1	C1
08-03	1	C1
08-04	1	C1
08-05	1	C1
08-06	1	C1
08-07	1	C1
08-08	1	C1
08-09	1	C1
08-10	6	C3
08-11	6	C3
08-12	6	C3
08-13	6	C3
08-14	6	C3
08-16	1	C1
08-17	1	C1
08-18	1	C1
08-19	1	C1
08-20	1	C1
08-21	1	C1
08-22	1	C1
08-23	1	C1
08-24	1	C1
08-25	1	C1
[illegible]	1	C1
[illegible]	1	C1

[illegible]

DESIG	APP NO.	FIGURE SH NO.
3VREGA.B	1	C1
3VREGC.D	1	C1
[illegible]	2	C2
[illegible]	4	C3
[illegible]	1	[illegible]
[illegible]	[illegible]	C2
[illegible]	4	C3

CIRCUIT PACKS(CONT)

DESIG	APP NO.	FIG. SH NO.
MP008.00	2	C2
MP009.01	2	C2
MP010.02	2	C2
MP011.03	2	C2
MP012.04	2	C2
MP013.05	2	C2
MP014.06	2	C2
MP015.07	2	C2
MP020.16	3	C2
MP021.17	3	C.
MP022.18	3	C2
MP023.19	3	C2
MP108.00	4	C3
MP109.01	4	C3
MP110.02	4	C3
MP111.03	4	C3
MP112.04	4	C3
MP113.05	4	C3
MP114.06	4	C3
MP115.07	4	C3
MP120.16	5	C3
MP121.17	5	C3
MP122.18	5	C3
MP123.19	5	C3
TS1	1	C1

BTR ASSY

DESIG	LOCATION FS/SYM	APPFIG	EQPT
BTR0	8/1	1	07-27T
BTR1	8/2	1	07-27B
BTR2	8/3	1	07-20T
BTR3	8/4	1	07-20B
BTR4	8/5	1	07-13T
BTR5	8/6	1	07-13B
BTR6	8/7	1	07-09T
BTR7	8/8	1	07-09B
BTR8	8/9	1	07-05T
BTR9	8/10	1	07-05B
BTR10	8/11	1	01-34T
BTR11	8/12	1	01-34B
BTR12	8/13	1	01-32B
BTR13	8/14	1	01-29B
BTR14	8/15	1	01-26B
BTR15	8/16	1	01-23B
BTR16	8/17	1	01-21B
BTR17	8/18	1	01-16B
BTR18	8/19	1	01-14B
BTR19	8/20	1	01-11B
BTR20	8/21	1	01-08B
BTR21	8/22	1	01-05B
BTR22	8/23	1	01-03B
BTR23	8/24	1	01-03T

CIRCUIT PACK-CP

DESIG	LOCATION FS/SYM	APPFIG	EQPT
2BDPARBD	1/19	6	08-14
2BPARCHK	5/6	6	08-14
2BPARCNV	2/18	6	08-14
2BPISCDA	6/22	6	08-14
3VREGA.B	10/5	1	08-35
3VREGC.D	10/6	1	08-40
A19.MPH	2/15	1	08-17
ABIT17	2/14	1	08-23
ABIT18	2/16	1	08-18
A.D08.00	2/13	1	08-02
A.D09.01	2/12	1	08-03
A.D10.02	2/11	1	08-04
A.D11.03	2/10	1	08-05
A.D12.04	2/9	1	08-06
A.D13.05	2/8	1	08-07
A.D14.06	2/7	1	08-08
A.D15.07	2/6	1	08-09
ADPH.PDR	2/17	1	08-19
A.DPH.PL	2/1	1	08-16
ALARMS	10/8	1	08-25
BSC08.00	1/18	1	08-02
BSC09.01	1/17	1	08-03
BSC10.02	1/16	1	08-04
BSC11.03	1/15	1	08-05
BSC12.04	1/14	1	08-06
BSC13.05	1/13	1	08-07
BSC14.06	1/12	1	08-08
BSC15.07	1/11	1	08-09
BSC20.16	1/10	6	08-10
BSC21.17	1/9	6	08-11
BSC22.18	1/8	6	08-12
BSC23.19	1/7	6	08-13
BSCPH.PL	1/6	1	08-16
[illegible]	[illegible]	1	08-23
[illegible]	1/5	1	08-17
[illegible]	5/2	1	08-17
[illegible]	1/1	1	08-15

CIRCUIT PACK-CP (CONT)

DESIG	LOCATION FS/SYM	APPFIG	EQPT
CREG.DEC	1/2	1	08-21
CTLSGDLY	1/4	1	08-25
CTLSGRPT	1/3	1	08-24
CTSSDA	6/4	1	08-18
D2B20.16	2/5	6	08-10
D2B21.17	2/4	6	08-11
D2B22.18	2/3	6	08-12
D2B23.19	2/2	6	08-13
DGA08.00	6/20	1	08-02
DGA09.01	6/19	1	08-03
DGA10.02	6/18	1	08-04
DGA11.03	6/17	1	08-05
DGA12.04	6/16	1	08-06
DGA13.05	6/15	1	08-07
DCA14.06	6/14	1	08-08
DGA15.07	6/13	1	08-09
DGA20.16	6/12	6	08-10
DGA21.17	6/11	6	08-11
DGA22.18	6/10	6	08-12
DGA23.19	6/9	6	08-13
DGAPH.PL	6/8	1	08-16
DPREFCNT	3/2	1	08-17
EPR08.00	5/20	1	08-02
ERR09.01	5/19	1	08-03
ERR10.02	5/18	1	08-04
EPR11.03	5/17	1	08-05
ERR12.04	5/16	1	08-06
ERR13.05	5/15	1	08-07
ERR14.06	5/14	1	08-08
EPR15.07	5/13	1	08-09
FOB0	7/1	2	03-02
FOB1	7/15	4	03-33
IODECHK	5/7	1	08-20
IDINTFCE	6/1	1	08-23
IOORDDEC	6/3	1	08-20
IDSR.BUF	6/2	1	08-22
ISEQSHUT	10/9	1	08-25
ISO20.16	5/12	6	08-10
ISO21.17	5/11	6	08-11
ISO22.18	5/10	6	08-12
ISO23.19	5/9	6	08-13
ISOPH.PL	5/8	1	08-16
ISO.WRMD	6/21	1	08-24
MP0PH.PL	7/2	2	03-17
MP1PH.PL	7/16	4	03-18
MP008.00	7/14	2	03-04
MP009.01	7/13	2	03-05
MP010.02	7/12	2	03-06
MP011.03	7/11	2	03-07
MP012.04	7/10	2	03-08
MP013.05	7/9	2	03-09
MP014.06	7/8	2	03-10
MP015.07	7/7	2	03-11
MP020.16	7/6	3	03-12
MP021.17	7/5	3	03-13
MP022.18	7/4	3	03-14
MP023.19	7/3	3	03-16
MP108.00	7/28	4	03-31
MP109.01	[illegible]	4	03-29
MP110.02	7/26	4	03-28
MP111.03	7/25	4	03-27
MP112.04	7/24	4	03-26
MP113.05	7/23	4	03-25
MP114.06	7/22	4	03-24

CIRCUIT PACK-CP (CONT)

DESIG	LOCATION FS/SYM	APPFIG	EQPT
MP115.07	7/21	4	03-23
MP120.16	7/20	5	03-22
MP121.17	7/19	5	03-21
MP122.18	7/18	5	03-20
MP123.19	7/17	5	03-19
NCR20.16	3/17	6	08-10
NCR21.17	3/16	6	08-11
NCR22.18	3/15	6	08-12
NCR23.19	3/14	6	08-13
NCW20.16	4/15	6	08-10
NCW21.17	4/14	6	08-11
NCW22.18	4/13	6	08-12
NCW23.19	4/12	6	08-13
NMPFADCK	5/5	1	08-19
REF08.00	3/13	1	08-02
REF09.01	3/12	1	08-03
REF10.02	3/11	1	08-04
REF11.03	3/10	1	08-05
REF12.04	3/9	1	08-06
REF13.05	3/8	1	08-07
REF14.06	3/7	1	08-08
REF15.07	3/6	1	08-09
REF16.PL	3/5	1	08-16
REFA17	3/4	1	08-23
REFCNTRL	3/3	1	08-18
REFCOUNT	3/1	1	08-19
RFMCCDA	6/6	1	08-19
SDOP.ERA	5/4	1	08-24
SELSFTCK	5/1	1	08-18
TARSELDA	6/7	1	08-17
T.BDDA	6/5	1	08-21
TMWPDPHC	5/3	1	08-21
TRM-VMON	10/7	1	08-25
WPMODDEC	4/1	1	08-21
WPRSEG0	4/11	1	08-02
WPRSEG1	4/10	1	08-03
WPRSEG2	4/9	1	08-04
WPRSEG3	4/8	1	08-05
WPRSEG4	4/7	1	08-06
WPRSEG5	4/6	1	08-07
WPRSEG6	4/5	1	08-08
WPRSEG7	4/4	1	08-09
WPRSPAR	4/3	1	08-16
WPSEGDEC	4/2	1	08-17

CONNECTOR

DESIG	LOCATION FS/SYM	APPFIG	EQPT
TS1	10/10	1	08-01

POWER MODULE

DESIG	LOCATION FS/SYM	APPFIG	EQPT
+5VPM	9/2	1	03-38
PMC829	10/1	1	08-29
PMC834	10/2	1	08-34
PMC839	10/3	1	08-39
PMC844	10/4	1	08-44
[illegible]	9/1	1	03-44

MAIN STORE CONTROLLER AND MEMORY

BELL TELEPHONE LABORATORIES
INCORPORATED

DWG SIZE
C2

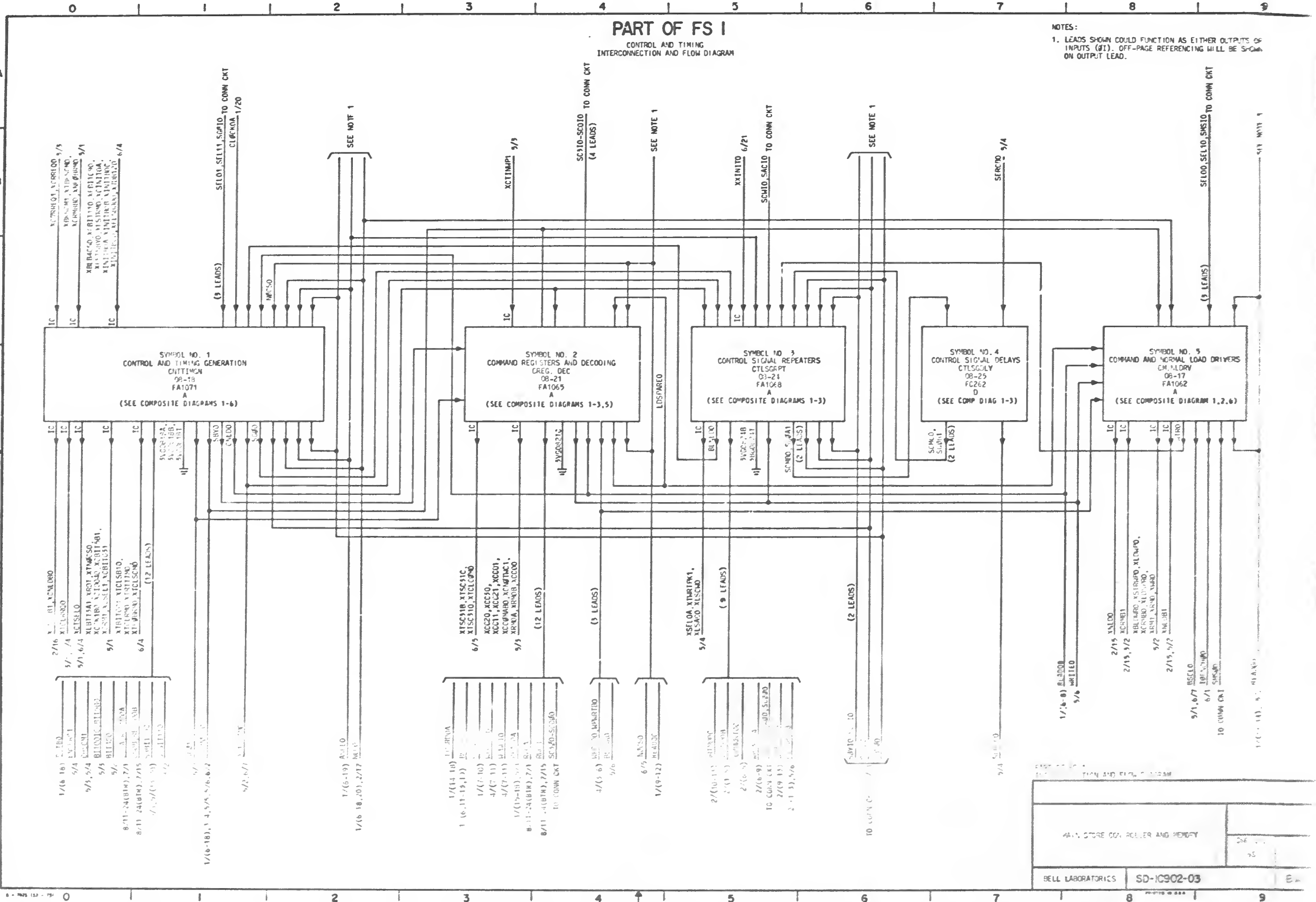
SD-1C902-[illegible]

PART OF FS I

CONTROL AND TIMING
INTERCONNECTION AND FLOW DIAGRAM

NOTES:

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OF INPUTS (ØI). OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



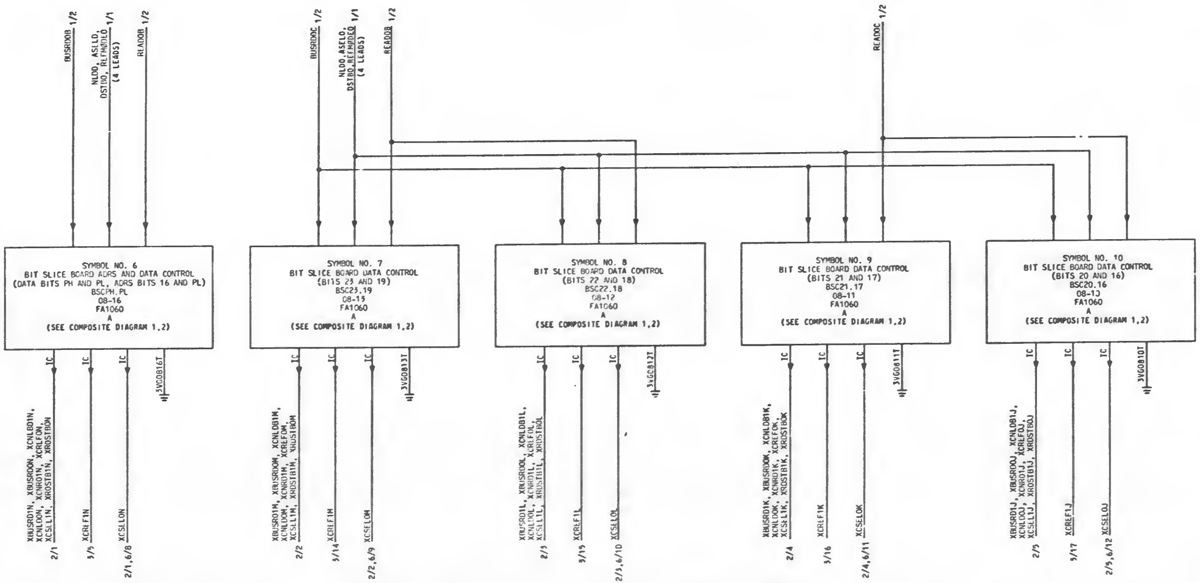
PART OF FS I
CONTROL AND TIMING INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES | SD-1C902-03

PART OF FS I

CONTROL AND TIMING
INTERCONNECTION AND FLOW DIAGRAM


BUSRDOB 1/2
NLDD, ASELD, DSTBO, REFMØDEO 1/1 (4 LEADS)
READOB 1/2
BUSRDOC 1/2
NLDO, ASELO, DSTBO, REFMØDEO 1/1 (4 LEADS)
READOB 1/2
READOC 1/2
SYMBOL NO. 6
BIT SLICE BOARD ADRS AND DATA CONTROL
(DATA BITS PH AND PL, ADRS BITS 16 AND PL)
BSCPH.PL
08-16
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 7
BIT SLICE BOARD DATA CONTROL
(BITS 23 AND 19)
BSC23.19
08-13
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 8
BIT SLICE BOARD DATA CONTROL
(BITS 22 AND 18)
BSC22.18
08-12
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 9
BIT SLICE BOARD DATA CONTROL
(BITS 21 AND 17)
BSC21.17
08-11
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 10
BIT SLICE BOARD DATA CONTROL
(BITS 20 AND 16)
BSC20.16
08-10
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
IC
3VGOB16T
XBUSRD1N, XBUSRDON, XCNLBD1N, XCNLDON, XCNRO1N, XCRLFON, XCSELL1N, XROSTB1N, XROSTBON
2/1
XCRLF1N
3/5
XCSLLON
2/1,6/8
3VGOB13T
XBUSRO1M, XBUSRDOM, XCNLDB1M, XCNLDOM, XCNRD1M, XCREFOM, XCSLL1M, XRDSTB1M, XRDSTBOM
2/2
XCRLF1M
3/14
XCSELOM
2/2,6/9
3VGOB12T
XBUSRD1L, XBUSROOL, XCNLDB1L, XCNLDOL, XCNRD1L, XCREFOL, XCSELL1L, XRDSTB1L, XRDSTBOL
2/3
XCREF1L
3/15
XCSELOL
2/3,6/10
3VGOB11T
XBUSRD1K, XBUSRDOK, XCNLDB1K, XCNLDOK, XCNRD1K, XCREFOK, XCSELL1K, XRDSTB1K, XRDSTBOK
2/4
XCREF1K
3/16
XCSELOK
2/4,6/11
3VGOB10T
XBUSRD1J, XBUSRDOJ, XCNLDB1J, XCNLDOJ, XCNRO1J, XCREFOJ, XCSELL1J, XROSTB1J, XRDSTBOJ
2/5
XCREF1J
3/17
XCSELOJ
2/5,6/12

PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE
65

ISSUE
1

BELL LABORATORIES

SD-1C902-03

B1AE

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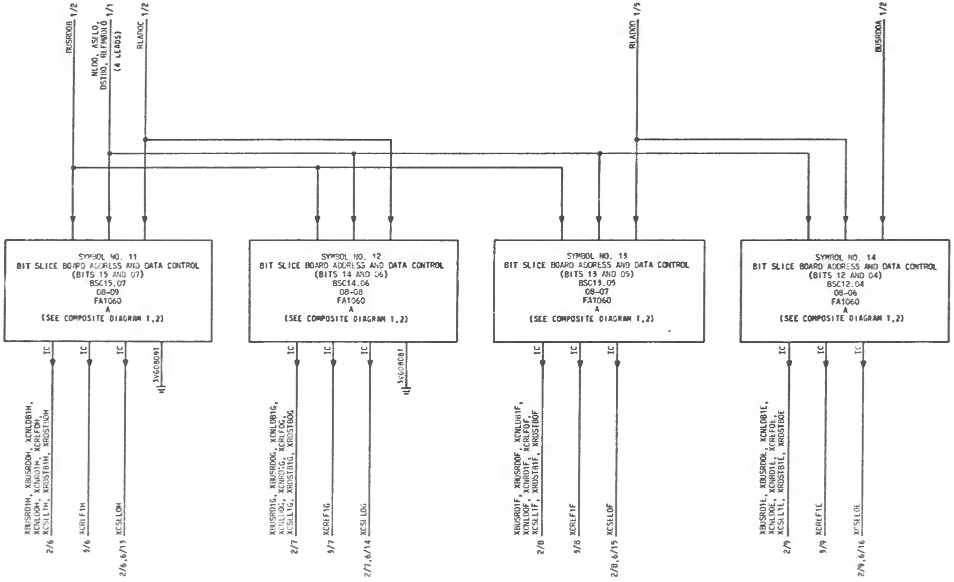
PART OF FS 1

CONTROL AND TIMING
INTERCONNECTION AND FLOW DIAGRAM


BUSRDOB 1/2
NLDO, ASELO, DSTBO, REFMODIO 1/1 (4 LEADS)
RLADOC 1/2
RLADOD 1/5
BUSRDOA 1/2
SYMBOL NO. 11
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 15 AND 07)
BSC15.07
08-09
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 12
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 14 AND 06)
BSC14.06
08-08
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 13
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 13 AND 05)
BSC13.05
08-07
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 14
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 12 AND 04)
BSC12.04
08-06
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
IC
3VG0B09T
3VG0B08T
XBUSRD1H, XBUSRD0H, XCNLDB1H, XCNLD0H, XCNRD1H, XCREF0H, XCSLL1H, XRDSTB1H, XRDSTB0H 2/6
XCREF1H 3/6
XCSEL0H 2/6,6/13
XBUSRD1G, XBUSRD0G, XCNLDB1G, XCNLD0G, XCNRD1G, XCREF0G, XCSLL1G, XRDSTB1G, XRDSTB0G 2/7
XCREF1G 3/7
XCSEL0G 2/7,6/14
XBUSRD1F, XBUSRD0F, XCNLDB1F, XCNLD0F, XCNRD1F, XCREF0F, XCSLL1F, XRDSTB1F, XRDSTB0F 2/8
XCREF1F 3/8
XCSEL0F 2/8,6/15
XBUSRD1E, XBUSRD0E, XCNLDB1E, XCNLD0E, XCNRD1E, XCREF0E, XCSLL1E, XRDSTB1E, XRDSTB0E 2/9
XCREF1E 3/9
XCSEL0E 2/9,6/16

PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

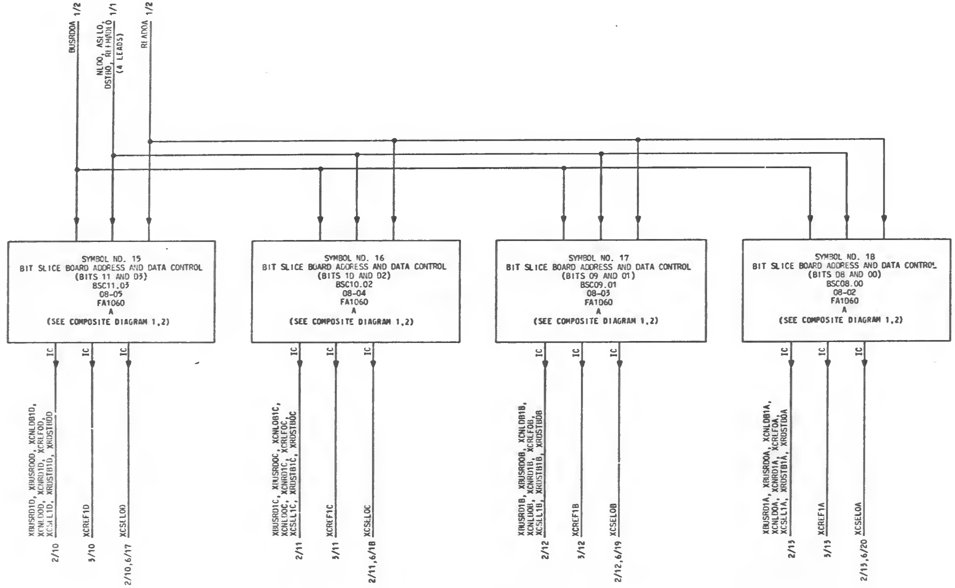
MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES

SD-1C902-03

PART OF FS I

CONTROL AND TIMING
INTERCONNECTION AND FLOW DIAGRAM


BUSRDOA 1/2
NLDO, ASELO, DSTBO, REFMODEO 1/1 (4 LEADS)
READOA 1/2
SYMBOL NO. 15
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 11 AND 03)
BSC11.03
08-05
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 16
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 10 AND 02)
BSC10.02
08-04
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 17
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 09 AND 01)
BSC09.01
08-03
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
SYMBOL NO. 18
BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 08 AND 00)
BSC08.00
08-02
FA1060
A
(SEE COMPOSITE DIAGRAM 1,2)
IC
XBUSRD1D, XBUSRD0D, XCNLDB1D, XCNLD0D, XCNRD1D, XCRLF0D, XCSLL1D, XRDSTB1D, XRDSTB0D 2/10
XCREF1D 3/10
XCSEL0D 2/10,6/17
XBUSRD1C, XBUSRD0C, XCNLDB1C, XCNLD0C, XCNRD1C, XCRLF0C, XCSLL1C, XRDSTB1C, XRDSTB0C 2/11
XCREF1C 3/11
XCSEL0C 2/11,6/18
XBUSRD1B, XBUSRD0B, XCNLDB1B, XCNLD0B, XCNRD1B, XCRLF0B, XCSLL1B, XRDSTB1B, XRDSTB0B 2/12
XCREF1B 3/12
XCSEL0B 2/12,6/19
XBUSRD1A, XBUSRD0A, XCNLDB1A, XCNLD0A, XCNRD1A, XCRLF0A, XCSLL1A, XRDSTB1A, XRDSTB0A 2/13
XCREF1A 3/13
XCSEL0A 2/13,6/20

PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

BELL LABORATORIES

SD-1C902-C3

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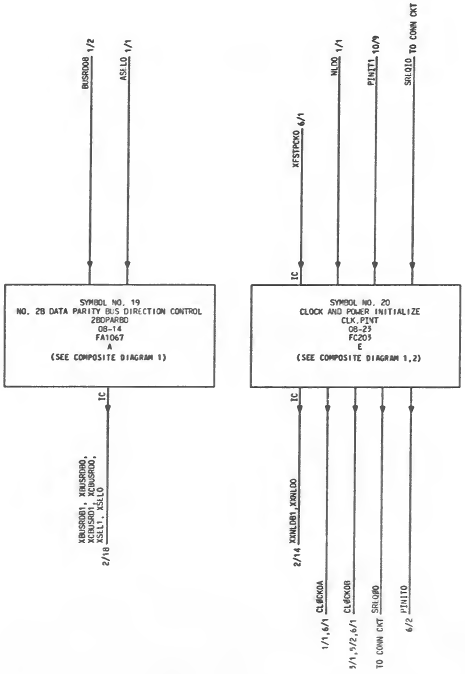
PART OF FS 1

CONTROL AND TIMING
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

4

DWG SIZE eS

ISSUE 1

BELL LABORATORIES

SD-1C902-03

BIAE

E-742112 73

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PART OF FS 1

CONTROL AND TIMING

SYMBOL NO. 1

CONTROL AND TIMING GENERATION

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CNTTIMGN	08-18	FA1071	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		002			GRD002	2A8
		010			I00.120	7H1
		101			GRD101	2A
		102			GRD102	2A7
		105			BIT13G00	4H0
		106			LSHB001	3H4
		110			I0LE0	2H4
		202			GRD202	2A6
		206			BIT02B1	5G9
		301			GRD301	2A5
		305			BIT10G00	4H4
ASEL0	0I	103	1/3,1/6 1/7,1/8 1/9,1/10 1/11,1/12 1/13,1/14 1/15,1/16 1/17,1/18 1/19		SEL0	7H5
BIT00I0	0	314	5/3		BIT00B0	6G8
BIT4B0	0	104	5/2		BIT04B0	5H7
BIT5B0	0	203	5/3		BIT050	5H5
BLNLD0	I	217	1/3		BLNL0I0	3A2
CHSCM1	0	212	5/3,5/4		CHSCM1	4H7
CLOCK0A	I	208	1/20		CKI0	4A0
CNLD0	0	311	1/2,1/3		CNLD0	3H5
CS0A	0	302	7/1 8/11-24(BTR)		CS0A	6H2
CS0B	0	300	7/15 8/11-24(BTR)		CS0B	6H1
DSTB0	0	201	1/6,1/7 1/8,1/9 1/10,1/11 1/12,1/13 1/14,1/15 1/16,1/17 1/18		CS0C	6H1
ENSERC1	0	204	5/4		LBIT13B1	4H9
NLD0	0I	309	1/5,1/6 1/7,1/8 1/9,1/10 1/11,1/12 1/13,1/14 1/15,1/16 1/17,1/18 1/20,2/17		NL00	3H2
N0CS0	I	008	1/2		N0CSI0	6A0
READ0B	I	019	1/2		R0I0	5A7
REFM0DA	0	100	7/1 8/11-24(BTR)		F0BRM0A	3H7
REFM0DB	0	001	7/15 8/11-24(BTR)		F0BRM0B	3H8
REFM0DE0	0	003	1/2,1/5 1/6,1/7 1/8,1/9 1/10,1/11 1/12,1/13 1/14,1/15 1/16,1/17 1/18,3/4 5/5,5/6 6/2		REFM0D0	3H6
SBY00	0I	115	1/3		SBY0	6H3
SBY0	0	209	1/2		SBYB0	6H4
SCM00	0T	214	1/3		SCM0	4H7
SEL01	I	109	TO CONN CKT		SEL0I1	7A3

SYMBOL NO. 1 (CONT)

CONTROL AND TIMING GENERATION

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CNTTIMGN	08-18	FA1071	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
SEL11	I	210	TO CONN CKT		SEL111	7A3
SG0I0	I	116	TO CONN CKT		SG0I0	2A1
SG00	0	014	1/3		GSG00	2H2
SG01	0	316	1/2,5/2		SG01B	2H1
SHBIT090	0	012	5/8,5/13 5/14,5/15 5/16,5/17 5/18,5/19 5/20		BIT09B0	4H6
SHBIT110	0	006	3/2		BIT11G00	4H3
XBKSCM1	I	IC	5/1		BKSCM1	4A8
XBLB4CS0	I	IC	6/4		BLB4CS0	5A6
XCBIT031	0	IC	5/1		BIT031	5G8
XCBIT5B1	0	IC	5/1		BIT05B1	5H4
XCCK1B0	0	IC	5/1		CK1B0	5G9
XCI0040	0	IC	5/1		ID0.40	7D6
XCINIT0A	I	IC	6/4		INIT0A	6D0
XCNL0B0	0	IC	2/16		NL0B0	3H3
XCNL0B1	0	IC	2/16		NL0B1	3H3
XCRMBB0	I	IC	5/1		RMBB0	7A2
XCRM1	0	IC	5/1		RM1	3H5
XCRRE00	I	IC	3/3		RRE00	3D0
XCSEL1	0	IC	5/1		SEL1	7D5
XCTSEL0	CT I	IC	6/4 5/1		TSEL0	7A3
XC7RRE01	I	IC	3/3		7RRE01	2A4
XFBIT090	I	IC	6/4		FBIT090	4A6
XFBIT110	I	IC	6/4		FBIT110	4A3
XFLSB000	I	IC	6/4		FLSHB000	6C0
XFSTRM0	I	IC	6/4		FSTRM0	2A4
XFSTSBY0	I	IC	6/4		FSTSBY0	6C0
XINITB0A	I	IC	6/4		INITB0A	5A6
XINITB0B	I	IC	6/4		INITB0B	5A2
XINITB0C	I	IC	6/4		INITB0C	4A4
XINITB0D	I	IC	6/4		INITB0D	4A0
XLBT13A1	0	IC	5/1		LBIT13A1	4G9
XNF0BRM0	I	IC	5/1		N0F0BRM0	2A4
XRDI	0	IC	5/1		RD1	5G7
XTBIT001	0T	IC	6/4		TBIT001	6E9
XTBKSCM0	I	IC	5/1		TBKSCM0	4A6
XTCLRM0	0T	IC	6/4		TCLRM0	4A2
XTCLRR00	0T I	IC	6/4 3/3		TCLRR00	5H3
XTCLSB10	0T	IC	6/4		TCLSB10	6H5
XTCLSCM0	0T	IC	6/4		TCLSCM0	2A3
XTDB120	I	IC	6/4		TDB120	4A1
XTF0BRM0	0T	IC	6/4		TF0BRM0	2A3
XTN0CS0	0T	IC	5/1		TN0CS0	6F0
XTRTTIM0	0T	IC	6/4		TRTTIM0	7A2
0.10TIM0	0T I	111	5/2,6/7 1/3,5/2		0.10TIM0	7H2
3TC46A	GRD	0GD		202	GRD	
	GRD	2G0		202	GRD	
	GRD	200		202	GRD	
	GRD	319		202	GRD	
3VG0818A	GRD	005			GRD005	2A7
	GRD	2GD			GRD	
	GRD	205			GRD205	2A6
3VG0818B	GRD	200			GRD	
	GRD	303			GRD303	2A5
3VG0818T	GRD	011			GRD011	2A7

SYMBOL NO. 1 (CONT)

CONTROL AND TIMING GENERATION

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CNTTIMGN	08-18	FA1071	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3VG0818T	GRD	114			GRD114	2A6
	GRD	211			GRD211	2A6
	GRD	319			GRD	
3V046A	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 2

COMMAND REGISTERS AND DECODING

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CREG.DEC	08-21	FA1065	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
BL0WR0	0	305	1/5,5/6		C0M50	3H5
BUSR00A	0	310	1/14,1/15 1/16,1/17 1/18		SC30A	2H2
BUSRD0B	0	117	1/6,1/11 1/12,1/13 1/19		SC30B	2H2
BUSRD0C	0	019	1/7,1/8 1/9,1/10		SC30C	2H2
CNLD0	I	115	1/1		NL0I0	2A3
LDSPARE0	0I	209	1/5		C0MA0	3H4
N0CS0	0I	307	6/5 1/1		TN0CS0	4H4
	0T	IC307	1/2		TN0CS0	4A4
READ0A	0	011	1/15,1/16 1/17,1/18 5/4		C0MCB0B	2H3
READ0B	0	016	1/1,1/5 1/6,1/7 1/8		C0MCB0A	2H3
READ0C	0I	012	1/9,1/10 1/11,1/12		C0MC0	2H4
REFM0DE0	I	215	1/1		RMI0	4A0
RW0A	0	100	7/1 8/11-24(BTR)		RW0A	4H4
RW0B	0	300	7/15 8/11-24(BTR)		RW0B	4H3
SBY0	I	211	1/1		SBYI0	3A1
SC0I0	I	219	TO CONN CKT		SC0I0	3A5
SC000	0	105	TO CONN CKT		SC00	3H5
SC1I0	I	103	TO CONN CKT		SC1I0	3A3
SC100	0	213	TO CONN CKT		SC10	3H3
SC2I0	I	216	TO CONN CKT		SC2I0	2A6
SC200	0	312	TO CONN CKT		SC20	2H6
SC3I0	I	218	TO CONN CKT		SC3I0	2A1
SC300	0	205	TO CONN CKT		SC300	2H1
SG01	I	314	1/1		G0I1	3A1
WPRRDB0	0	207	4/7,4/8 4/9,4/10 4/11		C0M90B	3H5
WPRR00	0	007	1/5,4/3 4/4,4/5 4/6		C0M90A	3H4
WPWRTB0	0	306	1/5,4/3 4/4,4/5 4/6		C0M60B	2H5

SYMBOL NO. 2 (CONT)

COMMAND REGISTERS AND DECODING

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CREG.DEC	08-21	FA1065	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
WPWRT0	0	107	4/7,4/8 4/9,4/10 4/11		C0M60A	2H5
WRITE0	0T I	014	1/3 1/5,5/6		C0M30	3H4
XCC0MAB0	0	IC	5/3		C0MAB0	3H3
XCC00	0	IC	5/3		C00	3D7
XCC01	0	IC	5/3		C01	3E7
XCC11	0	IC	5/3		C11	3E7
XCC20	0	IC	5/3		C20	2H4
XCC21	0	IC	5/3		C21	2H4
XCC30	0	IC	5/3		C30	2H4
XCN0TWC1	0	IC	5/3		N0TWC1	4H3
XCTINWP1	I	IC	5/3		TINWP1	4A2
XRM0A	0	IC	5/3		RM0A	4H0
XRM0B	0	IC	5/3		RM0B	4H0
XTCLC0M0	0T	IC	6/5		TCLC0M0	3C0
XTSC31B	0T	IC	6/5		TSC31B	2D0
XTSC31C	0T	IC	6/5		TSC31C	2D0
XTSC310	0T	IC	6/5		TSC310	2E0
3T0460	GRD	0GD		202	GRD	
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	319		202	GRD	
3VG0821C	GRD	104				
	GRD	2GD			GRD	
3V046D	PWR	000		202	+3	
	PWR	119		202	+3	

PART OF FS 1
SYMBOL(S) 1 2

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE C2	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B1CA

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PART OF FS 1

CONTROL AND TIMING

SYMBOL NO. 3

CONTROL SIGNAL REPEATERS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CTLSGRPT	08-24	FA1068	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		001			TIMACT0A	2H6
		300			TIMACT0B	2H5
ASEL0	I	110	1/1		SELI0	3A
BLNLD0	0	013	1/1		SCW0B	2H3
CNLD0	I	210	1/1		NLDI0	2A4
COMWRT0A	0	312	2/9,2/10 2/11,2/12 2/13		LSCW0A	2H4
COMWRT0B	0	311	2/1,2/2 2/3,5/6		LSCW0B	2H5
COMWRT0C	0	112	2/4,2/5 2/6,2/7 2/8		LSCW0C	2H5
SACI0	I	214	TO CONN CKT		SACI0	2A2
SAC00	0	016	TO CONN CKT		SAC0	2H2
SBYI0	I0	115	6/21 TO CONN CKT		TSBYI0	3A1
	0T	IC115	1/3		TSBYI0	3A1
SBY00	0I	106	1/1 6/21 TO CONN CKT		SBYB0	3H1
	0	IC106	1/3		SBYB0	3H1
SCMB0	0	313	1/4		SCM0	3H3
SCMC0	I	306	1/4		SCMI0	3A3
SCMI0	I0	111	6/21 TO CONN CKT		TSCMI0	3A3
	0T	IC111	1/3		TSCMI0	3A4
SCM00	0I	202	1/1 6/21 TO CONN CKT		GSCM0	3H3
	0T	IC202	1/3		GSCM0	3H3
SCWI0	I	207	TO CONN CKT		SCWI0	2A2
SCW00	0	014	TO CONN CKT		SCW0A	2H2
SG0A1	0	316	1/4		SG01	3H5
SG0B1	I	304	1/4		SG0II	3A6
SG000	0	010	TO CONN CKT		SG00	3H6
SG00	I	018	1/1		SG0I0	3A5
WRITE0	0T	301	1/2		LSCW0D	2H4
WRTMD0A	0	209	2/6,2/7 2/8,2/9		WRTMD0A	2H0
WRTMD0B	0	009	2/1,2/2 2/3,2/4 2/5		WRTMD0B	2H0
WRTMD0C	0	113	2/10,2/11 2/12,2/13		WRTMD0C	2H1
WTR0	I	011	1/5		WRTI0	2A0
XLSAC0	0	IC	5/4		LSAC0	2H5
XLSCW0	0	IC	5/4		LSCW0	2H4
XSEL0A	0	IC	5/4		SEL0	3H1
XTWRTPK1	0T I	IC	6/21 5/4		TWRTPCK1	2A0
XXINIT0	I	IC	6/21		INIT0	2A2
0.10TIM0	I	305	1/1		TIMACTI0	2A5
3T046C	GRD	0GD		202	GRD	
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	319		202	GRD	
3VG0824B	GRD	005			GRD005	2A8
	GRD	102			GRD102	2A8
	GRD	107			GRD107	2A8
	GRD	200			GRD	
	GRD	205			GRD205	2A7
	GRD	302			GRD302	2A7
	GRD	307			GRD307	2A7

SYMBOL NO. 3 (CONT)

CONTROL SIGNAL REPEATERS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CTLSGRP1	08-24	FA1068	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3VG0824T	GRD	012			GRD012	2A8
3VG0824T	GRD	017			GRD017	2A8
	GRD	114			GRD114	2A7
	GRD	212			GRD212	2A7
	GRD	217			GRD217	2A7
	GRD	314			GRD314	2A6
	GRD	319			GRD	
3V046C	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 4

CONTROL SIGNAL DELAYS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CTLSGDLY	08-25	FC262	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
SCMB0	I	013	1/3		SCMI0	
SCMC0	0	006	1/3		SCM0	
SERCB0	I	010	5/4		SERCI0	
SERCC0	0	008	5/4		SERC0	
SG0A1	I	016	1/3		SG0I1	
SG0B1	0	004	1/3		SG01	

SYMBOL NO. 5

COMMAND AND NORMAL LOAD DRIVERS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CM.NLDRV	08-17	FA1062	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
BLDWR0	I	114	1/2		BLDWRI0	2A7
BSEL0	0T I	IC111	6/7 5/1		TSEL0	2A4
	0T	111	1/5		TSEL0	2H4
ICENCH00	0	212	6/1		SMS0B	2H1
LDSPARE0	I	215	1/2		LDSPRI0	2A5
NLD0	I	301	1/1		NLDI0	2A3
READ0B	I	305	1/2		RDI0	2A6
REA[illegible]	0	IC012	1/13,1/14 5/2		RD0	2G6
	0I	012	1/5		RD0	2H6
REFM0DE0	I	310	1/1		RMI0	2A2
SEL00	I	101	TO CONN CKT		SEL0I0	2A4
SEL10	I	011	TO CONN CKT		SEL1I0	2A4

SYMBOL NO. 5 (CONT)

COMMAND AND NORMAL LOAD DRIVERS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CM.NLDRV	08-17	FA1062	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
SMSI0	I	312	TO CONN CKT		SMSI0	2A1
SMS00	0	112	TO CONN CKT		SMS0A	2H1
WPRRD0	I	115	1/2		LDWPI0	2A6
WPWRTB0	I	314	1/2		STRWPI0	2A6
WRITE0	I	015	1/2		WRI0	2A7
WTR0	0	018	1/3		TWRTMD0	2G8
XBLDWR0	0	IC	5/2		BLDWR0	2G7
XCRMB0	0	IC	5/2		RMB0	2G3
XCRMB1	0	IC	2/15,3/2		RMB1	2G3
XLDSPR0	0	IC	5/2		LDSPR0	2G5
XLDWP0	0	IC	5/2		LDWP0	2G6
XNLDB1	0	IC	2/15,5/2		NLDB1	2G4
XNLD0	0	IC	2/15		NLD0	2G3
XRM0	0	IC	5/2		RM0	2G2
XRM1	0	IC	5/2		RM1	2G2
XSTRWP0	0	IC	5/2		STRWP0	2G6
XWR0	0	IC	5/2		WR0	2G7
3T046D	GRD	0GD		202	GRD	
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	319		202	GRD	
3V046D	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 6

BIT SLICE BOARD ADRS AND DATA CONTROL
(DATA BITS PH AND PL,ADRS BITS 16 AND PL)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSCPH.PL	08-16	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0B	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		CSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0B	I	309	1/2		NRDI0	2A6
REFM0DE0	I	206	1/1		REFI0	2A5
XBUSRD0N	0	IC	2/1		BUSRD0	2G3
XBUSRD1N	0	IC	2/1		BUSRD1	2G3
XCNLDB1N	0	IC	2/1		NLDB1	2G0
XCNLD0N	0	IC	2/1		NLD0	2G0
XCNRD1N	0	IC	2/1		NRD1	2G6
XCREF0N	0	IC	2/1		REF0	2G5
XCREF1N	0	IC	3/5		REF1	2G4
XCSEL0N	0	IC	2/1,6/8		SEL0	2G2
XCSEL1N	0	IC	2/1		SEL1	2G1
XRDSTB0N	0	IC	2/1		RDDSTB0	2G5
XRDSTB1N	0	IC	2/1		RDDSTB1	2G5
3T046C	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8

SYMBOL NO. 6 (CONT)

BIT SLICE BOARD ADRS AND DATA CONTROL
(DATA BITS PH AND PL,ADRS BITS 16 AND PL)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSCPH.PL	08-16	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3T046C	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0816T	GRD	319	3/5		GRD	
3V046C	PWR	000		202	+3	
	PWR	119		202	+3	

PART OF FS 1
SYMBOL(S) 3 4 5 6

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE
C2
BELL LABORATORIES
SD-1C902-03

PRINTED IN U. S. A.

PART OF FS 1

CONTROL AND TIMING

SYMBOL NO. 7

BIT SLICE BOARD DATA CONTROL
(BITS 23 AND 19)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC23.19	08-13	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0C	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0B	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0M	O	IC	2/2		BUSRD0	2G3
XBUSRD1M	D	IC	2/2		BUSRD1	2G3
XCNLDB1M	O	IC	2/2		NLDB1	2G0
XCNLD0M	D	IC	2/2		NLD0	2G0
XCNRD1M	O	IC	2/2		NRD1	2G6
XCREF0M	O	IC	2/2		REF0	2G5
XCREF1M	O	IC	3/14		REF1	2G4
XCSEL0M	D	IC	2/2,6/9		SEL0	2G2
XCSEL1M	O	IC	2/2		SEL1	2G1
XRDSTB0M	O	IC	2/2		RDDSTB0	2G5
XRDSTB1M	O	IC	2/2		RDDSTB1	2G5
3T046B	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0813T	GRD	319	3/14		GRD	
3V046B	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 8

BIT SLICE BOARD DATA CONTROL
(BITS 22 AND 18)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC22.18	08-12	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0C	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0B	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0L	O	IC	2/3		BUSRD0	2G3

SYMBOL NO. 8 (CONT)

BIT SLICE BOARD DATA CONTROL
(BITS 22 AND 18)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC22.18	08-12	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XBUSRD1L	O	IC	2/3		BUSRD1	2G3
XCNLDB1L	O	IC	2/3		NLDB1	2G0
XCNLD0L	O	IC	2/3		NLD0	2G0
XCNRD1L	O	IC	2/3		NRD1	2G6
XCREF0L	O	IC	2/3		REF0	2G5
XCREF1L	O	IC	3/15		REF1	2G4
XCSEL0L	O	IC	2/3,6/10		SEL0	2G2
XCSEL1L	O	IC	2/3		SEL1	2G1
XRDSTB0L	O	IC	2/3		RDDSTB0	2G5
XRDSTB1L	O	IC	2/3		RDDSTB1	2G5
3T046D	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0812T	GRD	319	3/15		GRD	
3V046D	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 9

BIT SLICE BOARD DATA CONTROL
(BITS 21 AND 17)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC21.17	08-11	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0C	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0C	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0K	O	IC	2/4		BUSRD0	2G3
XBUSRD1K	D	IC	2/4		BUSRD1	2G3
XCNLDB1K	D	IC	2/4		NLDB1	2G0
XCNLD0K	D	IC	2/4		NLD0	2G0
XCNRD1K	D	IC	2/4		NRD1	2G6
XCREF0K	D	IC	2/4		REF0	2G5
XCREF1K	O	IC	3/16		REF1	2G4
XCSEL0K	D	IC	2/4,6/11		SEL0	2G2
XCSEL1K	D	IC	2/4		SEL1	2G1
XRDSTB0K	D	IC	2/4		RDDSTB0	2G5

SYMBOL NO. 9 (CONT)

BIT SLICE BOARD DATA CONTROL
(BITS 21 AND 17)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC21.17	08-11	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XRDSTB1K	O	IC	2/4		RDDSTB1	2G5
3T046C	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0811T	GRD	319	3/16		GRD	
3V046C	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 10

BIT SLICE BOARD DATA CONTROL
(BITS 20 AND 16)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC20.16	08-10	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0C	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0C	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0J	O	IC	2/5		BUSRD0	2G3
XBUSRD1J	D	IC	2/5		BUSRD1	2G3
XCNLDB1J	O	IC	2/5		NLDB1	2G0
XCNLD0J	O	IC	2/5		NLD0	2G0
XCNRD1J	O	IC	2/5		NRD1	2G6
XCREF0J	O	IC	2/5		REF0	2G5
XCREF1J	O	IC	3/17		REF1	2G4
XCSEL0J	O	IC	2/5,6/12		SEL0	2G2
XCSEL1J	O	IC	2/5		SEL1	2G1
XRDSTB0J	O	IC	2/5		RDDSTB0	2G5
XRDSTB1J	D	IC	2/5		RDDSTB1	2G5
3T046B	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	

SYMBOL NO. 10 (CONT)

BIT SLICE BOARD DATA CONTROL
(BITS 20 AND 16)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC20.16	08-10	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3T046B	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0810T	GRD	319	3/17		GRD	
3V046B	PWR	000		202	+3	
	PWR	119		202	+3	

PART OF FS 1
SYMBOL(S) 7 8 9 10

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C2
ISSU 1
BELL LABORATORIES
SD-1C902-03

PRINTED IN U. S. A.

PART OF FS 1

CONTROL AND TIMING

SYMBOL NO. 11

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 15 AND 07)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC15.07	08-09	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A
BUSRD0B	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0C	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0H	0	IC	2/6		BUSRD0	2G3
XBUSRD1H	0	IC	2/6		BUSRD1	2G3
XCNLDB1H	0	IC	2/6		NLDB1	2G0
XCNLD0H	0	IC	2/6		NLD0	2G0
XCNRD1H	0	IC	2/6		NRD1	2G6
XCREF0H	0	IC	2/6		REF0	2G5
XCREF1H	0	IC	3/6		REF1	2G4
XCSEL0H	0	IC	2/6,6/13		SEL0	2G2
XCSEL1H	0	IC	2/6		SEL1	2G1
XRDSTB0H	0	IC	2/6		RDDSTB0	2G5
XRDSTB1H	0	IC	2/6		RDDSTB1	2G5
3T046A	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0809T	GRD	319	3/6		GRD	
3V046A	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 12

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 14 AND 06)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC14.06	08-08	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0B	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ[illegible]	I	309	1/2		NRDI0	2A6
REFM[illegible]	I	206	1/1		REFI0	2A5
XBUSRD[illegible]	0	IC	[illegible]		BUSRD0	2G3

SYMBOL NO. 12 (CONT)

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 14 AND 06)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC14.06	08-08	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XBUSRD1G	0	IC	2/7		BUSRD1	2G3
XCNLDB1G	0	IC	2/7		NLDB1	2G0
XCNLD0G	0	IC	2/7		NLD0	2G0
XCNRD1G	0	IC	2/7		NRD1	2G6
XCREF0G	0	IC	2/7		REF0	2G5
XCREF1G	0	IC	3/7		REF1	2G4
XCSEL0G	0	IC	2/7,6/14		SEL0	2G2
XCSEL1G	0	IC	2/7		SEL1	2G1
XRDSTB0G	0	IC	2/7		RDDSTB0	2G5
XRDSTB1G	0	IC	2/7		RDDSTB1	2G5
3T046D	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3VG0808T	GRD	319	3/7		GRD	
3V046D	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 13

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 13 AND 05)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC13.05	08-07	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0B	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0D	I	309	1/5		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0F	0	IC	2/8		BUSRD0	2G3
XBUSRD1F	0	IC	2/8		BUSRD1	2G3
XCNLDB1F	0	IC	2/8		NLDB1	2G0
XCNLD0F	0	IC	2/8		NLD0	2G0
XCNRD1F	0	IC	2/8		NRD1	2G6
XCREF0F	0	IC	2/8		REF0	2G5
XCREF1F	0	IC	3/8		REF1	2G4
XCSEL0F	0	IC	2/8,6/15		SEL0	2G2
XCSEL1F	0	IC	2/8		SEL1	2G1
XRDSTB0F	0	IC	2/8		RDDSTB0	2G5

SYMBOL NO. 13 (CONT)

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 13 AND 05)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC13.05	08-07	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XRDSTB1F	0	IC	2/8		RDDSTB1	2G5
3T046C	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3V046C	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 14

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 12 AND 04)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC12.04	08-06	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASEL0	I	211	1/1		SELI0	2A2
BUSRD0A	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0D	I	309	1/5		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0E	0	IC	2/9		BUSRD0	2G3
XBUSRD1E	0	IC	2/9		BUSRD1	2G3
XCNLDB1E	0	IC	2/9		NLDB1	2G0
XCNLD0E	0	IC	2/9		NLD0	2G0
XCNRD1E	0	IC	2/9		NRD1	2G6
XCREF0E	0	IC	2/9		REF0	2G5
XCREF1E	0	IC	3/9		REF1	2G4
XCSEL0E	0	IC	2/9,6/16		SEL0	2G2
XCSEL1E	0	IC	2/9		SEL1	2G1
XRDSTB0E	0	IC	2/9		RDDSTB0	2G5
XRDSTB1E	0	IC	[illegible]		RDDSTB1	2G5
3T046B	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	[illegible]		202	GRD	

SYMBOL NO. 14 (CONT)

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 12 AND 04)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC12.04	08-06	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3T046B	GRD	202		202	[illegible]	
	GRD	205		202	[illegible]	
	GRD	302		202	[illegible]	
	GRD	304		202	[illegible]	
	GRD	319		202	[illegible]	
3V046B	PWR	000		202	+3	
	PWR	119		202	+3	

PART OF FS 1
SYMBOL(S) 11 TO 14

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES [illegible]-03

PART OF FS 1

CONTROL AND TIMING

SYMBOL NO. 15

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 11 AND 03)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC11.03	08-05	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASELO	I	211	1/1		SELI0	2A
BUSRD0A	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0A	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0D	0	IC	2/10		BUSRD0	2G3
XBUSRD1D	0	IC	2/10		BUSRD1	2G3
XCNLDB1D	0	IC	2/10		NLDB1	2G0
XCNLD0D	0	IC	2/10		NLD0	2G0
XCNRD1D	0	IC	2/10		NRD1	2G6
XCREF0D	0	IC	2/10		REF0	2G5
XCREF1D	0	IC	3/10		REF1	2G4
XCSEL0D	0	IC	2/10,6/17		SEL0	2G2
XCSEL1D	0	IC	2/10		SEL1	2G1
XRDSTB0D	0	IC	2/10		RDDSTB0	2G5
XRDSTB1D	0	IC	2/10		RDDSTB1	2G5
3T046A	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3V046A	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 16

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 10 AND 02)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC10.02	08-04	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASELO	I	211	1/1		SELI0	2A2
BUSRD0A	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0A	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0C	0	IC	2/11		BUSRD0	2G3

SYMBOL NO. 16 (CONT)

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 10 AND 02)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC10.02	08-04	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XBUSRD1C	0	IC	2/11		BUSRD1	2G3
XCNLDB1C	0	IC	2/11		NLDB1	2G0
XCNLD0C	0	IC	2/11		NLD0	2G0
XCNRD1C	0	IC	2/11		NRD1	2G6
XCREF0C	0	IC	2/11		REF0	2G5
XCREF1C	0	IC	3/11		REF1	2G4
XCSEL0C	0	IC	2/11,6/18		SEL0	2G2
XCSEL1C	0	IC	2/11		SEL1	2G1
XRDSTB0C	0	IC	2/11		RDDSTB0	2G5
XRDSTB1C	0	IC	2/11		RDDSTB1	2G5
3T046D	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3V046D	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 17

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 09 AND 01)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC09.01	08-03	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASELO	I	211	1/1		SELI0	2A2
BUSRD0A	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0A	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0B	0	IC	2/12		BUSRD0	2G3
XBUSRD1B	0	IC	2/12		BUSRD1	2G3
XCNLDB1B	0	IC	2/12		NLDB1	2G0
XCNLD0B	0	IC	2/12		NLD0	2G0
XCNRD1B	0	IC	2/12		NRD1	2G6
XCREF0B	0	IC	2/12		REF0	2G5
XCREF1B	0	IC	3/12		REF1	2G4
XCSEL0B	0	IC	2/12,6/19		SEL0	2G2
XCSEL1B	0	IC	2/12		SEL1	2G1
XRDSTB0B	0	IC	2/12		RDDSTB0	2G5

SYMBOL NO. 17 (CONT)

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 09 AND 01)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC09.01	08-03	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XRDSTB1B	0	IC	2/12		RDDSTB1	2G5
3T046C	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	302		202	GRD302	2A7
	GRD	304		202	GRD304	2A7
	GRD	319		202	GRD	
3V046C	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 18

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 08 AND 00)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC08.00	08-02	FA1060	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		110			GRD110	2A7
		310			GRD310	2A7
ASELO	I	211	1/1		SELI0	2A2
BUSRD0A	I	109	1/2		BUSRDI0	2A3
DSTB0	I	209	1/1		DSTBI0	2A5
NLD0	I	306	1/1		NLDI0	2A0
READ0A	I	309	1/2		NRDI0	2A6
REFMODE0	I	206	1/1		REFI0	2A5
XBUSRD0A	0	IC	2/13		BUSRD0	2G3
XBUSRD1A	0	IC	2/13		BUSRD1	2G3
XCNLDB1A	0	IC	2/13		NLDB1	2G0
XCNLD0A	0	IC	2/13		NLD0	2G0
XCNRD1A	0	IC	2/13		NRD1	2G6
XCREF0A	0	IC	2/13		REF0	2G5
XCREF1A	0	IC	3/13		REF1	2G4
XCSEL0A	0	IC	2/13,6/20		SEL0	2G2
XCSEL1A	0	IC	2/13		SEL1	2G1
XRDSTB0A	0	IC	2/13		RDDSTB0	2G5
XRDSTB1A	0	IC	2/13		RDDSTB1	2G5
3T046B	GRD	0GD		202	GRD	
	GRD	002		202	GRD002	2A8
	GRD	003		202	GRD003	2A8
	GRD	005		202	GRD005	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	

SYMBOL NO. 18 (CONT)

BIT SLICE BOARD ADDRESS AND DATA CONTROL
(BITS 08 AND 00)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
BSC08.00	08-02	FA1060	A	

FS INFO					CP INFO
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD
3T046B	GRD	202		202	GRD202
	GRD	205		202	GRD205
	GRD	302		202	GRD302
	GRD	304		202	GRD304
	GRD	319		202	GRD
3V046B	PWR	000		202	+3
	PWR	119		202	+3

SYMBOL NO. 19

NO. 28 DATA PARITY BUS DIRECTION CONTROL

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
28DPARBD	08-14	FA1067	A	

FS INFO					CP INFO
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD
ASELO	I	309	1/1		SELI0
BUSRD0B	I	211	1/2		BUSRDI0
XBUSRDB0	0	IC	2/18		BUSRDB0
XBUSRDB1	0	IC	2/18		BUSRDB1
XCBUSRD0	0	IC	2/18		BUSRD0
XCBUSRD1	0	IC	2/18		BUSRD1
XSEL0	0	IC	2/18		SEL0
XSEL1	0	IC	2/18		SEL1
3T046A	GRD	0GD		202	GRD
	GRD	005		202	GRD005
	GRD	012		202	GRD012
	GRD	017		202	GRD017
	GRD	102		202	GRD102
	GRD	107		202	GRD107
	GRD	114		202	GRD114
	GRD	2GD		202	GRD
	GRD	200		202	GRD
	GRD	205		202	GRD205
	GRD	212		202	GRD212
	GRD	217		202	GRD217
	GRD	302		202	GRD302
	GRD	314		202	GRD314
	GRD	319		202	GRD
3V046A	PWR	000		202	+3
	PWR	119		202	+3

RAPT OF FS 1
SYMBOL(S) 15 16 17 18 19

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE
CZ
BELL LABORATORIES
SD-1C902-03
E
PRINTED IN U. S. A.

PART OF FS 1

CONTROL AND TIMING

SYMBOL NO. 20

CLOCK AND POWER INITIALIZE

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CLK.PINT	08-23	FC203	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLOCKOA	OT	204	6/1		TCKOA	2E5
	I		1/1			
	OT	IC204	1/20		TCKOA	2D
CLOCKOB	OT	202	6/1		TCKOB	2E5
	I		3/1,5/2			
	OT	IC202	1/20		TCKOB	2D5
NLDO	I	105	1/1		NLDIO	2B5
PINITO	O	305	6/2		PINITO	2E5
PINIT1	I	004	10/9		PINITI1	2B5
SREQIO	I	006	TO CONN CKT		SREQIO	2B5
SREQOO	O	302	TO CONN CKT		SREQO	2E5
XFSTPCKO	I	IC	6/1		FSTPCKO	2D5
XXNLDB1	O	IC	2/14		NLOB1	2D5
XXNLDO	O	IC	2/14		NLOO	2D5

PART OF FS 1
SYMBOL(S) 20

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE [illegible]
ISSUE 1

BELL LABORATORIES
SD-1C902-03
B1CF

PRINTED IN U. S. A.

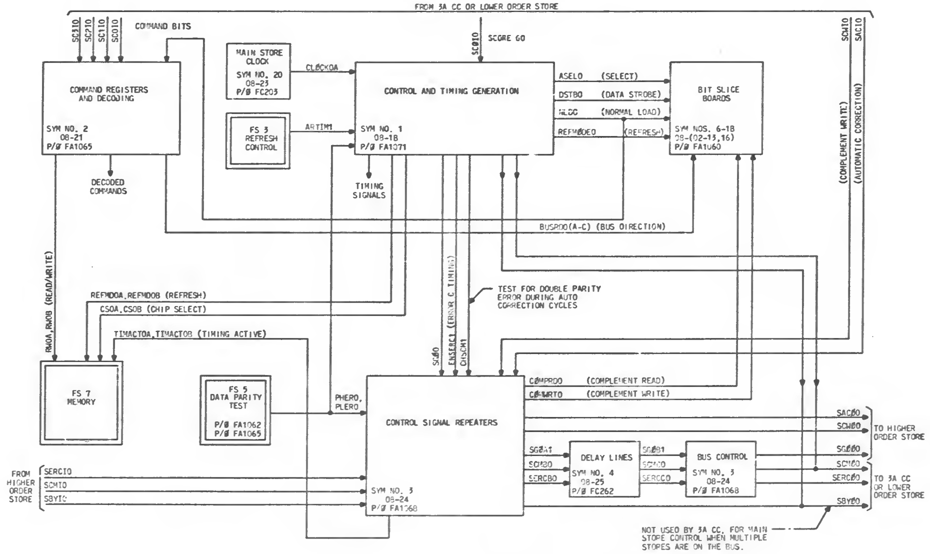
[illegible]

PART OF FS I

CONTROL AND TIMING

COMPOSITE DIAGRAM I

FUNCTIONAL DIAGRAM



PART OF FS 1
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY ②	DWG SIZE 6S	ISSUE 1
BELL LABORATORIES	SD-1C902-03	BIGA

PART OF FS 1

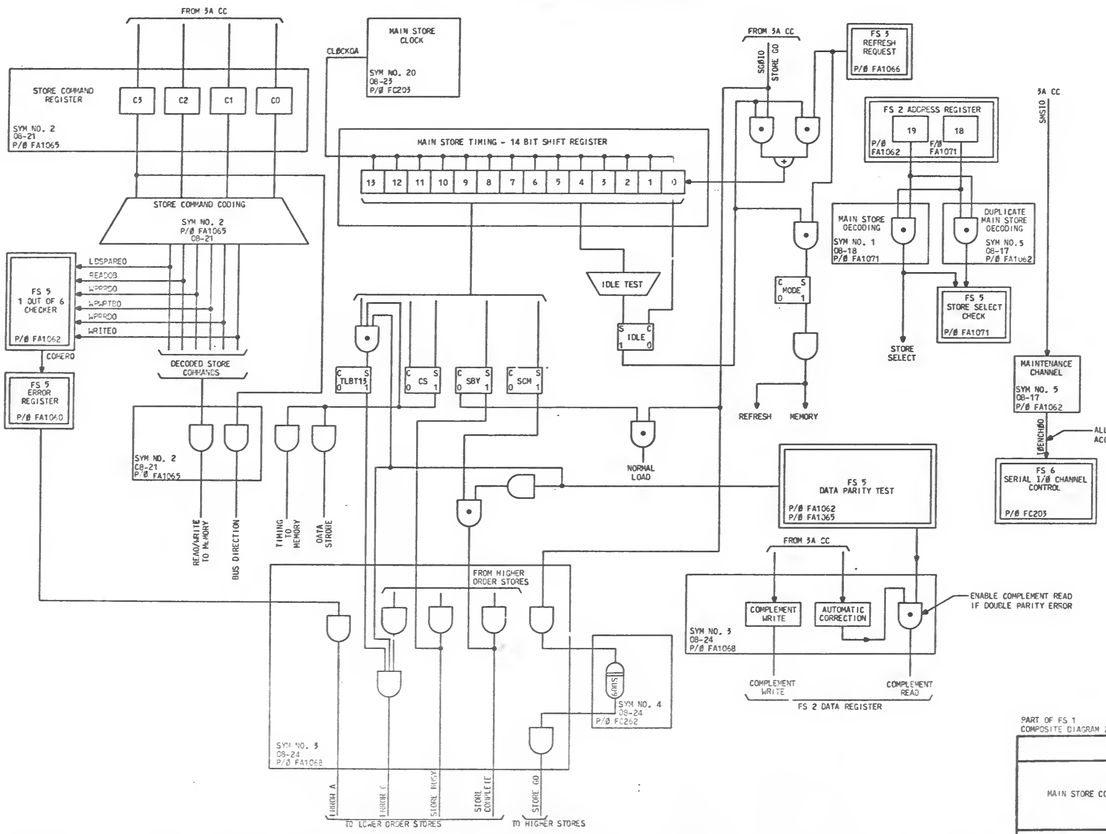
CONTROL AND TIMING

COMPOSITE DIAGRAM 2

CONTROL AND TIMING CONTROL DIAGRAM

NOTE:

1. ALL CIRCUITS SHOWN ON THIS COMPOSITE ARE CONTAINED ON CP FA1071 UNLESS OTHERWISE NOTED.



PART OF FS 1
COMPOSITE DIAGRAM 2

MAIN STORE CONTROLLER AND MEMORY	2	DWG SIZE 6S
BELL LABORATORIES	SD-1C902-03	E

PART OF FS I

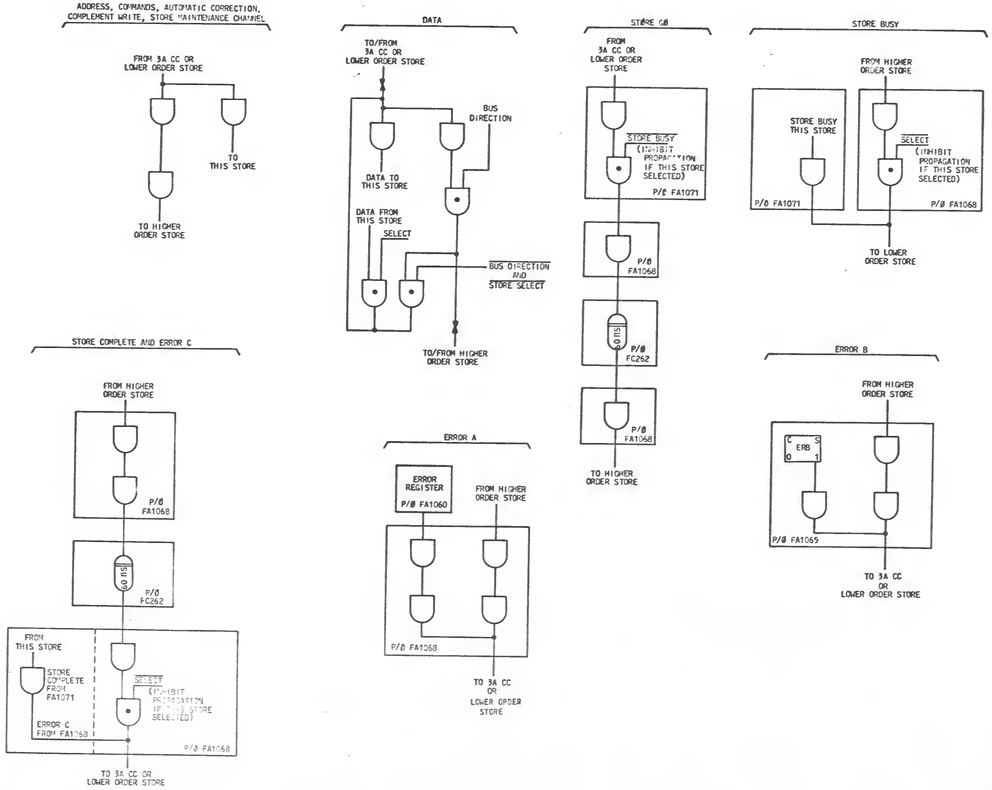
CONTROL AND TIMING

COMPOSITE DIAGRAM 3

MAIN STORE BUS REPEATERS

NOTE:

1. NOT ALL BUS REPEATERS ARE IN FS 1, BUT FOR COMPLETENESS ALL TYPES OF CONFIGURATION ARE SHOWN IN THIS COMPOSITE.


ADDRESS, COMMANDS, AUTOMATIC CORRECTION, COMPLEMENT WRITE, STORE MAINTENANCE CHANNEL
FROM 3A CC OR LOWER ORDER STORE
TO THIS STORE
TO HIGHER ORDER STORE
DATA
TO/FROM 3A CC OR LOWER ORDER STORE
BUS DIRECTION
DATA TO THIS STORE
DATA FROM THIS STORE
SELECT
BUS DIRECTION AND STORE SELECT
TO/FROM HIGHER ORDER STORE
STORE GO
FROM 3A CC OR LOWER ORDER STORE
STORE BUSY
(INHIBIT PROPAGATION IF THIS STORE SELECTED)
P/O FA1071
P/O FA1068
60 ns
P/O FC262
P/O FA1068
TO HIGHER ORDER STORE
STORE BUSY
STORE BUSY THIS STORE
FROM HIGHER ORDER STORE
SELECT
(INHIBIT PROPAGATION IF THIS STORE SELECTED)
P/O FA1071
P/O FA1068
TO LOWER ORDER STORE
STORE COMPLETE AND ERROR C
FROM HIGHER ORDER STORE
P/O FA1068
60 ns
P/O FC262
FROM THIS STORE
STORE COMPLETE FROM FA1071
ERROR C FROM FA1068
SELECT
(INHIBIT PROPAGATION IF THIS STORE SELECTED)
P/O FA1068
TO 3A CC OR LOWER ORDER STORE
ERROR A
ERROR REGISTER
P/O FA1060
FROM HIGHER ORDER STORE
P/O FA1068
TO 3A CC OR LOWER ORDER STORE
ERROR B
FROM HIGHER ORDER STORE
C S
ERB
0 1
P/O FA1065
TO 3A CC OR LOWER ORDER STORE

PART OF FS 1
COMPOSITE DIAGRAM 3

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

BELL LABORATORIES

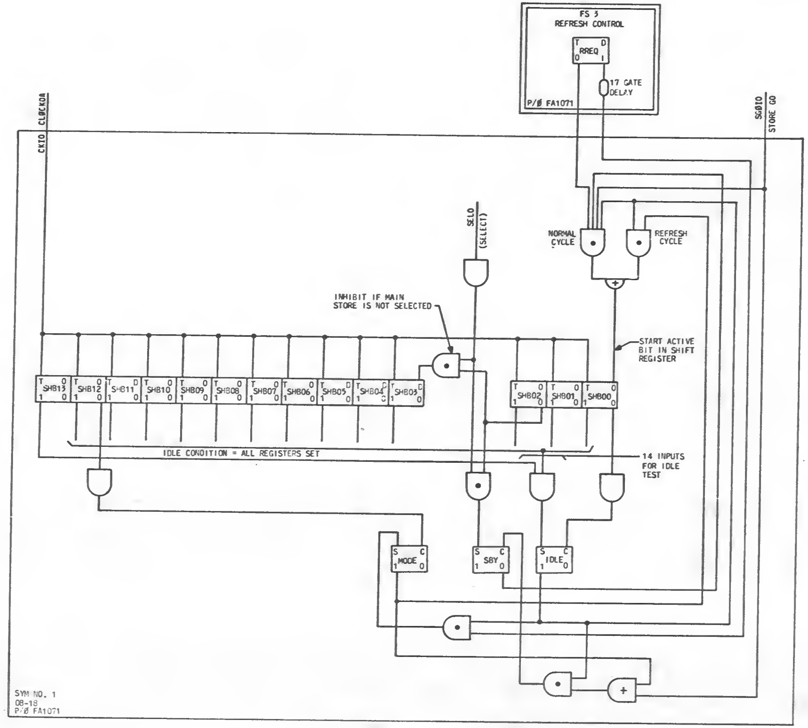
SD-1C902-03

PART OF FS I

CONTROL AND TIMING

COMPOSITE DIAGRAM 4

TIMING SHIFT REGISTER



IDLE F/F

SET = NO ACTIVE BITS IN SHIFT REGISTER BITS 0 THRU 12, AND BIT 13 ACTIVE
CLEAR = SHIFTER BIT 0 ACTIVE.

MODE F/F

SET = IDLE F/F SET ANO RREQ F/F SET.
CLEAR = SHIFTER BIT 12 ACTIVE.

SBY F/F

SET = SHIFTER BIT 2 ACTIVE ANO STORE SELECTED.
CLEAR = IDLE F/F SET ANO (STORE GO INACTIVE OR MODE F/F SET)

PART OF FS 1
COMPOSITE DIAGRAM 4

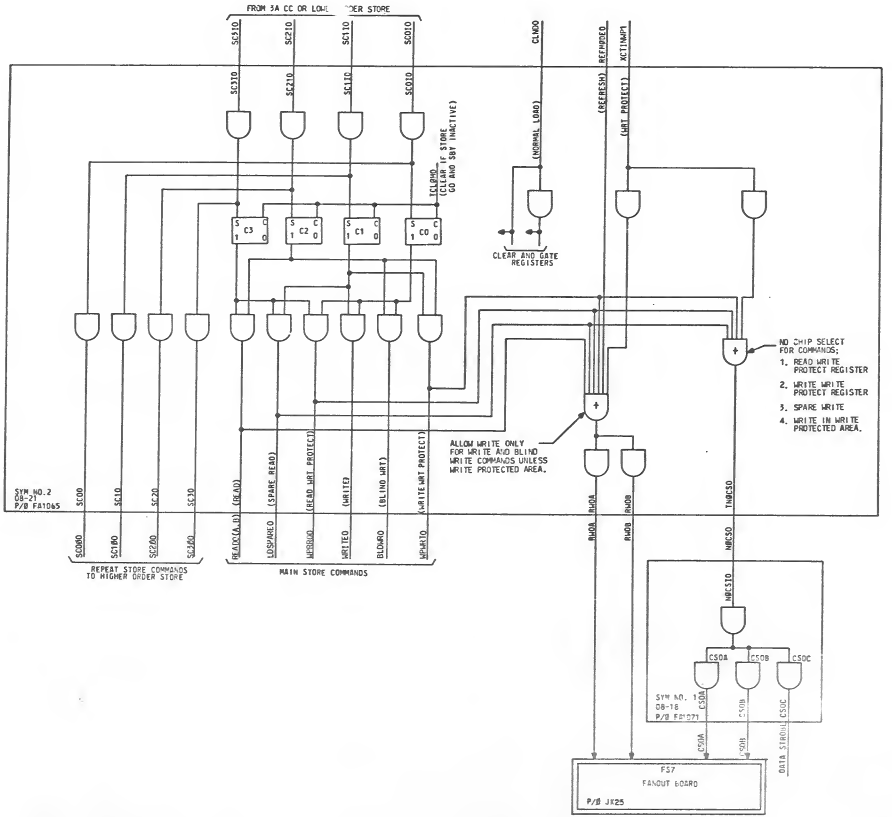
MAIN STORE CONTROLLER AND MEMORY ②	DWG SIZE bS	ISSUE 1
BELL LABORATORIES	SD-1C902-03	BIGD

PART OF FS I

CONTROL AND TIMING

COMPOSITE DIAGRAM 5

COMMAND REGISTER



NOTES:

1. COMMAND CODE BITS

SC3I0	SC2I0	SC1I0	SC0I0	FUNCTION
0	0	1	1	WRITE
1	1	0	0	READ
0	1	1	0	STORE WRITE PROTECT INFOMATION IN MAIN STORE
1	0	0	1	READ WRITE PROTECT INFOMATION FROM MAIN STORE
0	1	0	1	BLIND WRITE *
1	0	1	0	SPARE READ+

* BLIND WRITE CODE CAN ONLY BE USED WHEN ACCOMPANIED BY A WIRING OPTION ON THE MAIN STORE CONTROLLER BACKPLANE.

+ SPARE READ IS A NON-USABLE CODE; WHILE IT IS DECODED IN THE MAIN STORE IT IS NOT PRESENTLY USED AS AN ASSIGNED FUNCTION

2. A LOW VOLTAGE ON THE MAIN STORE BUS IS DESIGNATED A LOGIC 1.
A HIGH VOLTAGE ON THE MAIN STORE BUS IS DESIGNATED A LOGIC 0.

PART OF FS 1
COMPOSITE DIAGRAM 5

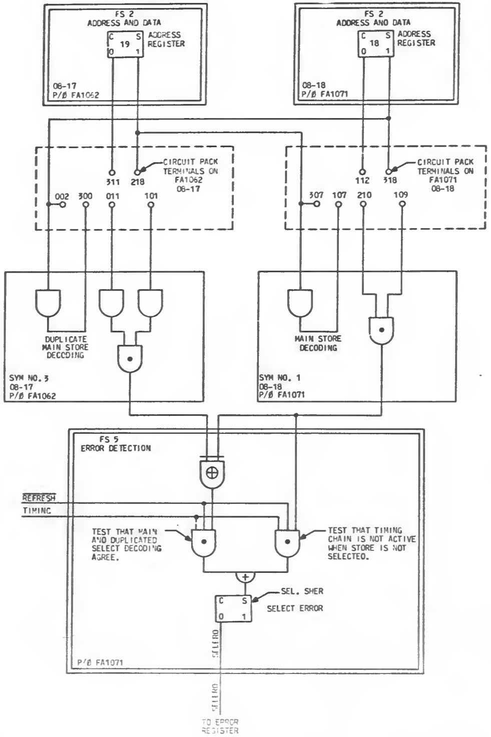
MAIN STORE CONTROLLER AND MEMORY ②	DWG SIZE 6S	ISS 1
BELL LABORATORIES	SD-1C902-03	BIGE

PART OF FS I

CONTROL AND TIMING

COMPOSITE DIAGRAM 6

MAIN STORE SELECT



MAIN STORE SELECT WIRING				
STORE	ADDRESS BITS		FROM	TO
	19	18		
STORE 0	0	0	08-18-318	08-18-109
	0	0	08-18-107	08-18-210
	0	0	08-17-002	08-17-101
	0	0	08-17-218	08-17-011
STORE 1	0	1	08-18-112	08-18-109
	0	1	08-18-107	08-18-210
	0	1	08-17-300	08-17-101
	0	1	08-17-218	08-17-011
STORE 2	1	0	08-18-318	08-18-109
	1	0	08-18-307	08-18-210
	1	0	08-17-002	08-17-101
	1	0	08-17-311	08-17-011
STORE 3	1	1	08-18-112	08-18-109
	1	1	08-18-307	08-18-210
	1	1	08-17-300	08-17-101
	1	1	08-17-311	08-17-011

PART OF FS 1
COMPOSITE DIAGRAM 6

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE 6S

ISSUE 1

BELL LABORATORIES

SD-1C902-03

PART OF FS I

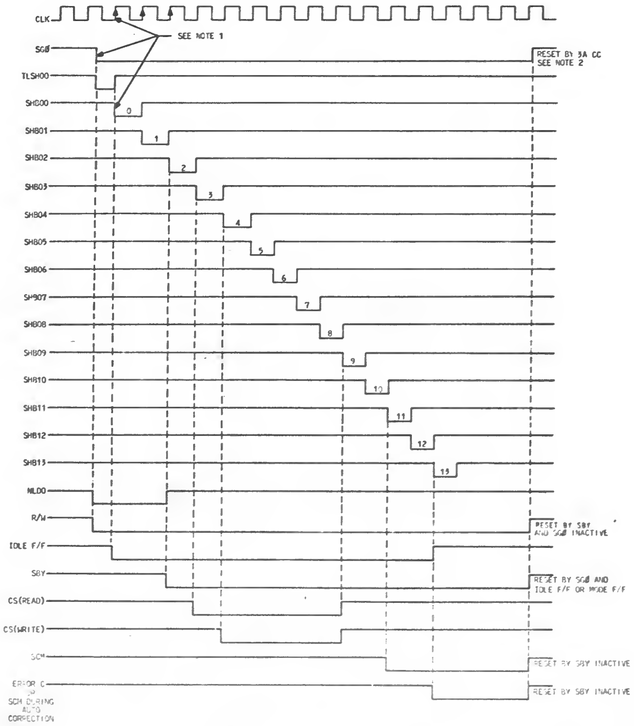
CONTROL AND TIMING

COMPOSITE DIAGRAM 7

TIMING DIAGRAM

NOTES:

1. THIS EDGE IS CONTROLLED BY THE [illegible] AND [illegible] NECESSARILY OCCUR AT THIS [illegible] (SH600) WILL NOT OCCUR [illegible] SG0 [illegible] AND COINCIDENT WITH THE [illegible]
2. THIS EDGE IS CONTROLLED BY THE [illegible] AND [illegible] NECESSARILY OCCUR AT THIS [illegible] OF SG0 IS ASYNCHRONOUS TO THE [illegible]



PART OF FS 1
COMPOSITE DIAGRAM 7

MAIN STORE CONTROLLER AND MEMORY

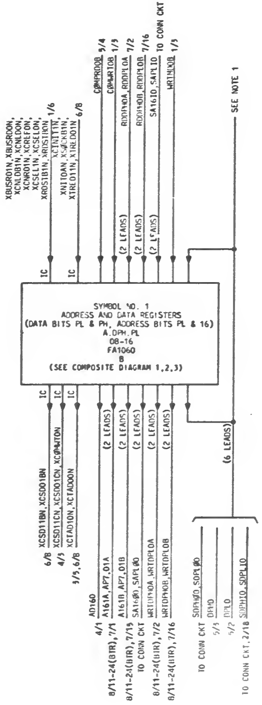
BELL LABORATORIES

SD-1C902-C3

PART OF FS 2

ADDRESS AND DATA
INTERCONNECTION AND FLOW DIAGRAM

NOTES:
1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (OI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



XBUSR01N, XBUSRDON, XCNLDB1N, XCNLDON, XCNR01N, XCREFON, XCSEL1N, XCSELON, XROS1B1N, XRDSTBON, XCINIT1N 1/6
XNITOAN, XSØCKB1N, XTRED011N 6/8
CØMPRDOB 5/4
CØMWRTOB 1/3
RDDIMOA, RDDPLOA 7/2
RDDIMOB, RDDPLOB 7/16
SA16IO, SAPLIO TO CONN CKT
WRTMIOB 1/5
SEE NOTE 1
(2 LEADS)
(2 LEADS)
(2 LEADS)
IC
IC
SYMBOL NO. 1
ADDRESS AND DATA REGISTERS
(DATA BITS PL & PH, ADDRESS BITS PL & 16)
A.DPH.PL
08-16
FA1060
B
(SEE COMPOSITE DIAGRAM 1,2,3)
IC
IC
IC
XCSD11BN, XCSD01BN 6/8
XCSD11CN, XCSD01CN, XCØMWTON 4/5
XCTAD10N, XCTADOON 5/5, 6/8
AD160 4/1
A161A, AP7, 01A 8/11-24(BTR), 7/1
A161B, AP7, 01B 8/11-24(BTR), 7/15
SA16ØO, SAPLØO TO CONN CKT
WRTDMOA, WRTDPLOA 8/11-24(BTR), 7/2
WRTDMOB, WRTDPLOB 8/11-24(BTR), 7/16
(2 LEADS)
(2 LEADS)
(2 LEADS)
(2 LEADS)
(2 LEADS)
(6 LEADS)
SDPHØO, SDPLØO TO CONN CKT
DPHO 5/3
DPLO 5/2
SDPHIO, SDPLIO TO CONN CKT, 2/18

PART OF FS 2
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE
ISSUE 1

BELL LABORATORIES SD-1C902-03 B2AA

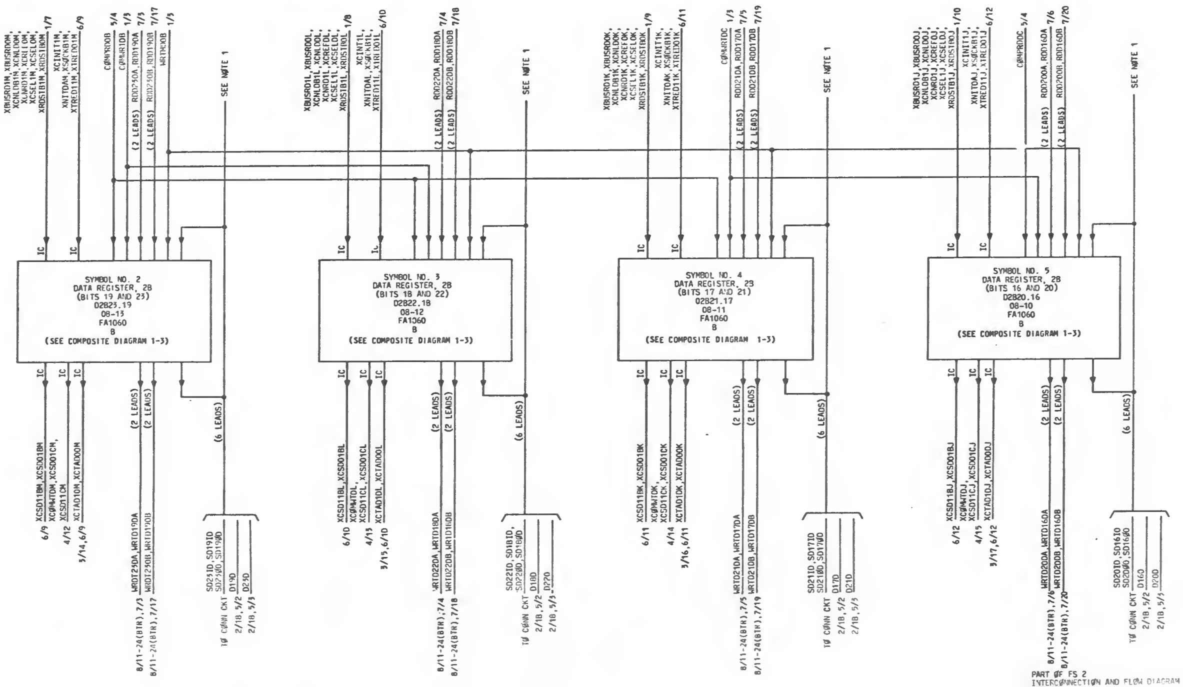
PRINTED IN U.S.A.

PART OF FS 2

ADDRESS AND DATA
INTERCONNECTION AND FLOW DIAGRAM

NOTES:

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (OI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.


XBUSRD1M, XBUSRD0M, XCNLDB1M, XCNLD0M, XLNRD1M, XCRI1OM, XCSEL1M, XCSEL0M, XRDSTB1M, XRDSTB0M 1/7
XCINIT1M, XNITDAM, XSOCKB1M, XTRED11M, XTRED01M 6/9
COMWRD0B 5/4
COMWR1DB 1/3
(2 LEADS) RDD230A, RDD190A 7/3
(2 LEADS) RDD230B, RDD190B 7/17
WRTM00B 1/3
SEE NOTE 1
IC
SYMBOL NO. 2
DATA REGISTER, 28
(BITS 19 AND 23)
02B23.19
08-13
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/9 XCSD11BM, XCSD01BM
XCOMWTDM, XCSD01CM,
4/12 XCSD11CM
3/14, 6/9 XCTAD10M, XCTAD00M
(2 LEADS)
8/11-24(BTR), 7/3 WRDT23DA, WRTD19DA
8/11-24(BTR), 7/17 WRDT23DB, WRTD19DB
(6 LEADS)
SD23ID, SD19ID
SD230D, SD190D
TO CONN CKT
2/1B, 5/2 D19D
2/1B, 5/3 D23D
XBUSRD1L, XBUSRD0L, XCNLDB1L, XCNLD0L, XCNRD1L, XCREFDL, XCSEL1L, XCSEL0L, XRDSTB1L, XRDSTB0L 1/8
XCINT1L, XNITDAL, XSOCKB1L, XTRED11L, XTRED01L 6/10
(2 LEADS) RDD220A, RDD180A 7/4
(2 LEADS) RDD220B, RDD180B 7/18
SYMBOL NO. 3
DATA REGISTER, 28
(BITS 18 AND 22)
02B22.18
08-12
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/10 XCSD11BL, XCSD01BL
XCOMWTDL,
4/13 XCSD11CL, XCSD01CL
3/15, 6/10 XCTAD10L, XCTAD00L
8/11-24(BTR), 7/4 WRTD22DA, WRTD18DA
8/11-24(BTR), 7/18 WRTD22DB, WRTD18DB
SD22ID, SD18ID,
SD220D, SD180D
TO CONN CKT
2/1B, 5/2 D18D
2/1B, 5/3 D22D
XBUSRD1K, XBUSRD0K, XCNLDB1K, XCNLD0K, XCNRD1K, XCREF0K, XCSEL1K, XCSEL0K, XRDSTB1K, XRDSTB0K 1/9
XCINIT1K, XNITOAK, XSOCKB1K, XTRED11K, XTRED01K 6/11
COMWRIDC 1/3
(2 LEADS) RDD210A, RDD170A 7/5
(2 LEADS) RDD210B, RDD170B 7/19
SYMBOL NO. 4
DATA REGISTER, 28
(BITS 17 AND 21)
02B21.17
08-11
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/11 XCSD11BK, XCSD01BK
XCOMWTDK,
4/14 XCSD11CK, XCSD01CK
3/16, 6/11 XCTAD10K, XCTAD00K
8/11-24(BTR), 7/5 WRTD21DA, WRTD17DA
8/11-24(BTR), 7/19 WRTD21DB, WRTD17DB
SD21ID, SD17ID
SD210D, SD170D
TO CONN CKT
2/1B, 5/2 D17D
2/1B, 5/3 D21D
XBUSRD1J, XBUSRD0J, XCNLDB1J, XCNLD0J, XCNRD1J, XCREF0J, XCSEL1J, XCSEL0J, XRDSTB1J, XRDSTB0J 1/10
XCINIT1J, XNITOAJ, XSOCKB1J, XTRED11J, XTRED01J 6/12
COMPRDDC 5/4
(2 LEADS) RDD200A, RDD160A 7/6
(2 LEADS) RDD200B, RDD160B 7/20
SYMBOL NO. 5
DATA REGISTER, 28
(BITS 16 AND 20)
D2B20.16
08-10
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/12 XCSD11BJ, XCSD01BJ
XCOMWTDJ,
4/15 XCSD11CJ, XCSD01CJ
3/17, 6/12 XCTAD10J, XCTAD00J
8/11-24(BTR), 7/6 WRTD20DA, WRTD16DA
8/11-24(BTR), 7/20 WRTD20DB, WRTD16DB
SD20ID, SD16IO
SD200O, SD160D
TO CONN CKT
2/1B, 5/2 D16D
2/1B, 5/3 D20D

PART OF FS 2
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE 6S

ISSUE 1

BELL LABORATORIES

SD-1C902-03

B2AB

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E-7421(12 73)

PART OF FS 2

ADDRESS AND DATA
INTERCONNECTION AND FLOW DIAGRAM

NOTES:

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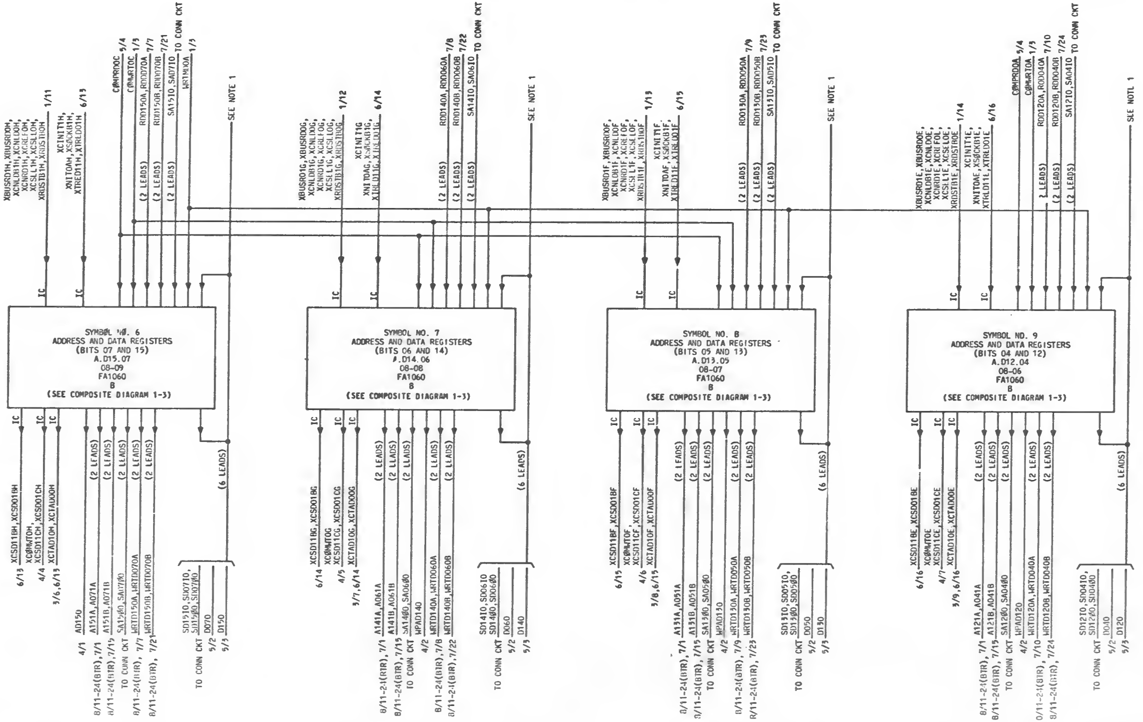

PART OF FS 2
INTERCONNECTIØN AND FLØW DIAGRAM

MAIN STØRE CØNTRØLLER AND MEMØRY

DWG SIZE	ISSUE
6S	1

BELL LABORATORIES | SD-1C902-03 | B2AC

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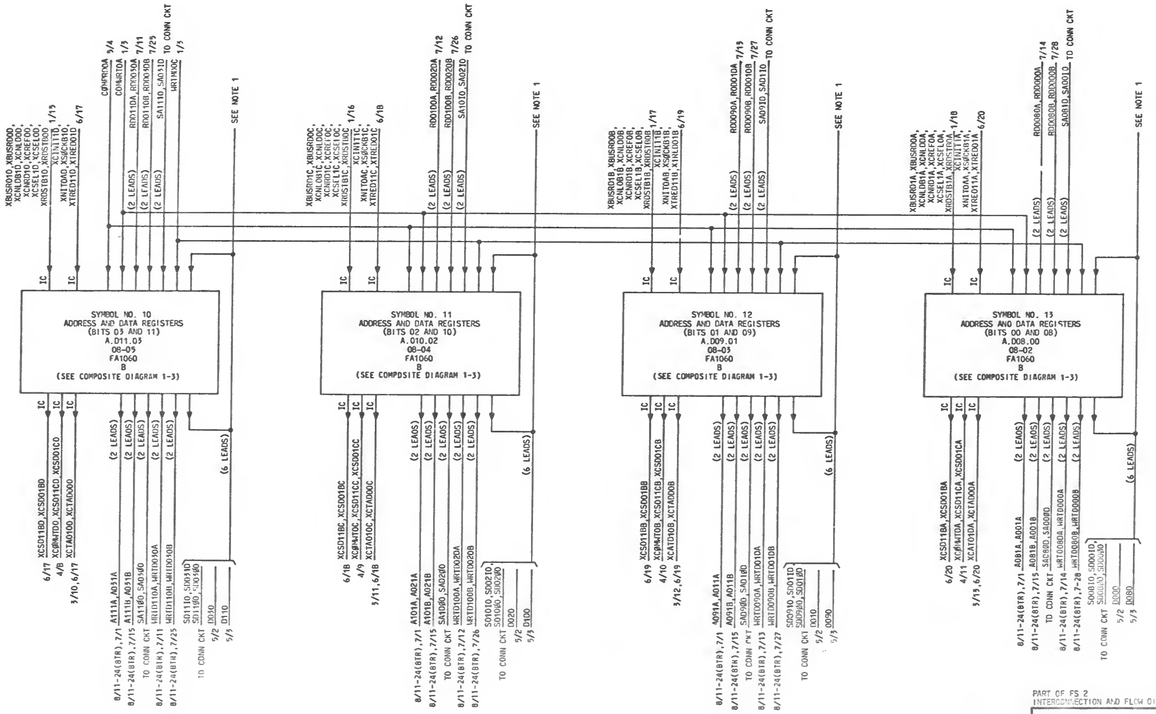
PART OF FS 2

ADDRESS AND DATA
INTERCONNECTION AND FLOW DIAGRAM

NOTES:

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (OI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



XBUSRO10, XBUSRDOD, XCNLDB1D, XCNLDOD, XCNRD10, XCREFOO, XCSEL1D, XCSELOD, XRDSTB10, XRDSTBOO 1/15
XCINIT1D, XNITOAD, XSØCKB10, XTRED11D, XTREDO1D 6/17
CØMPRDOA 5/4
COMWRTOA 1/3
RDD11DA, RDD030A 7/11
RDD110B, RDD030B 7/25
SA1110, SA031D TO CONN CKT
WR1M00C 1/3
SEE NOTE 1
(2 LEADS)
IC
SYMBOL NO. 10
ADDRESS AND DATA REGISTERS
(BITS 03 AND 11)
A.D11.03
08-05
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/17 XCSD11BD, XCSD01BD
4/8 XCØMWTDO, XCSO11CD, XCSD01CO
3/10, 6/17 XCTA0100, XCTAD000
8/11-24(BTR), 7/1 A111A, A031A
8/11-24(BTR), 7/15 A111B, A031B
TO CONN CKT SA11Ø0, SA03Ø0
8/11-24(BTR), 7/11 WRTD110A, WRTD030A
8/11-24(BTR), 7/25 WRTD110B, WRTD030B
TO CONN CKT SD1110, SD031D, SD11Ø0, SD03Ø0
5/2 D030
5/3 D110
(6 LEADS)
XBUSRD1C, XBUSRDOC, XCNLDB1C, XCNLDOC, XCNRD1C, XCREFOC, XCSEL1C, XCSELOC, XRDSTB1C, XRDSTBOC 1/16
XCINIT1C, XNITOAC, XSØCKB1C, XTRED11C, XTREDO1C 6/18
RDD100A, RDD020A 7/12
RDD100B, RDD020B 7/26
SA1010, SA0210 TO CONN CKT
SYMBOL NO. 11
ADDRESS AND DATA REGISTERS
(BITS 02 AND 10)
A.D10.02
08-04
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/18 XCSD11BC, XCSD01BC
4/9 XCØMWTOC, XCSD11CC, XCSD01CC
3/11, 6/18 XCTA010C, XCTADOOC
8/11-24(BTR), 7/1 A101A, A021A
8/11-24(BTR), 7/15 A101B, A021B
TO CONN CKT SA10Ø0, SA02Ø0
8/11-24(BTR), 7/12 WRTD100A, WRTD020A
8/11-24(BTR), 7/26 WRTD100B, WRTD020B
TO CONN CKT SD1010, SD0210, SD10Ø0, SD02Ø0
5/2 D020
5/3 D100
XBUSRD1B, XBUSRDOB, XCNLDB1B, XCNLDOB, XCNRD1B, XCREFOB, XCSEL1B, XCSELOB, XRDSTB1B, XRDSTBOB 1/17
XCINIT1B, XNITOAB, XSØCKB1B, XTRED11B, XTREDO1B 6/19
RDD090A, RDD010A 7/13
RDD090B, RDD010B 7/27
SA091D, SA0110 TO CONN CKT
SYMBOL NO. 12
ADDRESS AND DATA REGISTERS
(BITS 01 AND 09)
A.D09.01
08-03
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/19 XCSD11BB, XCSD01BB
4/10 XCØMWTOB, XCSD11CB, XCSD01CB
3/12, 6/19 XCATD10B, XCTADOOB
8/11-24(BTR), 7/1 A091A, A011A
8/11-24(BTH), 7/15 A091B, A011B
TO CONN CKT SA09Ø0, SA01Ø0
8/11-24(BTR), 7/13 WRTD090A, WRTD010A
8/11-24(BTR), 7/27 WRTD090B, WRTD010B
TO CONN CKT SD0910, SD011D, SD09Ø0, SD01Ø0
5/2 D010
5/3 D090
XBUSRD1A, XBUSRDOA, XCNLDB1A, XCNLDOA, XCNRD1A, XCREFOA, XCSEL1A, XCSELOA, XRDSTB1A, XRDSTBOA 1/18
XCINIT1A, XNITOAA, XSØCKB1A, XTRED11A, XTREDO1A 6/20
RDD080A, RDD000A 7/14
RDD080B, RDD000B 7/28
SA081D, SA0010 TO CONN CKT
SYMBOL NO. 13
ADDRESS AND DATA REGISTERS
(BITS 00 AND 08)
A.D08.00
08-02
FA1060
B
(SEE COMPOSITE DIAGRAM 1-3)
6/20 XCSD11BA, XCSD01BA
4/11 XCØMWTDA, XCSD11CA, XCSD01CA
3/13, 6/20 XCATD10A, XCTADOOA
8/11-24(BTR), 7/1 A081A, A001A
8/11-24(BTR), 7/15 A081B, A001B
TO CONN CKT SA08Ø0, SA00Ø0
8/11-24(BTR), 7/14 WRTD080A, WRTD000A
8/11-24(BTR), 7-28 WRTD080B, WRTD000B
TO CONN CKT SD0810, SD001D, SD08Ø0, SD00Ø0
5/2 D000
5/3 D080

PART OF FS 2
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

②

DWG SIZE

BELL LABORATORIES

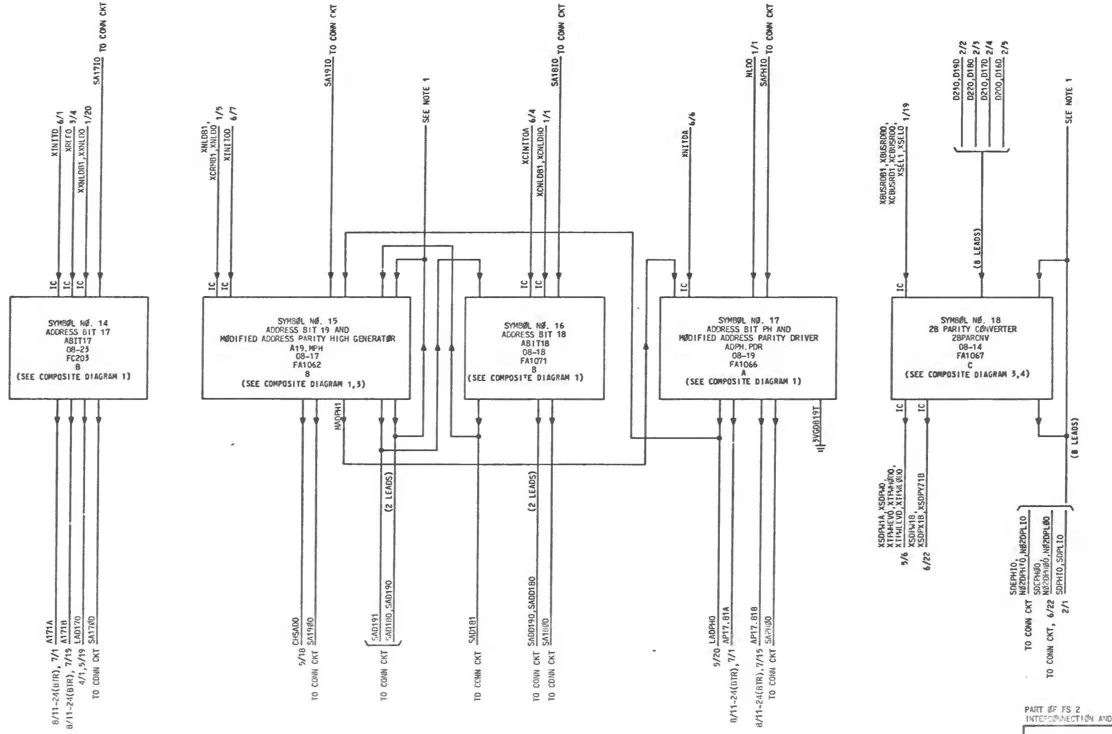
SD-1C902-03

PART OF FS 2

ADDRESS AND DATA
INTERCØNNECTIØN AND FLØW DIAGRAM

NOTES:

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (ØI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



PART ØF FS 2
INTERCØNNECTIØN AND FLØW DIAGRAM

MAIN STØRE CØNTRØLLER AND MEMØRY

(2)

DWG SIZE
65

ISS.
1

BELL LABORATORIES

SD-1C902-03

B2A

PART OF FS 2

ADDRESS AND DATA

SYMBOL NO. 1

ADDRESS AND DATA REGISTERS
(DATA BITS PL AND PH, ADDRESS BITS PL AND 16)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.DPH.PL	08-16	FA1060	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		**CP INFO**	
AD160	O	106	4/1		WPAD10	3H3
AP7.01A	O	204	7/1 8/11-24(BTR)		AD01A	3H1
AP7.01B	O	004	7/15 8/11-24(BTR)		AD01B	3H2
A161A	O	201	7/1 8/11-24(BTR)		AD11A	3H4
A161B	O	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRD0B	I	301	5/4		COMPRDI0	4A6
COMWRT0B	I	101	1/3		COMWRTI0	4A2
DPH0	OI	009	5/3		D10	4H3
DPL0	OI	006	5/2		D00	5H3
RDDPH0A	I	111	7/2		RDD1AI0	4A4
RDDPH0B	I	311	7/16		RDD1BI0	4A5
RDDPL0A	I	010	7/2		RDD0AI0	5A3
RDDPL0B	I	210	7/16		RDD0BI0	5A5
SAPLI0	I	018	TO CONN CKT		SAD0I0	3A3
SAPL00	O	112	TO CONN CKT		SAD00	3H2
SA16I0	I	317	TO CONN CKT		SAD1I0	3A3
SA1600	O	312	TO CONN CKT		SAD10	3H4
SDPHI0	OI	013	2/18 TO CONN CKT		TSD1I0	4A0
SDPH00	OI	213	TO CONN CKT		TG.SD10	4G8
SDPLI0	OI	314	2/18 TO CONN CKT		TSD0I0	5A0
SDPL00	OI	113	TO CONN CKT		TG.SD00	5G7
WRTDPH0A	O	303	7/2 8/11-24(BTR)		D10A	4H2
WRTDPH0B	O	103	7/16 8/11-24(BTR)		D10B	4H3
WRTDPL0A	O	309	7/2 8/11-24(BTR)		D00A	5H2
WRTDPL0B	O	100	7/16 8/11-24(BTR)		D00B	5H3
WRTMD0B	I	214	1/3		WRTMDI0	4A0
XBUSRD0N	I	IC	1/6		BUSRD0	4A8
XBUSRD1N	I	IC	1/6		BUSRD1	4A7
XCINIT1N	I	IC	6/8		INIT1	4A2
XCNLDB1N	I	IC	1/6		NLDB1	3D0
XCNLD0N	I	IC	1/6		NLD0	3D0
XCNRD1N	I	IC	1/6		NRD1	4A5
XCOMWT0N	O	IC	4/3		COMWRT0	4H1
XCREF0N	I	IC	1/6		REF0	3D0
XCSD01BN	O	IC	6/8		SD01B	5H1
XCSD01CN	O	IC	4/3		SD01C	5H0
XCSD11BN	O	IC	6/8		SD11B	4H1
XCSD11CN	O	IC	4/3		SD11C	4H1
XCSEL0N	I	IC	1/6		SEL0	4A7
XCSEL1N	I	IC	1/6		SEL1	4A5
XCTAD00N	OT I	IC	3/5 6/8		TAD00	3H2
XCTAD10N	OT I	IC	3/5 6/8		TAD10	3H3
XNIT0AN	I	IC	6/8		INIT0A	3D0
XRDSTB0N	I	IC	1/6		RDDSTB0	4A3
XRDSTB1N	I	IC	1/6		RDDSTB1	4A3
XSOCKB1N	I	IC	6/8		ISOCKB1	4A8
XTRED01N	I	IC	6/8		TRETD01	5A5
XTRED11N	I	IC	6/8		TRETD11	4A7

SYMBOL NO. 2

DATA REGISTER, 2B
(BITS 19 AND 23)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
D2B23.19	08-13	FA1060	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		**CP INFO**	
		001			AD11B	3H5
		004			AD01B	3H2
		018			SAD0I0	3A3
		106			WPAD10	3H3
		112			SAD00	3H2
		201			AD11A	3H4
		204			AD01A	3H1
		312			SAD10	3H4
		317			SAD1I0	3A3
COMPRD0B	I	301	5/4		COMPRDI0	4A6
COMWRT0B	I	101	1/3		COMWRTI0	4A2
D190	OI	006	2/18,5/2		D00	5H3
D230	OI	009	2/18,5/3		D10	4H3
RDD190A	I	010	7/3		RDD0AI0	5A3
RDD190B	I	210	7/17		RDD0BI0	5A5
RDD230A	I	111	7/3		RDD1AI0	4A4
RDD230B	I	311	7/17		RDD1BI0	4A5
SD19I0	OI	314	TO CONN CKT		TSD0I0	5A0
SD1900	OI	113	TO CONN CKT		TG.SD00	5G7
SD23I0	OI	013	TO CONN CKT		TSD1I0	4A0
SD2300	OI	213	TO CONN CKT		TG.SD10	4G8
WRTD190A	O	300	7/3 8/11-24(BTR)		D00A	5H2
WRTD190B	O	100	7/17 8/11-24(BTR)		D00B	5H3
WRTD230A	O	303	7/3 8/11-24(BTR)		D10A	4H2
WRTD230B	O	103	7/17 8/11-24(BTR)		D10B	4H3
WRTMD0B	I	214	1/3		WRTMDI0	4A0
XBUSRD0M	I	IC	1/7		BUSRD0	4A8
XBUSRD1M	I	IC	1/7		BUSRD1	4A7
XCINIT1M	I	IC	6/9		INIT1	4A2
XCNLDB1M	I	IC	1/7		NLDB1	3D0
XCNLD0M	I	IC	1/7		NLD0	3D0
XCNRD1M	I	IC	1/7		NRD1	4A5
XCOMWT0M	O	IC	4/12		COMWRT0	4H1
XCREF0M	I	IC	1/7		REF0	3D0
XCSD01BM	O	IC	6/9		SD01B	5H1
XCSD01CM	O	IC	4/12		SD01C	5H0
XCSD11BM	O	IC	6/9		SD11B	4H1
XCSD11CM	O	IC	4/12		SD11C	4H1
XCSEL0M	I	IC	1/7		SEL0	4A7
XCSEL1M	I	IC	1/7		SEL1	4A5
XCTAD00M	OT I	IC	3/14 6/9		TAD00	3H2
XCTAD10M	OT I	IC	3/14 6/9		TAD10	3H3
XNIT0AM	I	IC	6/9		INIT0A	3D0
XRDSTB0M	I	IC	1/7		RDDSTB0	4A3
XRDSTB1M	I	IC	1/7		RDDSTB1	4A3
XSOCKB1M	I	IC	6/9		ISOCKB1	4A8
XTRED01M	I	IC	6/9		TRETD01	5A5
XTRED11M	I	IC	6/9		TRETD11	4A7

SYMBOL NO. 3

DATA REGISTER, 2B
(BITS 18 AND 22)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
D2B22.18	08-12	FA1060	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		**CP INFO**	
		001			AD11B	3H5
		004			AD01B	3H2
		018			SAD0I0	3A3
		106			WPAD10	3H3
		112			SAD00	3H2
		201			AD11A	3H4
		204			AD01A	3H1
		312			SAD10	3H4
		317			SAD1I0	3A3
COMPRD0B	I	301	5/4		COMPRDI0	4A6
COMWRT0B	I	101	1/3		COMWRTI0	4A2
D180	OI	006	2/18,5/2		D00	5H3
D220	OI	009	2/18,5/3		D10	4H3
RDD180A	I	010	7/4		RDD0AI0	5A3
RDD180B	I	210	7/18		RDD0BI0	5A5
RDD220A	I	111	7/4		RDD1AI0	4A4
RDD220B	I	311	7/18		RDD1BI0	4A5
SD18I0	OI	314	TO CONN CKT		TSD0I0	5A0
SD1800	OI	113	TO CONN CKT		TG.SD00	5G7
SD22I0	OI	013	TO CONN CKT		TSD1I0	4A0
SD2200	OI	213	TO CONN CKT		TG.SD10	4G8
WRTD180A	O	300	7/4 8/11-24(BTR)		D00A	5H2
WRTD180B	O	100	7/18 8/11-24(BTR)		D00B	5H3
WRTD220A	O	303	7/4 8/11-24(BTR)		D10A	4H2
WRTD220B	O	103	7/18 8/11-24(BTR)		D10B	4H3
WRTMD0B	I	214	1/3		WRTMDI0	4A0
XBUSRD0L	I	IC	1/8		BUSRD0	4A8
XBUSRD1L	I	IC	1/8		BUSRD1	4A7
XCINIT1L	I	IC	6/10		INIT1	4A2
XCNLDB1L	I	IC	1/8		NLDB1	3D0
XCNLD0L	I	IC	1/8		NLD0	3D0
XCNRD1L	I	IC	1/8		NRD1	4A5
XCOMWT0L	O	IC	4/13		COMWRT0	4H1
XCREF0L	I	IC	1/8		REF0	3D0
XCSD01BL	O	IC	6/10		SD01B	5H1
XCSD01CL	O	IC	4/13		SD01C	5H0
XCSD11BL	O	IC	6/10		SD11B	4H1
XCSD11CL	O	IC	4/13		SD11C	4H1
XCSEL0L	I	IC	1/8		SEL0	4A7
XCSEL1L	I	IC	1/8		SEL1	4A5
XCTAD00L	OT I	IC	3/15 6/10		TAD00	3H2
XCTAD10L	OT I	IC	3/15 6/10		TAD10	3H3
XNIT0AL	I	IC	6/10		INIT0A	3D0
XRDSTB0L	I	IC	1/8		RDDSTB0	4A3
XRDSTB1L	I	IC	1/8		RDDSTB1	4A3
XSOCKB1L	I	IC	6/10		ISOCKB1	4A8
XTRED01L	I	IC	6/10		TRETD01	5A5
XTRED11L	I	IC	6/10		TRETD11	4A7

PART OF FS 2
SYMBOL(S) 1 2 3

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C2
ISSUE 1
BELL LABORATORIES
SD-1C902-03
B2CA
PRINTED IN U. S. A.
03/06/[illegible]

PART OF FS 2

ADDRESS AND DATA

SYMBOL NO. 4

DATA REGISTER, 2B
(BITS 17 AND 21)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
D2B21.17	08-11	FA1060	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		001			AD11B	3H5
		004			AD01B	3H2
		018			SAD010	3A
		106			WPAD10	3H3
		112			SAD00	3H2
		201			AD11A	3H4
		204			AD01A	3H1
		312			SAD10	3H4
		317			SAD110	3A3
COMPRDOB	I	301	5/4		COMPRDI0	4A6
COMWRTOC	I	101	1/3		COMWRTI0	4A2
D170	0I	006	2/18,5/2		D00	5H3
D210	0I	009	2/18,5/3		D10	4H3
RDD170A	I	010	7/5		RDD0AI0	5A3
RDD170B	I	210	7/19		RDD0BI0	5A5
RDD210A	I	111	7/5		RDD1AI0	4A4
RDD210B	I	311	7/19		RDD1BI0	4A5
SD1710	DI	314	TO CONN CKT		TSD0I0	5A0
SD1700	0I	113	TO CONN CKT		TG.SD00	5G7
SD2110	0I	013	TO CONN CKT		TSD110	4A0
SD2100	0I	213	TO CONN CKT		TG.SD10	4G8
WRTD170A	0	300	7/5 8/11-24(BTR)		D00A	5H2
WRTD170B	0	100	7/19 8/11-24(BTR)		D00B	5H3
WRTD210A	0	303	7/5 8/11-24(BTR)		D10A	4H2
WRTD210B	0	103	7/19 8/11-24(BTR)		D10B	4H3
WRTMD0B	I	214	1/3		WRTMDI0	4A0
XBUSRD0K	I	IC	1/9		BUSRD0	4A8
XBUSRD1K	I	IC	1/9		BUSRD1	4A7
XCINIT1K	I	IC	6/11		INIT1	4A2
XCNLDB1K	I	IC	1/9		NLDB1	3D0
XCNLD0K	I	IC	1/9		NLD0	3D0
XCNRD1K	I	IC	1/9		NRD1	4A5
XCOMWTOK	0	IC	4/14		COMWRTO	4H1
XCREF0K	I	IC	1/9		REF0	3D0
XCSD01BK	0	IC	6/11		SD01B	5H1
XCSD01CK	0	IC	4/14		SD01C	5H0
XCSD11BK	0	IC	6/11		SD11B	4H1
XCSD11CK	0	IC	4/14		SD11C	4H1
XCSEL0K	I	IC	1/9		SEL0	4A7
XCSEL1K	I	IC	1/9		SEL1	4A5
XCTAD00K	0T I	IC	3/16 6/11		TAD00	3H2
XCTAD10K	0T I	IC	3/16 6/11		TAD10	3H3
XNITOAK	I	IC	6/11		INITOA	3D0
XRDSTB0K	I	IC	1/9		RDDSTB0	4A3
XRDSTB1K	I	IC	1/9		RDDSTB1	4A3
XSDCKB1K	I	IC	6/11		ISDCKB1	4A8
XTRED01K	I	IC	6/11		TRETD01	5A5
XTRED11K	I	IC	6/11		TRETD11	4A7

SYMBOL NO. 5

DATA REGISTER, 2B
(BITS 16 AND 20)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
D2B20.16	08-10	FA1060	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		001			AD11B	3H5
		004			AD01B	3H2
		018			SAD010	3A3
		106			WPAD10	3H3
		112			SAD00	3H2
		201			AD11A	3H4
		204			AD01A	3H1
		312			SAD10	3H4
		317			SAD110	3A3
COMPRDOC	I	301	5/4		COMPRDI0	4A6
COMWRTOC	I	101	1/3		COMWRTI0	4A2
0160	0I	006	2/18,5/2		D00	5H3
D200	0I	009	2/18,5/3		D10	4H3
RDD160A	I	010	7/6		RDD0AI0	5A3
RDD160B	I	210	7/20		RDD0BI0	5A5
RDD200A	I	111	7/6		RDD1AI0	4A4
RDD200B	I	311	7/20		RDD1BI0	4A5
SD1610	0I	314	TO CONN CKT		TSD0I0	5A0
SD16D0	0I	113	TO CONN CKT		TG.SD00	5G7
SD2010	DI	013	TO CONN CKT		TSD110	4A0
SD20D0	0I	213	TO CONN CKT		TG.SD10	4G8
WRTD160A	0	300	7/6 8/11-24(BTR)		D00A	5H2
WRTD160B	0	100	7/20 8/11-24(BTR)		000B	5H3
WRTD200A	0	303	7/6 8/11-24(BTR)		010A	4H2
WRTD200B	0	103	7/20 8/11-24(BTR)		D10B	4H3
WRTMD0B	I	214	1/3		WRTMDI0	4A0
XBUSRD0J	I	IC	1/10		BUSRD0	4A8
XBUSRD1J	I	IC	1/10		BUSRD1	4A7
XCINIT1J	I	IC	6/12		INIT1	4A2
XCNLDB1J	I	IC	1/10		NLDB1	3D0
XCNLD0J	I	IC	1/10		NLD0	3D0
XCNRD1J	I	IC	1/10		NRD1	4A5
XCOMWTOJ	0	IC	4/15		COMWRTO	4H1
XCREF0J	I	IC	1/10		REF0	3D0
XCSD01BJ	0	IC	6/12		SD01B	5H1
XCSD01CJ	0	IC	4/15		SD01C	5H0
XCSD11BJ	0	IC	6/12		SD11B	4H1
XCSD11CJ	0	IC	4/15		SD11C	4H1
XCSEL0J	I	IC	1/10		SEL0	4A7
XCSEL1J	I	IC	1/10		SEL1	4A5
XCTAD00J	0T I	IC	3/17 6/12		TAD00	3H2
XCTAD10J	0T I	IC	3/17 6/12		TAD10	3H3
XNITOAJ	I	IC	6/12		INITOA	3D0
XRDSTB0J	I	IC	1/10		RDDSTB0	4A3
XRDSTB1J	I	IC	1/10		RDDSTB1	4A3
XSDCKB1J	I	IC	6/12		ISDCKB1	4A8
XTRED01J	I	IC	6/12		TRETD01	5A5
XTRED11J	I	IC	6/12		TRETD11	4A7

SYMBOL NO. 6

ADDRESS AND DATA REGISTERS
(BITS 07 AND 15)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.015.07	08-09	FA1060	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
AD150	0	106	4/1		WPAD10	3H3
A071A	0	204	7/1 8/11-24(BTR)		AD01A	3H1
A071B	0	004	7/15 8/11-24(BTR)		AD01B	3H2
A151A	0	201	7/1 8/11-24(BTR)		AD11A	3H4
A151B	0	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRDOC	I	301	5/4		COMPRDI0	4A6
COMWRTOC	I	101	1/3		COMWRTI0	4A2
D070	DI	006	5/2		D00	5H3
D150	0I	009	5/3		D10	4H3
RDD070A	I	010	7/7		RDD0AI0	5A3
RDD070B	I	210	7/21		RDD0BI0	5A5
RDD150A	I	111	7/7		RDD1AI0	4A4
RDD150B	I	311	7/21		RDD1BI0	4A5
SA0710	I	018	TO CONN CKT		SAD0I0	3A3
SA07D0	0	112	TO CONN CKT		SAD00	3H2
SA1510	I	317	TO CONN CKT		SAD110	3A3
SA1500	0	312	TO CONN CKT		SAD10	3H4
SD0710	0I	314	TO CONN CKT		TSD0I0	5A0
SD0700	0I	113	TO CONN CKT		TG.SD00	5G7
SD1510	0I	013	TO CONN CKT		TSD110	4A0
SD1500	0I	213	TO CONN CKT		TG.SD10	4G8
WRTD070A	0	300	7/7 8/11-24(BTR)		D00A	5H2
WRTD070B	0	100	7/21 8/11-24(BTR)		D00B	5H3
WRTD150A	0	303	7/7 8/11-24(BTR)		010A	4H2
WRTD150B	0	103	7/21 8/11-24(BTR)		010B	4H3
WRTMD0A	I	214	1/3		WRTMDI0	4A0
XBUSRD0H	I	IC	1/11		BUSRD0	4A8
XBUSRD1H	I	IC	1/11		BUSRD1	4A7
XCINIT1H	I	IC	6/13		INIT1	4A2
XCNLDB1H	I	IC	1/11		NLDB1	3D0
XCNLD0H	I	IC	1/11		NLD0	3D0
XCNRD1H	I	IC	1/11		NRD1	4A5
XCOMWTOH	0	IC	4/4		COMWRTO	4H1
XCREF0H	I	IC	1/11		REF0	3D0
XCSD01BH	0	IC	6/13		SD01B	5H1
XCSD01CH	0	IC	4/4		SD01C	5H0
XCSD11BH	0	IC	6/13		SD11B	4H1
XCSD11CH	0	IC	4/4		SD11C	4H1
XCSEL0H	I	IC	1/11		SEL0	4A7
XCSEL1H	I	IC	1/11		SEL1	4A5
XCTAD00H	0T I	IC	3/6 6/13		TAD00	3H2
XCTAD10H	0T I	IC	3/6 6/13		TAD10	3H3
XNITOAH	I	IC	6/13		INITOA	3D0
XRDSTB0H	I	IC	1/11		RDDSTB0	4A3
XRDSTB1H	I	IC	1/11		RDDSTB1	4A3
XSDCKB1H	I	IC	6/13		ISDCKB1	4A8
XTRED01H	I	IC	6/13		TRETD01	5A5
XTRED11H	I	IC	6/13		TRETD11	4A7

PART OF FS 2
SYMBOL(S) 4 5 6

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	C2	
ISSUE	1	
BELL LABORATORIES	SD-1C902-03	82C8

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PART OF FS 2

ADDRESS AND DATA

SYMBOL NO. 7

ADDRESS AND DATA PEGISTERS
(BITS 06 AND 14)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.014.06	08-08	FA1060	8	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
A061A	0	204	7/1 8/11-24(BTR)		AD01A	3H1
A061B	0	004	7/15 8/11-24(BTR)		AD01B	3H2
A141A	0	201	7/1 8/11-24(BTR)		AD11A	3H4
A141B	0	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRDOC	I	301	5/4		COMPRDIO	4A6
COMWRTOC	I	101	1/3		COMWRTIO	4A2
D060	OI	006	5/2		D00	5H3
D140	OI	009	5/3		D10	4H3
RDD060A	I	010	7/8		RDD0AIO	5A3
RDD060B	I	210	7/22		RDD0BIO	5A5
RDD140A	I	111	7/8		RDD1AIO	4A4
RDD140B	I	311	7/22		RDD1BIO	4A5
SA06IO	I	018	TO CONN CKT		SAD0IO	3A3
SA0600	0	112	TO CONN CKT		SAD00	3H2
SA14IO	I	317	TO CONN CKT		SAD1IO	3A3
SA1400	0	312	TO CONN CKT		SAD10	3H4
SD06IO	OI	314	TO CONN CKT		TSD0IO	5A0
SD0600	OI	113	TO CONN CKT		TG.SD00	5G7
SD14IO	OI	013	TO CONN CKT		TSD1IO	4A0
SD1400	OI	213	TO CONN CKT		TG.SD10	4G8
WPAD140	0	106	4/2		WPAD10	3H3
WRTD060A	0	300	7/8 8/11-24(BTR)		D00A	5H2
WRTD060B	0	100	7/22 8/11-24(BTR)		D00B	5H3
WRTD140A	0	303	7/8 8/11-24(BTR)		D10A	4H2
WRTD140B	0	103	7/22 8/11-24(BTR)		D10B	4H3
WRTMD0A	I	214	1/3		WRTMDIO	4A0
XBUSRD0G	I	IC	1/12		BUSRD0	4A8
XBUSRD1G	I	IC	1/12		BUSRD1	4A7
XCINIT1G	I	IC	6/14		INIT1	4A2
XCNLDB1G	I	IC	1/12		NLDB1	3D0
XCNLD0G	I	IC	1/12		NLD0	3D0
XCNRD1G	I	IC	1/12		NRD1	4A5
XCOMWT0G	0	IC	4/5		COMWRT0	4H1
XCREF0G	I	IC	1/12		REF0	3D0
XCSD01BG	0	IC	6/14		SD01B	5H1
XCSD01CG	0	IC	4/5		SD01C	5H0
XCSD11BG	0	IC	6/14		SD11B	4H1
XCSD11CG	0	IC	4/5		SD11C	4H1
XCSEL0G	I	IC	1/12		SEL0	4A7
XCSEL1G	I	IC	1/12		SEL1	4A5
XCTAD00G	OT I	IC	3/7 6/14		TAD00	3H2
XCTAD10G	OT I	IC	3/7 6/14		TAD10	3H3
XNITOAG	I	IC	6/14		INITOA	3D0
XRDSTB0G	I	IC	1/12		RDDSTB0	4A3
XRDSTB1G	I	IC	1/12		RDDSTB1	4A3
XSOCKB1G	I	IC	6/14		ISOCKB1	4A8
XTRED01G	I	IC	6/14		TRETD01	5A5
XTRED11G	I	IC	6/14		TRETD11	4A7

SYMBOL NO. 8

ADDRESS AND DATA REGISTERS
(BITS 05 AND 13)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.D13.05	08-07	FA1060	8	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
A051A	0	204	7/1 8/11-24(BTR)		AD01A	3H1
A051B	0	004	7/15 8/11-24(BTR)		AD01B	3H2
A131A	0	201	7/1 8/11-24(BTR)		AD11A	3H4
A131B	0	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRDOC	I	301	5/4		COMPRDIO	4A6
COMWRTOC	I	101	1/3		COMWRTIO	4A2
D050	OI	006	5/2		000	5H3
D130	OI	009	5/3		D10	4H3
RDD050A	I	010	7/9		RDD0AIO	5A3
RDD050B	I	210	7/23		RDD0BIO	5A5
RDD130A	I	111	7/9		RDD1AIO	4A4
RDD130B	I	311	7/23		RDD1BIO	4A5
SA05IO	I	018	TO CONN CKT		SAD0IO	3A3
SA0500	0	112	TO CONN CKT		SAD00	3H2
SA13IO	I	317	TO CONN CKT		SAD1IO	3A3
SA1300	0	312	TO CONN CKT		SAD10	3H4
SD05IO	OI	314	TO CONN CKT		TSD0IO	5A0
SD0500	OI	113	TO CONN CKT		TG.SD00	5G7
SD13IO	OI	013	TO CONN CKT		TSD1IO	4A0
SD1300	OI	213	TO CONN CKT		TG.SD10	4G8
WPAD130	0	106	4/2		WPAD10	3H3
WRTD050A	0	300	7/9 8/11-24(BTR)		D00A	5H2
WRTD050B	0	100	7/23 8/11-24(BTR)		D00B	5H3
WRTD130A	0	303	7/9 8/11-24(BTR)		D10A	4H2
WRTD130B	0	103	7/23 8/11-24(BTR)		010B	4H3
WRTMD0A	I	214	1/3		WRTMDIO	4A0
XBUSRD0F	I	IC	1/13		BUSRD0	4A8
XBUSRD1F	I	IC	1/13		BUSRD1	4A7
XCINIT1F	I	IC	6/15		INIT1	4A2
XCNLDB1F	I	IC	1/13		NLDB1	3D0
XCNLD0F	I	IC	1/13		NLD0	3D0
XCNRD1F	I	IC	1/13		NRD1	4A5
XCOMWT0F	0	IC	4/6		COMWRT0	4H1
XCREF0F	I	IC	1/13		REF0	3D0
XCSD01BF	0	IC	6/15		SD01B	5H1
XCSD01CF	0	IC	4/6		SD01C	5H0
XCSD11BF	0	IC	6/15		SD11B	4H1
XCSD11CF	0	IC	4/6		SD11C	4H1
XCSEL0F	I	IC	1/13		SEL0	4A7
XCSEL1F	I	IC	1/13		SEL1	4A5
XCTAD00F	OT I	IC	3/8 6/15		TAD00	3H2
XCTAD10F	OT I	IC	3/8 6/15		TAD10	3H3
XNITOAF	I	IC	6/15		INITOA	3D0
XRDSTB0F	I	IC	1/13		RDDSTB0	4A3
XRDSTB1F	I	IC	1/13		RDDSTB1	4A3
XSOCKB1F	I	IC	6/15		ISOCKB1	4A8
XTRED01F	I	IC	6/15		TRETD01	5A5
XTRED11F	I	IC	6/15		TRETD11	4A7

SYMBOL NO. 9

ADDRESS AND DATA REGISTERS
(BITS 04 AND 12)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.012.04	08-06	FA1060	8	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
A041A	0	204	7/1 8/11-24(BTR)		AD01A	3H1
A041B	0	004	7/15 8/11-24(BTR)		AD01B	3H2
A121A	0	201	7/1 8/11-24(BTR)		AD11A	3H4
A121B	0	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRD0A	I	301	5/4		COMPRDIO	4A6
COMWRT0A	I	101	1/3		COMWRTIO	4A2
D040	OI	006	5/2		000	5H3
D120	OI	009	5/3		010	4H3
RDD040A	I	010	7/10		RDD0AIO	5A3
RDD040B	I	210	7/24		RDD0BIO	5A5
RDD120A	I	111	7/10		RDD1AIO	4A4
RDD120B	I	311	7/24		RDD1BIO	4A5
SA04IO	I	018	TO CONN CKT		SAD0IO	3A3
SA0400	0	112	TO CONN CKT		SAD00	3H2
SA12IO	I	317	TO CONN CKT		SAD1IO	3A3
SA1200	0	312	TO CONN CKT		SAD10	3H4
SD04IO	OI	314	TO CONN CKT		TSD0IO	5A0
SD0400	OI	113	TO CONN CKT		TG.SD00	5G7
SD12IO	OI	013	TO CONN CKT		TSD1IO	4A0
SD1200	OI	213	TO CONN CKT		TG.SD10	4G8
WPAD120	0	106	4/2		WPAD10	3H3
WRTD040A	0	300	7/10 8/11-24(BTR)		D00A	5H2
WRTD040B	0	100	7/24 8/11-24(BTR)		000B	5H3
WRTD120A	0	303	7/10 8/11-24(BTR)		D10A	4H2
WRTD120B	0	103	7/24 8/11-24(BTR)		010B	4H3
WRTMD0A	I	214	1/3		WRTMDIO	4A0
XBUSRD0E	I	IC	1/14		BUSRD0	4A8
XBUSRD1E	I	IC	1/14		BUSRD1	4A7
XCINIT1E	I	IC	6/16		INIT1	4A2
XCNLDB1E	I	IC	1/14		NLDB1	3D0
XCNLD0E	I	IC	1/14		NLD0	3D0
XCNRD1E	I	IC	1/14		NRD1	4A5
XCOMWT0E	0	IC	4/7		COMWRT0	4H1
XCREF0E	I	IC	1/14		REF0	3D0
XCSD01BE	0	IC	6/16		SD01B	5H1
XCSD01CE	0	IC	4/7		SD01C	5H0
XCSD11BE	0	IC	6/16		SD11B	4H1
XCSD11CE	0	IC	4/7		SD11C	4H1
XCSEL0E	I	IC	1/14		SEL0	4A7
XCSEL1E	I	IC	1/14		SEL1	4A5
XCTAD00E	OT I	IC	3/9 6/16		TAD00	3H2
XCTAD10E	OT I	IC	3/9 6/16		TAD10	3H3
XNITOAE	I	IC	6/16		INITOA	3D0
XRDSTB0E	I	IC	1/14		RDDSTB0	4A3
XRDSTB1E	I	IC	1/14		RDDSTB1	4A3
XSOCKB1E	I	IC	6/16		ISOCKB1	4A8
XTRED01E	I	IC	6/16		TRETD01	5A5
XTRED11E	I	IC	6/16		TRETD11	4A7

PART OF FS 2
SYMBOL(S) 7 8 9

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C
ISSUE 1
BELL LABORATORIES
SD-1C902-03

PRINTED IN U. S. A.

PART OF FS 2

ADDRESS AND DATA

SYMBOL NO. 10

ADDRESS AND DATA REGISTERS
(BITS 03 AND 11)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.011.03	08-05	FA1060	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		106			WPAD10	3H3
A031A	O	204	7/1 8/11-24(BTR)		AD01A	3H1
A031B	O	004	7/15 8/11-24(BTR)		AD01B	3H2
A111A	O	201	7/1 8/11-24(BTR)		AD11A	3H4
A111B	O	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRD0A	I	301	5/4		COMPRDI0	4A6
COMWRT0A	I	101	1/3		COMWRTI0	4A2
D030	OI	006	5/2		D00	5H3
D110	OI	009	5/3		D10	4H3
RDD030A	I	010	7/11		RDD0AI0	5A3
RDD030B	I	210	7/25		RDD0BI0	5A5
RDD110A	I	111	7/11		RDD1AI0	4A4
RDD110B	I	311	7/25		RDD1BI0	4A5
SA03I0	I	018	TO CONN CKT		SAD0I0	3A3
SA0300	O	112	TO CONN CKT		SAD00	3H2
SA11I0	I	317	TO CONN CKT		SAD1I0	3A3
SA1100	O	312	TO CONN CKT		SAD10	3H4
SD03I0	OI	314	TO CONN CKT		TSD0I0	5A0
SD0300	OI	113	TO CONN CKT		TG.SD00	5G7
SD11I0	OI	013	TO CONN CKT		TSD1I0	4A0
SD1100	OI	213	TO CONN CKT		TG.SD10	4G8
WRTD030A	O	300	7/11 8/11-24(BTR)		D00A	5H2
WRTD030B	O	100	7/25 8/11-24(BTR)		D00B	5H3
WRTD110A	O	303	7/11 8/11-24(BTR)		D10A	4H2
WRTD110B	O	103	7/25 8/11-24(BTR)		D10B	4H3
WRTMD0C	I	214	1/3		WRTMDI0	4A0
XBUSRD0D	I	IC	1/15		BUSRD0	4A8
XBUSRD1D	I	IC	1/15		BUSRD1	4A7
XCINIT1D	I	IC	6/17		INIT1	4A2
XCNLDB1D	I	IC	1/15		NLDB1	3D0
XCNLD0D	I	IC	1/15		NLD0	3D0
XCNRD1D	I	IC	1/15		NRD1	4A5
XCOMWT0D	O	IC	4/8		COMWRT0	4H1
XCREF0D	I	IC	1/15		REF0	3D0
XCSD01BD	O	IC	6/17		SD01B	5H1
XCSD01CD	O	IC	4/8		SD01C	5H0
XCSD11BD	O	IC	6/17		SD11B	4H1
XCSD11CD	O	IC	4/8		SD11C	4H1
XCSEL0D	I	IC	1/15		SEL0	4A7
XCSEL1D	I	IC	1/15		SEL1	4A5
XCTAD00D	OT I	IC	3/10 6/17		TAD00	3H2
XCTAD10D	OT I	IC	3/10 6/17		TAD10	3H3
XNIT0AD	I	IC	6/17		INIT0A	3D0
XRDSTB0D	I	IC	1/15		RDDSTB0	4A3
XRDSTB1D	I	IC	1/15		RDDSTB1	4A3
XSOCKB1D	I	IC	6/17		ISOCKB1	4A8
XTRED01D	I	IC	6/17		TRET001	5A5
XTRED11D	I	IC	6/17		TRET011	4A7

SYMBOL NO. 11

ADDRESS AND DATA REGISTERS
(BITS 02 AND 10)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.D10.02	08-04	FA1060	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		106			WPAD10	3H3
A021A	O	204	7/1 8/11-24(BTR)		AD01A	3H1
A021B	O	004	7/15 8/11-24(BTR)		AD01B	3H2
A101A	O	201	7/1 8/11-24(BTR)		AD11A	3H4
A101B	O	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRD0A	I	301	5/4		COMPRDI0	4A6
COMWRT0A	I	101	1/3		COMWRTI0	4A2
D020	OI	006	5/2		D00	5H3
D100	OI	009	5/3		D10	4H3
RDD020A	I	010	7/12		RDD0AI0	5A3
RDD020B	I	210	7/26		RDD0BI0	5A5
RDD100A	I	111	7/12		RDD1AI0	4A4
RDD100B	I	311	7/26		RDD1BI0	4A5
SA02I0	I	018	TO CONN CKT		SAD0I0	3A3
SA0200	O	112	TO CONN CKT		SAD00	3H2
SA10I0	I	317	TO CONN CKT		SAD1I0	3A3
SA1000	O	312	TO CONN CKT		SAD10	3H4
SD02I0	OI	314	TO CONN CKT		TSD0I0	5A0
SD0200	OI	113	TO CONN CKT		TG.SD00	5G7
SD10I0	OI	013	TO CONN CKT		TSD1I0	4A0
SD1000	OI	213	TO CONN CKT		TG.SD10	4G8
WRTD020A	O	300	7/12 8/11-24(BTR)		D00A	5H2
WRTD020B	O	100	7/26 8/11-24(BTR)		D00B	5H3
WRTD100A	O	303	7/12 8/11-24(BTR)		D10A	4H2
WRTD100B	O	103	7/26 8/11-24(BTR)		D10B	4H3
WRTMD0C	I	214	1/3		WRTMDI0	4A0
XBUSRD0C	I	IC	1/16		BUSRD0	4A8
XBUSRD1C	I	IC	1/16		BUSRD1	4A7
XCINIT1C	I	IC	6/18		INIT1	4A2
XCNLDB1C	I	IC	1/16		NLDB1	3D0
XCNLD0C	I	IC	1/16		NLD0	3D0
XCNRD1C	I	IC	1/16		NRD1	4A5
XCOMWT0C	O	IC	4/9		COMWRT0	4H1
XCREF0C	I	IC	1/16		REF0	3D0
XCSD01BC	O	IC	6/18		SD01B	5H1
XCSD01CC	O	IC	4/9		SD01C	5H0
XCSD11BC	O	IC	6/18		SD11B	4H1
XCSD11CC	O	IC	4/9		SD11C	4H1
XCSEL0C	I	IC	1/16		SEL0	4A7
XCSEL1C	I	IC	1/16		SEL1	4A5
XCTAD00C	OT I	IC	3/11 6/18		TAD00	3H2
XCTAD10C	OT I	IC	3/11 6/18		TAD10	3H3
XNIT0AC	I	IC	6/18		INIT0A	3D0
XRDSTB0C	I	IC	1/16		RDDSTB0	4A3
XRDSTB1C	I	IC	1/16		RDDSTB1	4A3
XSOCKB1C	I	IC	6/18		ISOCKB1	4A8
XTRED01C	I	IC	6/18		TRET001	5A5
XTRED11C	I	IC	6/18		TRET011	4A7

SYMBOL NO. 12

ADDRESS AND DATA REGISTERS
(BITS 01 AND 09)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.D09.01	08-03	FA1060	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		106			WPAD10	3H3
A011A	O	204	7/1 8/11-24(BTR)		AD01A	3H1
A011B	O	004	7/15 8/11-24(BTR)		AD01B	3H2
A091A	O	201	7/1 8/11-24(BTR)		AD11A	3H4
A091B	O	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRD0A	I	301	5/4		COMPRDI0	4A6
COMWRT0A	I	101	1/3		COMWRTI0	4A2
D010	OI	006	5/2		D00	5H3
D090	OI	009	5/3		D10	4H3
RDD010A	I	010	7/13		RDD0AI0	5A3
RDD010B	I	210	7/27		RDD0BI0	5A5
RDD090A	I	111	7/13		RDD1AI0	4A4
RDD090B	I	311	7/27		RDD1BI0	4A5
SA01I0	I	018	TO CONN CKT		SAD0I0	3A3
SA0100	O	112	TO CONN CKT		SAD00	3H2
SA09I0	I	317	TO CONN CKT		SAD1I0	3A3
SA0900	O	312	TO CONN CKT		SAD10	3H4
SD01I0	OI	314	TO CONN CKT		TSD0I0	5A0
SD0100	OI	113	TO CONN CKT		TG.SD00	5G7
SD09I0	OI	013	TO CONN CKT		TSD1I0	4A0
SD0900	OI	213	TO CONN CKT		TG.SD10	4G8
WRTD010A	O	300	7/13 8/11-24(BTR)		D00A	5H2
WRTD010B	O	100	7/27 8/11-24(BTR)		D00B	5H3
WRTD090A	O	303	7/13 8/11-24(BTR)		D10A	4H2
WRTD090B	O	103	7/27 8/11-24(BTR)		D10B	4H3
WRTMD0C	I	214	1/3		WRTMDI0	4A0
XBUSRD0B	I	IC	1/17		BUSRD0	4A8
XBUSRD1B	I	IC	1/17		BUSRD1	4A7
XCINIT1B	I	IC	6/19		INIT1	4A2
XCNLDB1B	I	IC	1/17		NLDB1	3D0
XCNLD0B	I	IC	1/17		NLD0	3D0
XCNRD1B	I	IC	1/17		NRD1	4A5
XCOMWT0B	O	IC	4/10		COMWRT0	4H1
XCREF0B	I	IC	1/17		REF0	3D0
XCSD01BB	O	IC	6/19		SD01B	5H1
XCSD01CB	O	IC	4/10		SD01C	5H0
XCSD11BB	O	IC	6/19		SD11B	4H1
XCSD11CB	O	IC	4/10		SD11C	4H1
XCSEL0B	I	IC	1/17		SEL0	4A7
XCSEL1B	I	IC	1/17		SEL1	4A5
XCTAD00B	OT I	IC	3/12 6/19		TAD00	3H2
XCTAD10B	OT I	IC	3/12 6/19		TAD10	3H3
XNIT0AB	I	IC	6/19		INIT0A	3D0
XRDSTB0B	I	IC	1/17		RDDSTB0	4A3
XRDSTB1B	I	IC	1/17		RDDSTB1	4A3
XSOCKB1B	I	IC	6/19		ISOCKB1	4A8
XTRED01B	I	IC	6/19		TRET001	5A5
XTRED11B	I	IC	6/19		TRET011	4A7

PART OF FS 2
SYMBOL(S) 10 11 12

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
52

ISSUE
1

BELL LABORATORIES

SD-1C902-03

B2C

PRINTED IN U.S.A.

03/06

PART OF FS 2

ADDRESS AND DATA

SYMBOL NO. 13

ADDRESS AND DATA REGISTERS
(BITS 00 AND 08)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A.D08.00	08-02	FA1060	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		106			WPAD10	3H3
A001A	0	204	7/1 8/11-24(BTR)		AD01A	3H1
A001B	0	004	7/15 8/11-24(BTR)		AD01B	3H2
A081A	0	201	7/1 8/11-24(BTR)		AD11A	3H4
A081B	0	001	7/15 8/11-24(BTR)		AD11B	3H5
COMPRD0A	I	301	5/4		COMPRD10	4A6
COMWRT0A	I	101	1/3		COMWRT10	4A2
D000	OI	006	5/2		D00	5H3
D080	OI	009	5/3		D10	4H3
RDD000A	I	010	7/14		RDD0A10	5A3
RDD000B	I	210	7/28		RDD0B10	5A5
RDD080A	I	111	7/14		RDD1A10	4A4
RDD080B	I	311	7/28		RDD1B10	4A5
SA0010	I	018	TO CONN CKT		SAD010	3A3
SA0000	0	112	TO CONN CKT		SAD00	3H2
SA0810	I	317	TO CONN CKT		SAD110	3A3
SA0800	0	312	TO CONN CKT		SAD10	3H4
SD0010	OI	314	TO CONN CKT		TSD010	5A0
SD0000	OI	113	TO CONN CKT		TG.SD00	5G7
SD0810	OI	013	TO CONN CKT		TSD110	4A0
SD0800	OI	213	TO CONN CKT		TG.SD10	4G8
WRTD000A	0	300	7/14 8/11-24(BTR)		D00A	5H2
WRTD000B	0	100	7/28 8/11-24(BTR)		D00B	5H3
WRTD080A	0	303	7/14 8/11-24(BTR)		D10A	4H2
WRTD080B	0	103	7/28 8/11-24(BTR)		D10B	4H3
[illegible]	I	214	1/3		WRTMD10	4A0
*BUSRD0A	I	IC	1/18		BUSRD0	4A8
*BUSRD1A	I	IC	1/18		BUSRD1	4A7
*CINIT1A	I	IC	6/20		INIT1	4A2
*CNLDB1A	I	IC	1/18		NLDB1	300
*CNLD0A	I	IC	1/18		NLD0	300
*CNRD1A	I	IC	1/18		NRD1	4A5
*COMWT0A	0	IC	4/11		COMWRT0	4H1
*CREF0A	I	IC	1/18		REF0	300
*CSD01BA	0	IC	6/20		SD01B	5H1
*CSD01CA	0	IC	4/11		SD01C	5H0
*CSD11BA	0	IC	6/20		SD11B	4H1
*CSD11CA	0	IC	4/11		SD11C	4H1
*CSEL0A	I	IC	1/18		SEL0	4A7
*CTFL1A	I	IC	1/18		CFL1	4A5
*CTAD00A	OT I	IC	3/13 6/20		TAD00	3H2
*CTAD10A	OT I	IC	3/13 6/20		TAD10	3H3
[illegible]	I	IC	[illegible]		[illegible]	[illegible]
[illegible]	I	IC	[illegible]		[illegible]	[illegible]
[illegible]	I	IC	[illegible]		[illegible]	[illegible]
[illegible]	I	IC	[illegible]		[illegible]	[illegible]
[illegible]	I	IC	[illegible]		[illegible]	[illegible]
[illegible]	I	IC	6/20		[illegible]	[illegible]

SYMBOL NO. 14

ADDRESS BIT 17

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ABIT17	08-23	FC203	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
A171A	0	210	7/1 8/11-24(BTR)		AD171A	2E3
A171B	0	311	7/15 8/11-24(BTR)		AD171B	2E3
LAD170	0	304	4/1,5/19		AD170	2E3
SA1710	I	005	TO CONN CKT		SAD1710	2B3
SA1700	0	203	TO CONN CKT		SAD170	2E3
XINIT0	I	IC	6/1		INIT0	2E2
XREF0	I	IC	3/4		REF0	2C3
XXNLDB1	I	IC	1/20		NLDB1	2C3
XXNLD0	I	IC	1/20		NLD0	2C3

SYMBOL NO. 15

ADDRESS BIT 19 AND
MODIFIED ADDRESS PARITY HIGH GENERATOR

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
A19.MPH	08-17	FA1062	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CHSAD0	0	100	5/18		SA190181	3H3
LADPH0	I	102	2/17		LADPHI0	3A0
MADPH1	0	004	2/17		1MPH1	3H5
SAD180	OI	300	TO CONN CKT		SAD180	3H3
SAD181	I	002	2/16		SAD1811	3A3
SAD190	OI	311	TO CONN CKT		SAD190	3H6
SAD191	0	218	2/16 TO CONN CKT		SAD191	3H7
SA1910	I	118	TO CONN CKT		SAD1910	3A1
SA1900	0	211	TO CONN CKT		SAD1900	3H1
XCRMB1	I	IC	1/5		RMB1	3A5
XINIT00	I	IC	6/7		INIT00	3A4
XNLDB1	I	IC	1/5		NLDB1	3A4
XNLD0	I	IC	1/5		NLD0	3A4

SYMBOL NO. 16

ADDRESS BIT 18

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ABIT18	08-18	FA1071	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
SAD0180	0	318	TO CONN CKT		LSAD180	8H1
SAD0190	0	107	TO CONN CKT		SAD190	8H2
SAD181	0	112	2/15 TO CONN CKT		LSAD181	8H1
SAD191	I	307	2/15		SAD1911	8A2
SA1810	I	213	TO CONN CKT		SAD1810	8A1
SA1800	0	015	TO CONN CKT		SAD180	8H2
*CINITCA	I	IC	6/4		INITOA	8F0
XCNLDB0	I	IC	1/1		NLDB0	8E0
XCNLDB1	I	IC	1/1		NLDB1	8E0

SYMBOL NO. 17

ADDRESS BIT PH AND
MODIFIED ADDRESS PARITY DRIVER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ADPH.PDR	08-19	FA1066	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		012			GRD012	2A8
		015			GRD015	2A8
		017			GRD017	2A8
		111			GRD111	2A7
		114			GRD114	2A7
		117			GRD117	2A7
		215			GRD215	2A6
		217			GRD217	2A6
		311			GRD311	2A5
		314			GRD314	2A5
		316			GRD316	2A5
		317			GRD317	2A5
AP17.81A	0	201	7/1 8/11-24(BTR)		ADMPH1A	2H5
AP17.81B	0	101	7/15 8/11-24(BTR)		ADMPH1B	2H4
LADPH0	0	218	2/15,5/20		LADPH0	2H3
MADPH1	I	104	2/15		MPHI1	2A4
NLD0	I	006	1/1		NLD10	2A3
SAPHI0	I	011	TO CONN CKT		SADPHI0	2A2
SAPHO0	0	007	TO CONN CKT		GSADPH0	2H2
XNITCA	I	IC	6/6		INITOA	2E0
3T046B	GRD	0G0		202	GRD	
	GRD	005		202	GRD005	2A9
	GRD	008		202	GRD008	2A8
	GRD	102		202	GRD102	2A8
	GRD	105		202	GRD105	2A8
	GRD	107		202	GRD107	2A7
	GRD	2G0		202	GRD	
	GRD	200		202	GRD	
	GRD	202		202	GRD202	2A7
	GRD	205		202	GRD205	2A7
	GRD	207		202	GRD207	2A6
	GRD	213		202		
	GRD	302		202	GRD302	2A6

SYMBOL NO. 17 (CONT)

ADDRESS BIT PH AND
MODIFIED ADDRESS PARITY DRIVER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ADPH.PDR	08-19	FA1066	A	

FS INFO					CP INFO
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD
3T046B	GRD	304		202	GRD304
	GRD	307		202	GRD307
	GRD	319		202	GRD
3VG0819T	GRD	319	2/17		GRD
	GRD	019	3/1		
3V046B	PWR	000		202	-5
	PWR	119		202	-5

PART OF FS 2
SYMBOL(S) 13 14 15 16 17

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES | SD-1C902-03

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PART OF FS 2

ADDRESS AND DATA

SYMBOL NO. 18

2B PARITY CONVERTER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
2BPARCNV	08-14	FA1067	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
D160	I	018	2/5		SD1610	4A1
D170	I	316	2/4		SD1710	4A1
D180	I	116	2/3		SD1810	4A
D190	I	016	2/2		SD1910	4A0
D200	I	307	2/5		SD2010	3A1
D210	I	308	2/4		SD2110	3A1
D220	I	208	2/3		SD2210	3A0
D230	I	008	2/2		SD2310	3A0
NOZDPHI0	OI	305	TO CONN CKT		TSDPXI0	6A0
NOZDPHO0	OT	IC216	2/18		TGSDPX0	6H4
	OI	216	6/22		TGSDPX0	6H4
			TO CONN CKT			
NOZDPLI0	OI	207	TO CONN CKT		TSDPYZI0	6A4
NOZDPLO0	OT	IC209	2/18		TGSDPYZ0	6A8
	OI	209	6/22		TGSDPYZ0	6G8
			TO CONN CKT			
SDEPHI0	OI	306	TO CONN CKT		TSDPWI0	5AG
SDEPHO0	OT	IC015	2/18		TGSDPW0	5A4
	OI	015	6/22		TGSDPW0	5H4
			TO CONN CKT			
SDPHI0	OI	002	2/1		DPH0	6H1
SDPLI0	OI	006	2/1		DPL0	6H6
XBUSROB0	I	IC	1/19		BUSROB0	6D0
XBUSROB1	I	IC	1/19		BUSROB1	5F0
XCBUSRD0	I	IC	1/19		BUSRD0	5A4
XCBUSRD1	I	IC	1/19		BUSRD1	5E0
XSDPW0	O	IC	5/6		SDPW0	5H2
XSDPW1A	O	IC	5/6		SDPW1A	5H1
XSDPW1B	O	IC	6/22		SDPW1B	5H1
XSDPX1B	O	IC	6/22		SDPX1B	6H1
XSDPYZ1B	O	IC	6/22		SDPYZ1B	6H5
XSEL0	I	IC	1/19		SEL0	5E0
XSEL1	I	IC	1/19		SEL1	5F0
XTPWHEV0	O	IC	5/6		TPWHEV0	3H6
XTPWHOD0	O	IC	5/6		TPWHOD0	3H4
XTPWLEV0	O	IC	5/6		TPWLEV0	4H6
XTPWLOD0	O	IC	5/6		TPWLOD0	4H6

PART OF FS 2
SYMBOL(S) 18

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE CZ
ISSUE 1

BELL LABORATORIES
SD-1C902-03
B2CF

PRINTED IN U. S. A.

PART OF FS 2

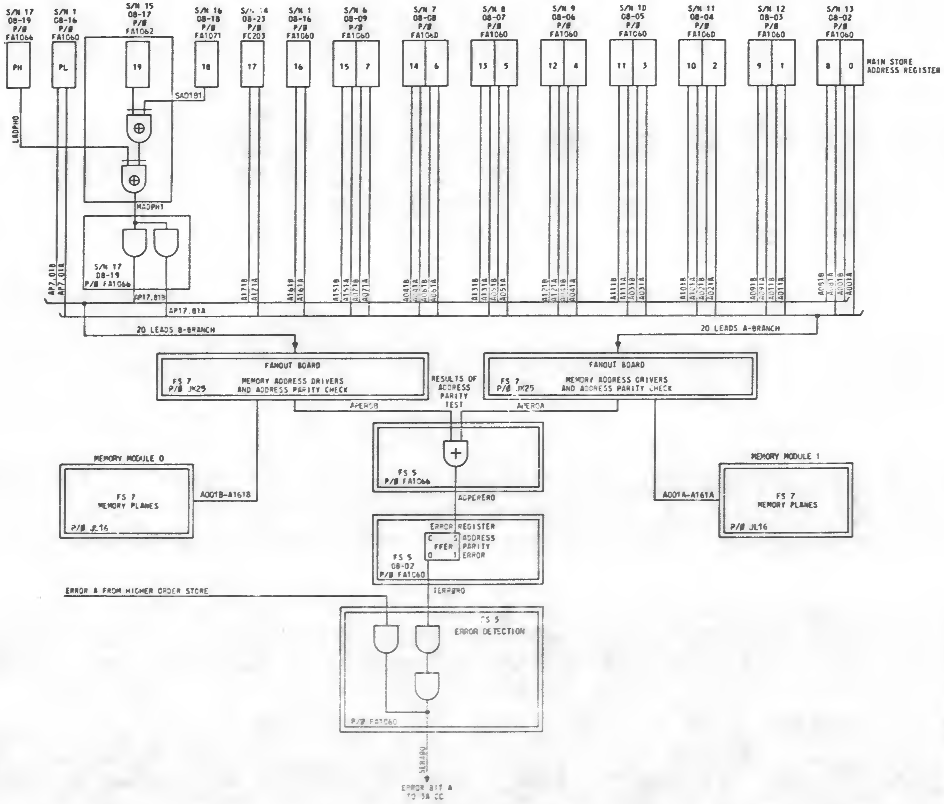
ADDRESS AND DATA

COMPOSITE DIAGRAM I

ADDRESS FUNCTIONAL BLOCK DIAGRAM

NOTES:

1. S/N DENOTES SYMBOL NUMBER.



PART OF FS 2
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY

2

BELL LABORATORIES

SD-IC902-03

PART OF FS 2

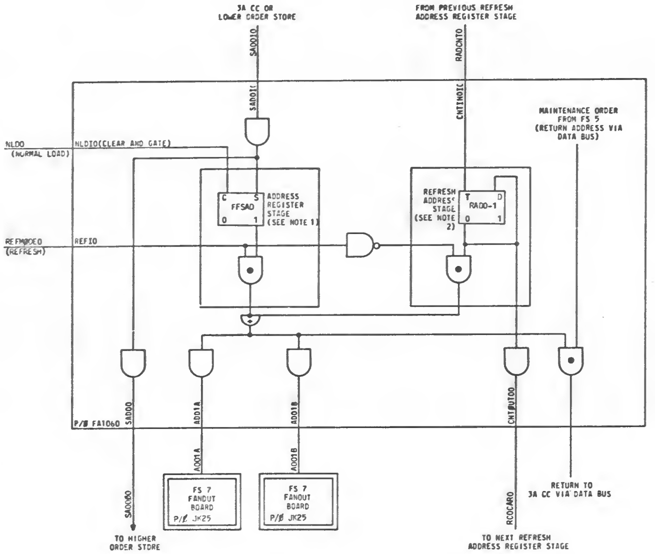
ADDRESS AND DATA

COMPOSITE DIAGRAM 2

ADDRESS REGISTER SINGLE STAGE

NOTES:

1. SHOWN IS A SINGLE STAGE OF THE ADDRESS REGISTER FOR TYPICAL EXAMPLE ONLY.
2. NOT ALL AVAILABLE REFRESH ADDRESS REGISTER STAGES ARE USED AND NOT ALL MAIN STORE ADDRESS REGISTER STAGES HAVE A COMPANION REFRESH ADDRESS STAGE. SEE FS 3 FOR COMPLETE INTERCONNECTION OF THE REFRESH ADDRESS REGISTER



PART OF FS 2
COMPOSITE DIAGRAM 2

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
65

BELL LABORATORIES
SD-1C902-03

PART OF FS 2

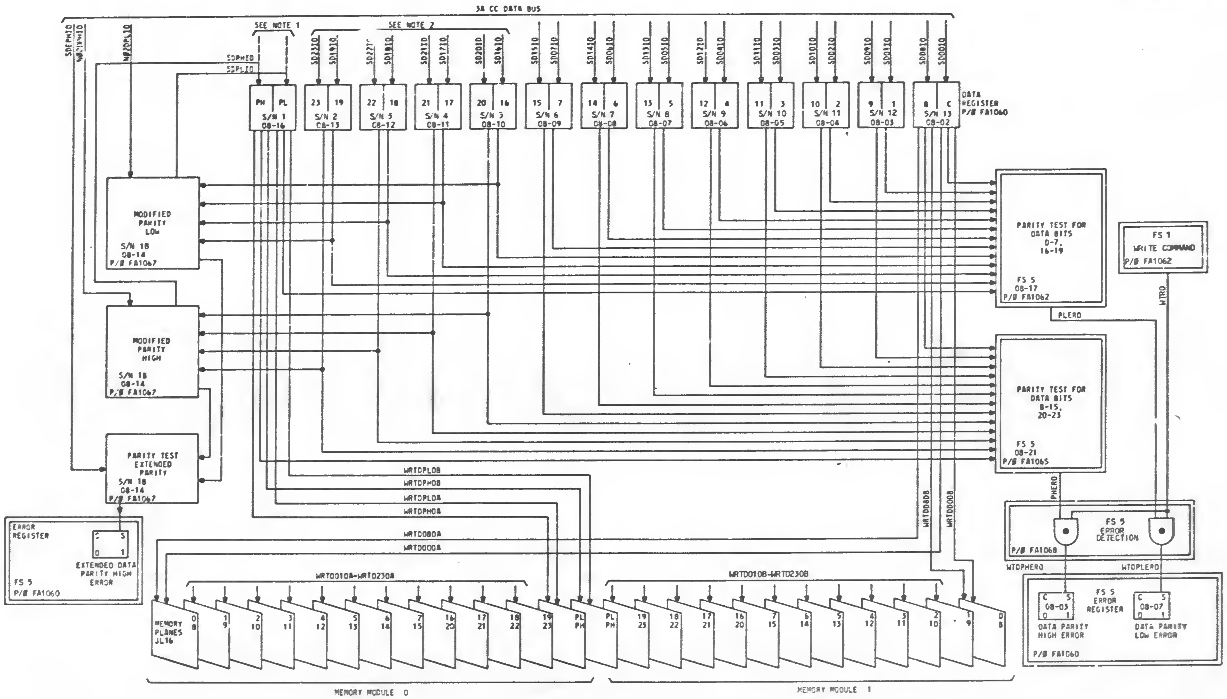
ADDRESS AND DATA

COMPOSITE DIAGRAM 3

DATA FUNCTIONAL BLOCK DIAGRAM (WRITE COMMAND)

NOTES:

1. PARITY LOW AND PARITY HIGH ARE TAKEN DIRECTLY FROM THE DATA BUS IN NARROW STORE (18 BITS).
2. ADDITIONAL DATA BITS FOR WIDE STORE (26 BITS).
3. S/N DENOTES SYMBOL NUMBER.



PART OF FS 2
COMPOSITE DIAGRAM 3

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
65

BELL LABORATORIES | SD-IC902-03 | B23C

PART OF FS 2

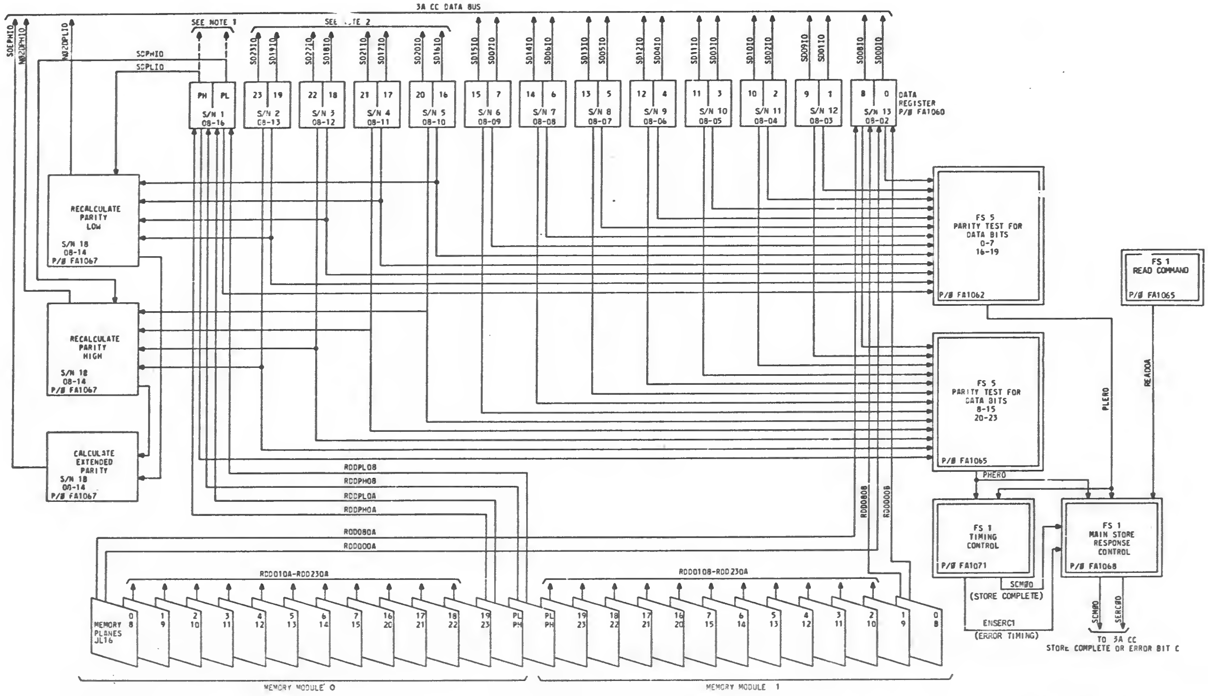
ADDRESS AND DATA

COMPOSITE DIAGRAM 4

DATA FUNCTIONAL BLOCK DIAGRAM (READ COMMAND)

NOTES:

1. PARITY LOW AND PARITY HIGH GO DIRECTLY TO THE DATA BUS IN NARROW STORES (18 BITS).
2. ADDITIONAL DATA BITS FOR WIDE STORE (26 BITS).
3. S/N DENOTES SYMBOL NUMBER.



PART OF FS 2
COMPOSITE DIAGRAM 4

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

BELL LABORATORIES

SD-IC902-03

PART OF FS 2

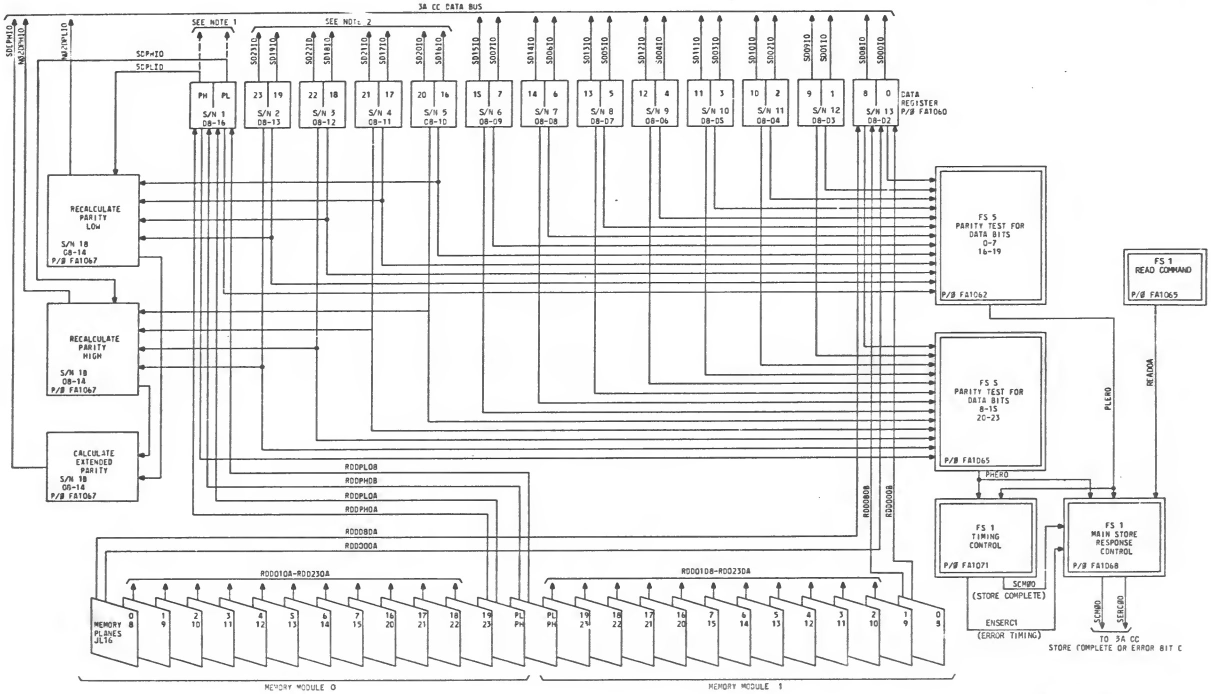
ADDRESS AND DATA

COMPOSITE DIAGRAM 4

DATA FUNCTIONAL BLOCK DIAGRAM (READ COMMAND)

NOTES:

1. PARITY LOW AND PARITY HIGH GO DIRECTLY TO THE DATA BUS IN NARROW STORES (18 BITS).
2. ADDITIONAL DATA BITS FOR WIDE STORE (26 BITS).
3. S/N DENOTES SYMBOL NUMBER.



PART OF FS 2
COMPOSITE DIAGRAM 4

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 6S	
BELL LABORATORIES	SD-1C902-03	B2

PART OF FS 2

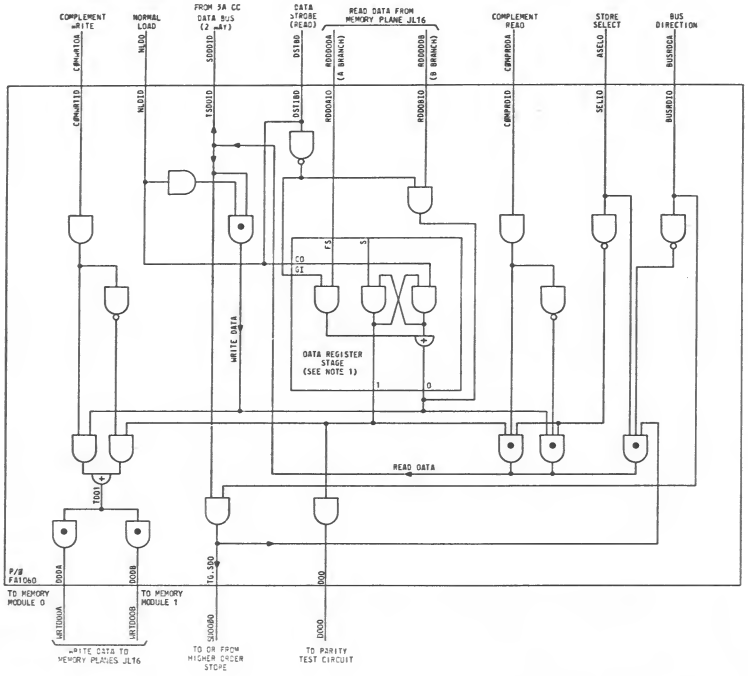
ADDRESS AND DATA

COMPOSITE DIAGRAM 5

DATA REGISTER SINGLE STAGE

NOTES:

1. SINGLE STAGE OF THE DATA REGISTER FOR TYPICAL EXAMPLE ONLY. NOT ALL GATES AND LOGIC ARE SHOWN IN A CORRECT FUNCTIONAL CONFIGURATION.



PART OF FS 2
COMPOSITE DIAGRAM 5

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 65	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B2GE

PART OF FS 2

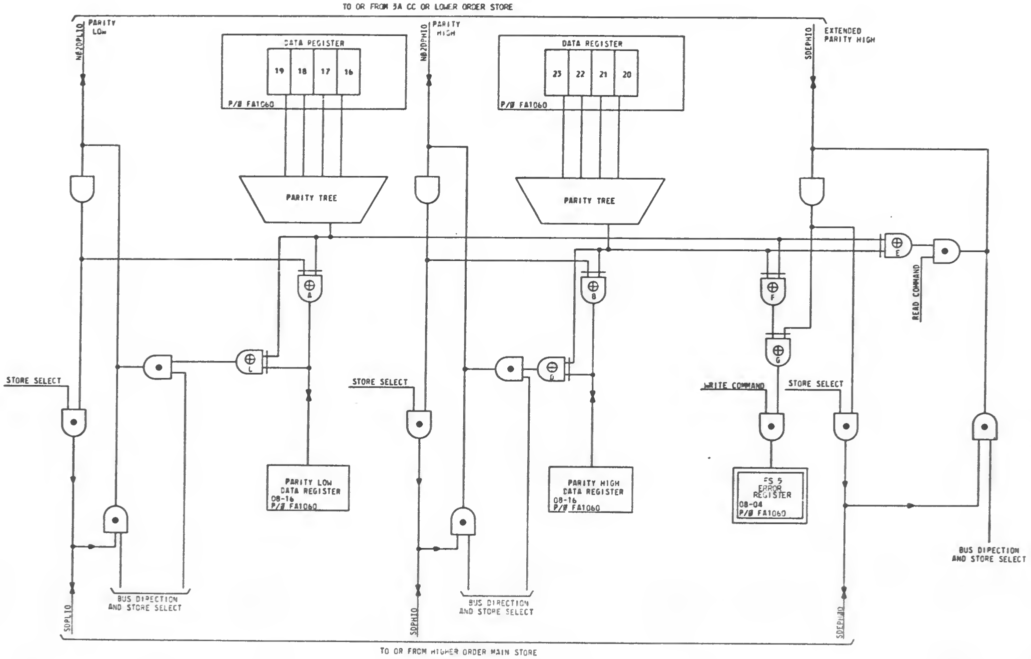
ADDRESS AND DATA

COMPOSITE DIAGRAM 6

NO.2B ESS PARITY CONTROL

NOTES:

1. ALL CIRCUITRY UNLESS OTHERWISE [illegible] ON CIRCUIT PACK FA1067, SYM NO. [illegible]
2. THE LETTER DESIGNATION ON EACH [illegible] COVERED IN THE MAIN STORE CIRCUIT [illegible]



PART OF FS 2
COMPOSITE DIAGRAM 6

MAIN STORE CONTROLLER AND MEMORY

[illegible]
03

BELL LABORATORIES

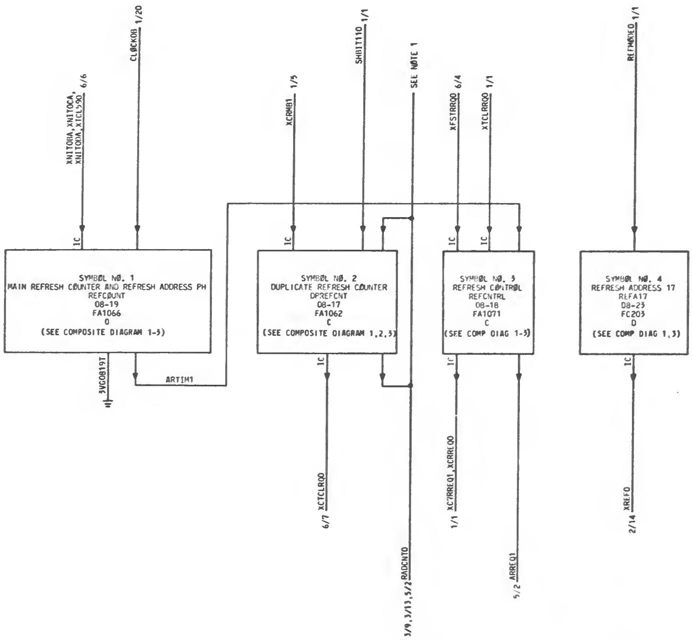
SD-1C902-03

PART OF FS 3

REFRESH CONTROL
INTERCONNECTION AND FLOW DIAGRAM

NOTES·

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (OI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



PART OF FS 3
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

|

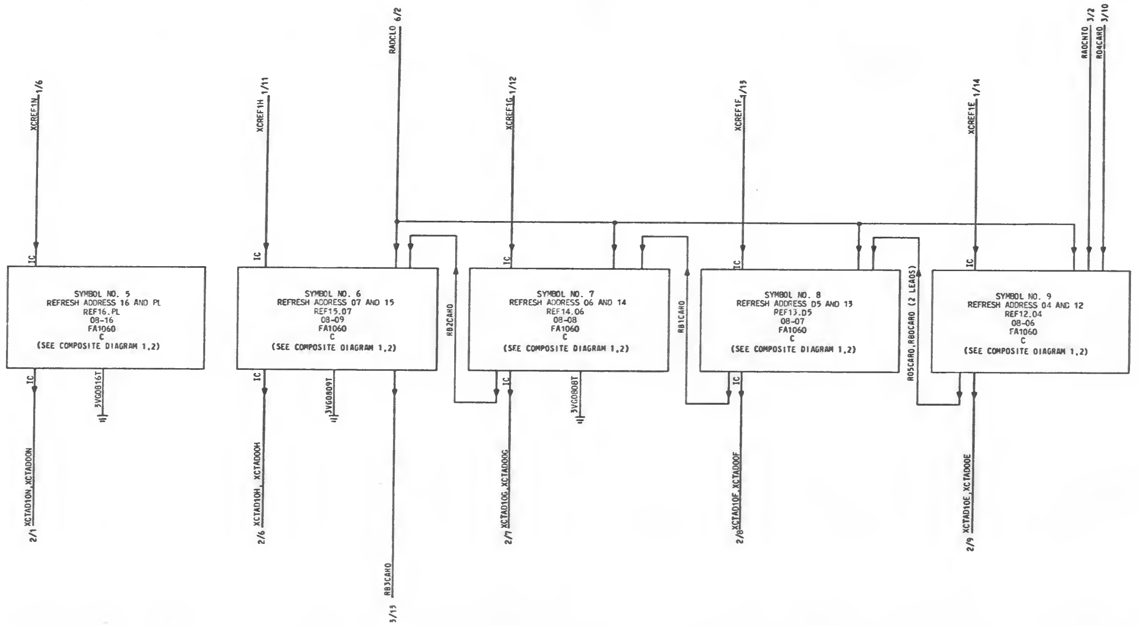
BELL LABORATORIES

SD-1C902-03

B3AA

PART OF FS 3

REFRESH CONTROL
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 3
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

ISSUE
1

BELL LABORATORIES

SD-1C902-03

B3

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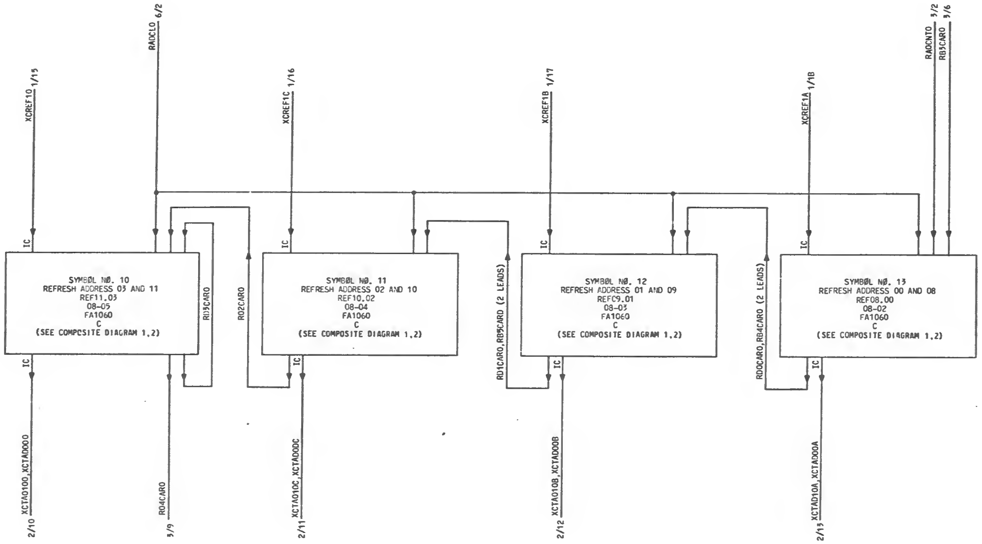
PART OF FS 3

REFRESH CØNTRØL
INTERCØNNECTIØN AND FLØW DIAGRAM



PART ØF FS 3
INTERCØNNECTIØN AND FLØW DIAGRAM

MAIN STØRE CØNTRØLLER AND MEMØRY

DWG SIZE
65

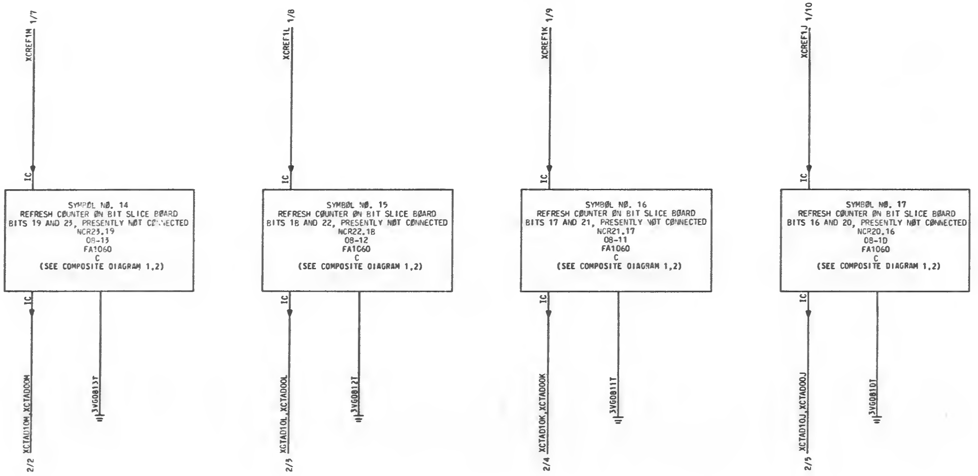
BELL LABORATORIES

SD-1C902-03

B3

PART OF FS 3

REFRESH CØNTRØL
INTERCØNNECTIØN AND FLØW DIAGRAM


XCREF1M 1/7
IC
SYMPØL NØ. 14
REFRESH CØUNTER ØN BIT SLICE BØARD
BITS 19 AND 23, PRESENTLY NØT CØNNECTED
NCR23.19
08-13
FA1060
C
(SEE COMPOSITE DIAGRAM 1,2)
IC
XCTAD10M,XCTAD00M
2/2
3VG0813T
XCREF1L 1/8
IC
SYMBØL NØ. 15
REFRESH CØUNTER ØN BIT SLICE BØARD
BITS 18 AND 22, PRESENTLY NØT CØNNECTED
NCR22.18
08-12
FA1C60
C
(SEE COMPOSITE DIAGRAM 1,2)
IC
XCTAD10L,XCTAD00L
2/3
3VG0812T
XCREF1K 1/9
IC
SYMBØL NØ. 16
REFRESH CØUNTER ØN BIT SLICE BØARD
BITS 17 AND 21, PRESENTLY NØT CØNNECTED
NCR21.17
08-11
FA1060
C
(SEE COMPOSITE DIAGRAM 1,2)
IC
XCTAD10K,XCTAD00K
2/4
3VG0811T
XCREF1J 1/10
IC
SYMBØL NØ. 17
REFRESH CØUNTER ØN BIT SLICE BØARD
BITS 16 AND 20, PRESENTLY NØT CØNNECTED
NCR20.16
08-1D
FA1060
C
(SEE COMPOSITE DIAGRAM 1,2)
IC
XCTAD10J,XCTAD00J
2/5
3VG0810T

PART OF FS 3
INTERCØNNECTION AND FLØW DIAGRAM

MAIN STØRE CØNTRØLLER AND MEMØRY

DWG SIZE
6S

BELL LABORATORIES

SD-1C902-03

B3AD

PART OF FS 3

REFRESH CONTROL

SYMBOL NO. 1

MAIN REFRESH COUNTER AND REFRESH ADDRESS PH

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REFCOUNT	08-19	FA1066	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		002			CNT91	7H1
		110			CNT501	7G8
ARTIM1	0	301	3/3		CNT81	7H
CLOCK0B	I	305	1/20		CKI0	8A7
XNIT0BA	I	IC	6/6		INIT0B	7E0
XNIT0CA	I	IC	6/6		INIT0C	7E0
XNIT00A	I	IC	6/6		INIT0D	8D0
XTCL590	I	IC	6/6		TCL590	7E0
3VG0819T	I	212	2/17		CNTTSTI0	7A1

SYMBOL NO. 2

DURLICATE REFRESH COUNTER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DPREFCNT	08-17	FA1062	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
RADCNT0	OI	304	3/9,3/13 5/2		RADCNT0	4H0
	0	IC304	3/2		RADCNT0	4H1
SHBIT110	I	309	1/1		RMADCTI1	4A1
XCRMB1	I	IC	1/5		RMB1	4A0
XCTCLRDG	OT	IC	6/7		TCLRR00	4E1

SYMBOL NO. 3

REFRESH CONTROL

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REFCNTRL	08-18	FA1071	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
APREO1	0	219	5/2		PREOB1	8H4
ARTIM1	I	108	3/1		RTIMI1	8A4
XCPREG0	0	IC	1/1		RREG0	8H4
XC7PREO1	0	IC	1/1		7RREO1	8G3
XFSTPRG0	I	IC	6/4		FSTPRG0	8A3
XTCLPRG0	I	IC	1/1		TCLPRG0	8A4

SYMBOL NO. 4

REFRESH ADDRESS 17

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PEFA17	08-23	FC203	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
REFMODE0	I	002	1/1		REFI0	2B4
XREF0	0	IC	2/14		REF0	204

SYMBOL NO. 5

REFRESH ADDRESS 16 AND RL

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF16.PL	08-16	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		107			CNTOUT00	6H4
		207			CNTOUT10	6H2
XCREF1N	I	IC	1/6		REF1	6D0
XCTAD00N	OT	IC	2/1		TAD00	6F5
XCTAD10N	OT	IC	2/1		TAD10	6F0
3VG0816T	I	014	1/6		RADCLI0	6A2
	I	015	1/6		CNTIN0I0	6A4
	I	215	1/6		CNTIN1I0	6A1

SYMBOL NO. 6

REFRESH ADDRESS 07 AND 15

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF15.07	08-09	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		207			CNTOUT10	6H2
RADCL0	I	014	6/2		RADCLI0	6A2
RB2CAR0	I	015	3/7		CNTIN0I0	6A4
RB3CAR0	0	107	3/13		CNTOUT00	6H4
XCREF1H	I	IC	1/11		REF1	6D0
XCTAD00H	OT	IC	2/6		TAD00	6F5
XCTAD10H	OT	IC	2/6		TAD10	6F0
3VG0809T	I	215	1/11		CNTIN1I0	6A1

SYMBOL NO. 7

REFRESH ADDRESS 06 AND 14

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF14.06	08-08	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		207			CNTOUT10	6H2
RADCL0	I	014	6/2		RADCLI0	6A2
RB1CAR0	I	015	3/8		CNTIN0I0	6A4
RB2CAR0	0	107	3/6		CNTOUT00	6H4
XCREF1G	I	IC	1/12		REF1	6D0
XCTAD00G	OT	IC	2/7		TAD00	6F5
XCTAD10G	OT	IC	2/7		TAD10	6F0
3VG0808T	I	215	1/12		CNTIN1I0	6A1

SYMBOL NO. 8

REFRESH ADDRESS 05 AND 13

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF13.05	08-07	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		207			CNTOUT10	6H2
RADCL0	I	014	6/2		RADCLI0	6A2
RB0CAR0	I	015	3/9		CNTIN0I0	6A4
RB1CAR0	0	107	3/7		CNTOUT00	6H4
RD5CAR0	I	215	3/9		CNTIN1I0	6A1
XCREF1F	I	IC	1/13		REF1	6D0
XCTAD00F	OT	IC	2/8		TAD00	6F5
XCTAD10F	OT	IC	2/8		TAD10	6F0

SYMBOL NO. 9

REFRESH ADDRESS 04 AND 12

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF12.04	08-06	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
RADCL0	I	014	6/2		RADCLI0	6A2
RADCNT0	I	015	3/2		CNTIN0I0	6A4
RB0CAR0	0	107	3/8		CNTOUT00	6H4
RO4CAR0	I	215	3/10		CNTIN1I0	6A1
RD5CAR0	0	207	3/8		CNTOUT10	6H2
XCREF1E	I	IC	1/14		REF1	6D0
XCTAD00E	OT	IC	2/9		TAD00	6F5
XCTAD10E	OT	IC	2/9		TAD10	6F0

SYMBOL NO. 10

REFRESH ADDRESS 03 AND 11

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF11.03	08-05	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
RADCL0	I	014	6/2		RADCLI0	6A2
RD2CAR0	I	015	3/11		CNTIN0I0	6A4
RD3CAR0	0	107			CNTOUT00	6H4
	I	215			CNTIN1I0	6A1
RO4CAR0	0	207	3/9		CNTOUT10	6H2
XCREF1D	I	IC	1/15		REF1	6D0
XCTAD00D	OT	IC	2/10		TAD00	6F5
XCTAD10D	OT	IC	2/10		TAD10	6F0

SYMBOL NO. 11

REFRESH ADDRESS 02 AND 10

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF10.02	08-04	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		207			CNTOUT10	6H2
RADCL0	I	014	6/2		RADCLI0	6A2
RB5CAR0	I	215	3/12		CNTIN1I0	6A1
RD1CAR0	I	015	3/12		CNTIN0I0	6A4
RD2CAR0	0	107	3/10		CNTOUT00	6H4
XCREF1C	I	IC	1/16		REF1	6D0
XCTAD00C	OT	IC	2/11		TAD00	6F5
XCTAD10C	OT	IC	2/11		TAD10	6F0

PART OF FS 3
SYMBOL(S) 1 2 3 4 5 6 7 8 9 10 11

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE CZ
1

BELL LABORATORIES
SD-1C902-03
B3CA

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PART OF FS 3

REFRESH CONTROL

SYMBOL NO. 12

REFRESH ADDRESS 01 AND 09

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF09.01	08-03	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
RADCLO	I	014	6/2		RADCLIO	6A2
RB4CAR0	I	215	3/13		CNTIN1IO	6A1
RB5CAR0	0	207	3/11		CNTOUT10	6H.
RD0CAR0	I	015	3/13		CNTIN0IO	6A4
RD1CAR0	0	107	3/11		CNTOUT00	6H4
XCREF1B	I	IC	1/17		REF1	6D0
XCTAD00B	OT	IC	2/12		TAD00	6F5
XCTAD10B	DT	IC	2/12		TAD10	6F0

SYMBOL NO. 13

REFRESH ADDRESS 00 AND 08

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
REF08.00	08-02	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
RADCLO	I	014	6/2		RADCLIO	6A2
RADCNTO	I	015	3/2		CNTIN0IO	6A4
RB3CAR0	I	215	3/6		CNTIN1IO	6A1
RB4CAR0	D	207	3/12		CNTOUT10	6H2
RD0CAR0	0	107	3/12		CNTOUT00	6H4
XCREF1A	I	IC	1/18		REF1	6D0
XCTAD00A	OT	IC	2/13		TAD00	6F5
XCTAD10A	OT	IC	2/13		TAD10	6F0

SYMBOL NO. 14

REFRESH COUNTER ON BIT SLICE BOARD
BITS 19 AND 23, PRESENTLY NOT CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCR23.19	08-13	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		107			CNTOUT00	6H4
		207			CNTOUT10	6H2
XCREF1M	I	IC	1/7		REF1	6D0
XCTAD00M	OT	IC	2/2		TAD00	6F5
XCTAD10M	OT	IC	2/2		TAD10	6F0
3VG0813T	I	014	1/7		RADCLIO	6A2
	I	015	1/7		CNTIN0IO	6A4
	I	215	1/7		CNTIN1IO	6A1

SYMBOL NO. 15

REFRESH COUNTER ON BIT SLICE BOARD
BITS 18 AND 22, PRESENTLY NOT CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCR22.18	08-12	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		107			CNTOUT00	6H4
		207			CNTOUT10	6H2
XCREF1L	I	IC	1/8		REF1	6D0
XCTAD00L	OT	IC	2/3		TAD00	6F5
XCTAD10L	DT	IC	2/3		TAD10	6F0
3VG0812T	I	014	1/8		RADCLIO	6A2
	I	015	1/8		CNTIN0IO	6A4
	I	215	1/8		CNTIN1IO	6A1

SYMBOL NO. 16

REFRESH COUNTER ON BIT SLICE BOARD
BITS 17 AND 21, PRESENTLY NOT CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCR21.17	08-11	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		107			CNTOUT00	6H4
		207			CNTOUT10	6H2
XCREF1K	I	IC	1/9		REF1	6D0
XCTAD00K	OT	IC	2/4		TAD00	6F5
XCTAD10K	OT	IC	2/4		TAD10	6F0
3VG0811T	I	014	1/9		RADCLIO	6A2
	I	015	1/9		CNTIN0IO	6A4
	I	215	1/9		CNTIN1IO	6A1

SYMBOL NO. 17

REFRESH COUNTER ON BIT SLICE BOARD
BITS 16 AND 20, PRESENTLY NOT CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCR20.16	08-10	FA1060	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		107			CNTOUT00	6H4
		207			CNTOUT10	6H2
XCREF1J	I	IC	1/10		REF1	6D0
XCTAD00J	OT	IC	2/5		TAD00	6F5
XCTAD10J	OT	IC	2/5		TAD10	6F0
3VG0810T	I	014	1/10		RADCLIO	6A2
	I	015	1/10		CNTIN0IO	6A4
	I	215	1/10		CNTIN1IO	6A1

PART OF FS 3
SYMBOL(S) 12 13 14 15 16 17

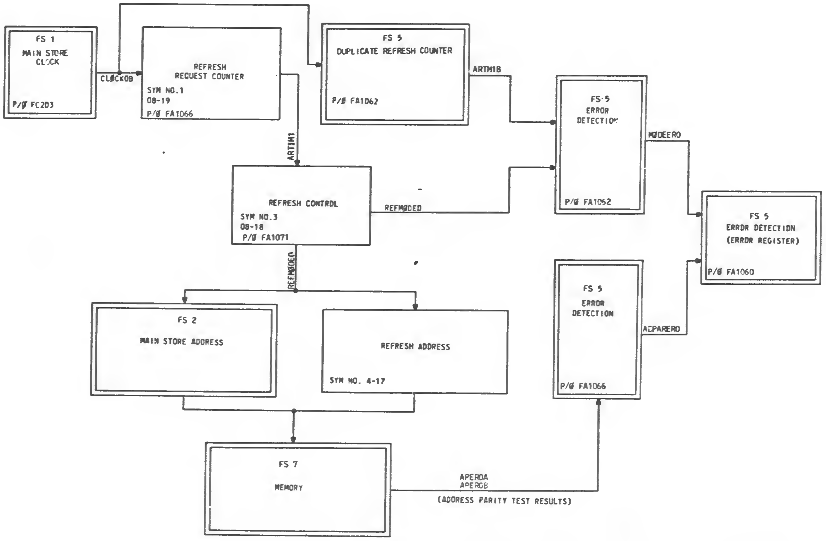
MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C2
ISSUE 1
BELL LABORATORIES
SD-1C902-03
B3CB
PRINTED IN U. S. A.
03/06/78

PART OF FS 3

REFRESH CONTROL

COMPOSITE DIAGRAM I

FUNCTION BLOCK DIAGRAM



PART OF FS 3
COMPOSITE DIAGRAM 1

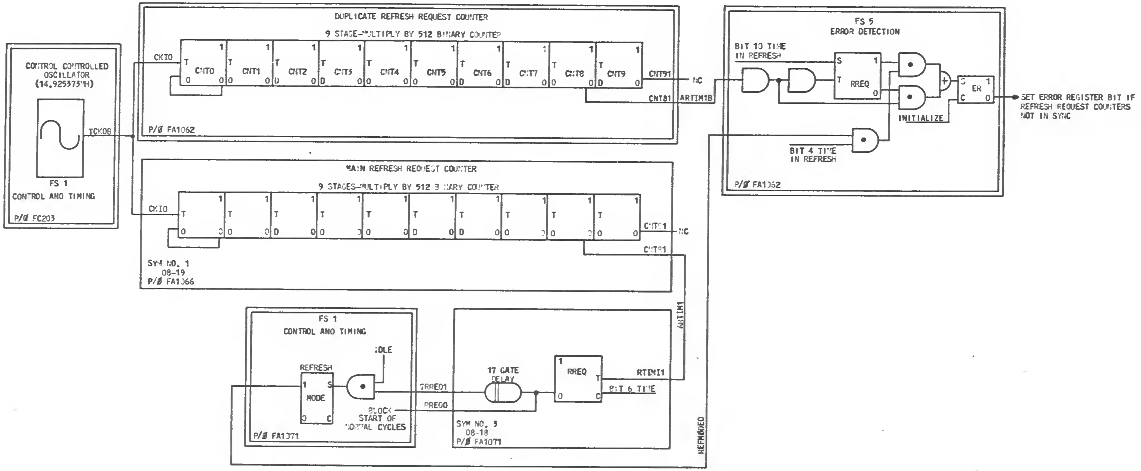
MAIN STORE CONTROLLER AND MEMORY ②	DWG SIZE 65	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B3GA

PART OF FS 3

REFRESH CONTROL

COMPOSITE DIAGRAM 3

REFRESH COUNTER



PART OF FS 3
COMPOSITE DIAGRAM 3

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 6S	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B3GC

PART OF FS 3

REFRESH CONTROL

COMPOSITE DIAGRAM 2

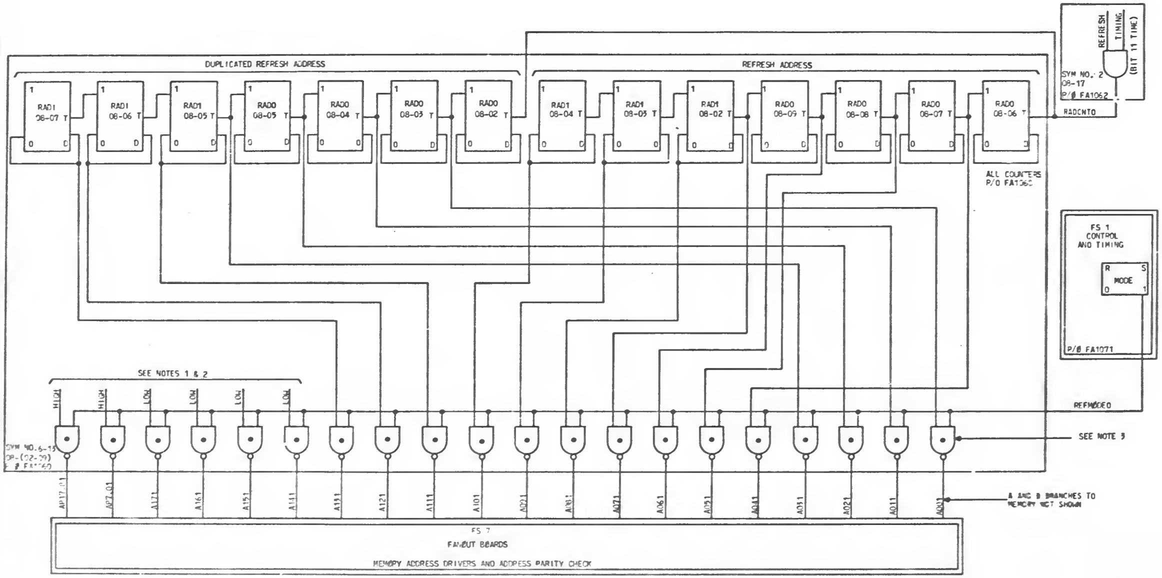
REFRESH ADDRESS

NOTES:

1. NON-COUNTING STAGES THAT REMAIN AT THE SAME LOG LEVELS DURING ALL REFRESH CYCLES.
2. NON-COUNTING STAGES ORIGINATE FROM THE FOLLOWING LOCATIONS:

ADDRESS BITS	CP LOCATION	CODE	[illegible]
14	08-08	FA1040	RA
15	08-09	FA1062	RA
16	08-16	FA1040	RA
17	08-23	[illegible]	
PL	08-16	FA1062	RA
PH	08-19	FA1066	

3. GATES TO FANOUT BOARD ARE P/O FS 2.



PART OF FS 3
COMPOSITE DIAGRAM 2

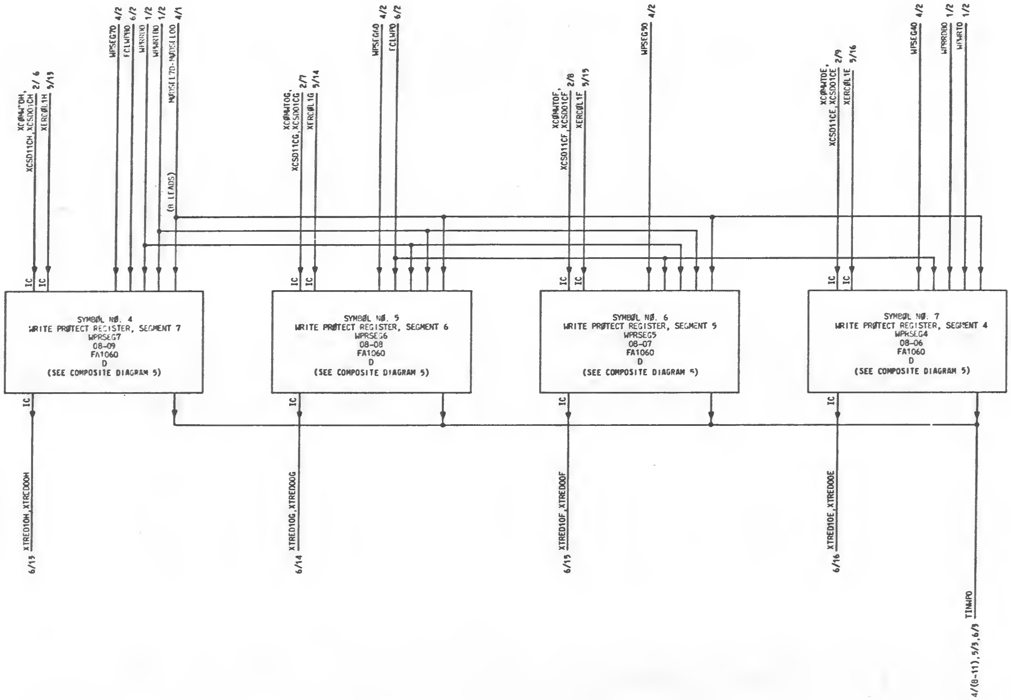
MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES

SD-1C902-03

PART OF FS 4

WRITE PROTECT
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 4
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
65

BELL LABORATORIES

SD-1C902-03

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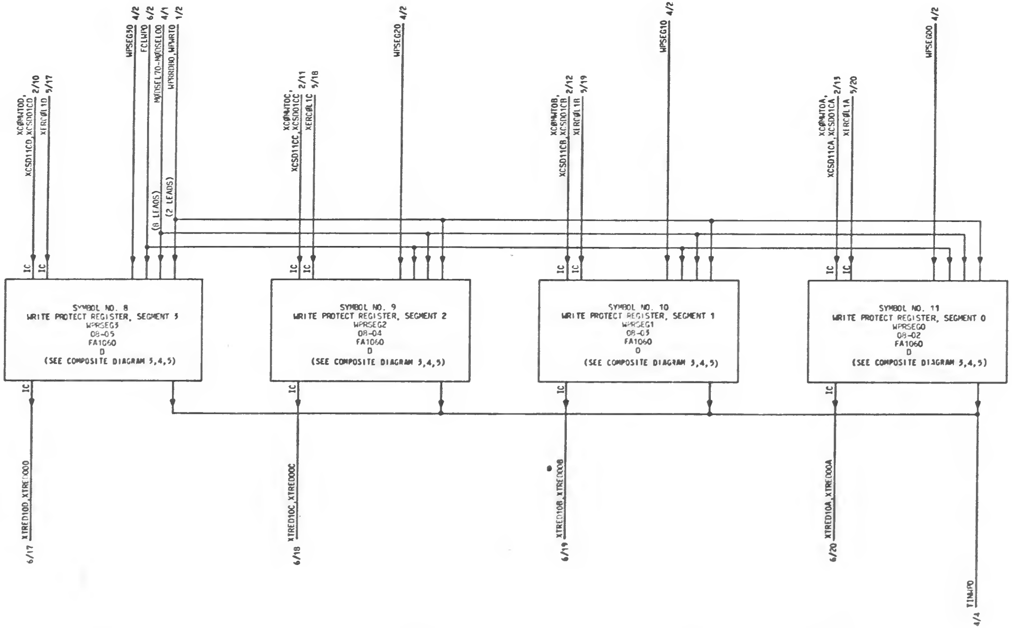
PART OF FS 4

WRITE PROTECT
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 4
INTERCONNECTION AND FLOW DIAGRAM

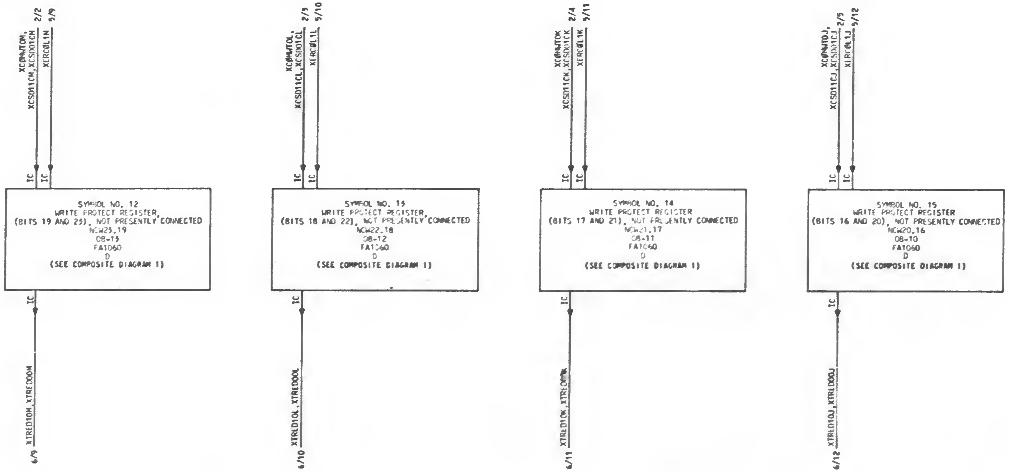
MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES

SD-1C902-03

PART OF FS 4

WRITE PROTECT
INTERCONNECTION AND FLOW DIAGRAM



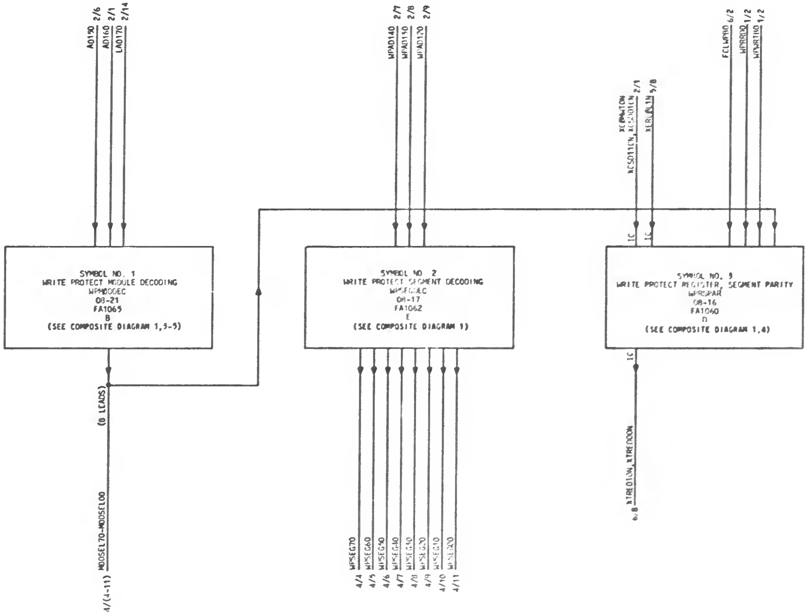
PART OF FS 4
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES | SD-1C902-03

PART OF FS 4

WRITE PROTECT
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 4
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES

SD-1C902-03

PART OF FS 4

WRITE PROTECT

SYMBOL NO. 1

WRITE PROTECT MODULE DECODING

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPMODDEC	08-21	FA1065	B	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
AD150	I	201	2/6		AD15I0	5A6
AD160	I	118	2/1		AD16I0	5A3
LAD170	I	316	2/14		AD17I0	5A
MODSEL00	0	113	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD00	5H7
MODSEL10	0	106	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD10	5H6
MODSEL20	0	313	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD20	5H5
MODSEL30	0	112	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD30	5H5
MODSEL40	0	108	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD40	5H4
MODSEL50	0	008	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD50	5H3
MODSEL60	0	006	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD60	5H2
MODSEL70	0	005	4/3,4/4 4/5,4/6 4/7,4/8 4/9,4/10 4/11		MOD70	5H1

SYMBOL NO. 2

WRITE PROTECT SEGMENT DECODING

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPSEGDEC	08-17	FA1062	E	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
WPAD120	I	006	2/9		AD12I0	5A4
[illegible]	I	[illegible]	2/8		AD13I0	5A[illegible]
[illegible]	I	[illegible]	2/7		AD14I0	5A[illegible]
[illegible]	0	[illegible]	4/[illegible]		WPSEG00	5H[illegible]
[illegible]	[illegible]	[illegible]	4/[illegible]		WPSEG10	5H4
[illegible]	0	[illegible]	4/9		WPSEG20	5H4
[illegible]	0	[illegible]	4/8		WPSEG30	5H3
[illegible]	[illegible]	[illegible]	4/7		WPSEG40	5H3
[illegible]	[illegible]	[illegible]	4/6		WPSEG50	5H2

SYMBOL NO. 2 (CONT)

WRITE PROTECT SEGMENT DECODING

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPSEGDEC	08-17	FA1062	E	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
WPSEG60	0	204	4/5		WPSEG60	5H2
WPSEG70	0	104	4/4		WPSEG70	5H1

SYMBOL NO. 3

WRITE PROTECT REGISTER,SEGMENT PARITY

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSPAR	08-16	FA1060	D	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
		019			INWPO	8H5
		114			WPSEGI0	8A1
FCLWPB0	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A7
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
WPRP00	I	208	1/2		WPRDI0	7A0
WPWRTB0	I	017	1/2		WPWRI0	7A5
XCOMWTON	I	IC	2/1		COMWRT0	8G0
XCSD01CN	I	IC	2/1		SD01C	8D0
XCSD11CN	I	IC	2/1		SD11C	8D0
XERCOL1N	I	IC	5/8		ERCOLL1	7A5
XTRED00N	OT	IC	6/8		TRETD00	7H2
XTRED10N	OT	IC	6/8		TRETD10	7H4

SYMBOL NO. 4

WRITE PROTECT REGISTER,SEGMENT 7

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG7	08-09	FA1060	0	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
[illegible]	I	117	6/2		WPCLRI0	7A6
[illegible]	I	216	4/1		M0SELI0	7A2
[illegible]	I	008	4/1		M1SELI0	8A7
[illegible]	I	[illegible]	4/[illegible]		M2SELI0	7A2
[illegible]	I	318	4/[illegible]		M3SELI0	8A5
[illegible]	I	308	4/[illegible]		M4SELI0	7A1
[illegible]	I	118	4/[illegible]		M5SELI0	8A3
[illegible]	I	315	4/[illegible]		M6SELI0	7A1
[illegible]	I	218	4/[illegible]		M7SELI0	8A1

SYMBOL NO. 4 (CONT)

WRITE PROTECT REGISTER,SEGMENT 7

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG7	08-09	FA1060	0	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
TINWPO	OT	019	4/5,4/6 4/7,4/8 4/9,4/10 4/11,6/3		INWPO	8H5
	I		5/3			
WPRD00	I	208	1/2		WPRDI0	7A0
WPSEG70	I	114	4/2		WPSEGI0	8A1
WPWRTB0	I	017	1/2		WPWRI0	7A5
XCOMWTOH	I	IC	2/6		COMWRT0	8G0
XCSD01CH	I	IC	2/6		SD01C	8D0
XCSD11CH	I	IC	2/6		SD11C	8D0
XERCOL1H	I	IC	5/13		ERCOLL1	7A5
XTRED00H	OT	IC	6/13		TRETD00	7H2
XTRED10H	OT	IC	6/13		TRETD10	7H4

SYMBOL NO. 5

WRITE PROTECT REGISTER,SEGMENT 6

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG6	08-08	FA1060	D	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
FCLWPO	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A7
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
TINWPO	OT	019	4/4		INWPO	8H5
WPRD00	I	208	1/2		WPRDI0	7A0
WPSEG60	I	114	4/2		WPSEGI0	8A1
WPWRTB0	I	017	1/2		WPWRI0	7A5
XCOMWTOG	I	IC	2/7		COMWRT0	8G0
XCSD01CG	I	IC	2/7		SD01C	8D0
XCSD11CG	I	IC	2/7		SD11C	8D0
XERCOL1G	I	IC	5/14		ERCOLL1	7A5
XTRED00G	OT	IC	6/14		TRETD00	7H2
XTRED10G	OT	IC	6/14		TRETD10	7H4

SYMBOL NO. 6

WRITE PROTECT REGISTER,SEGMEN

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG5	08-07	FA1060	0	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
FCLWPO	I	117	6/2		[illegible]	[illegible]
MODSEL00	I	216	4/1		[illegible]	[illegible]
MODSEL10	I	008	4/1		[illegible]	[illegible]
MODSEL20	I	011	4/1		[illegible]	[illegible]
MODSEL30	I	318	4/1		[illegible]	[illegible]
MODSEL40	I	308	4/1		[illegible]	[illegible]
MODSEL50	I	118	4/1		[illegible]	[illegible]
MODSEL60	I	315	4/1		[illegible]	[illegible]
MODSEL70	I	218	4/1		[illegible]	[illegible]
TINWPO	OT	019	4/4		[illegible]	[illegible]
WPRD00	I	208	1/2		[illegible]	[illegible]
WPSEG50	I	114	4/2		[illegible]	[illegible]
WPWRTB0	I	017	1/2		[illegible]	[illegible]
XCOMWTOF	I	IC	2/8		[illegible]	[illegible]
XCSD01CF	I	IC	2/8		[illegible]	[illegible]
XCSD11CF	I	IC	2/8		[illegible]	[illegible]
XERCOL1F	I	IC	5/15		[illegible]	[illegible]
XTRED00F	OT	IC	6/15		[illegible]	[illegible]
XTRED10F	OT	IC	6/15		[illegible]	[illegible]

PART OF FS 4
SYMBOL(S) 1 2 3 4 5 6

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES
SD-1C902-03

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PART OF FS 4

WRITE PROTECT

SYMBOL NO. 7

WRITE PROTECT REGISTER,SEGMENT 4

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG4	08-06	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCLWP0	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
TINWP0	OT	019	4/4		INWP0	8H5
WPRRDB0	I	208	1/2		WPPDI0	7A0
WPSEG40	I	114	4/2		WPSEGI0	8A1
WPWRT0	I	017	1/2		WPWRI0	7A5
XCOMWTOE	I	IC	2/9		COMWRT0	8G0
XCSD01CE	I	IC	2/9		SD01C	8D0
XCSD11CE	I	IC	2/9		SD11C	8D0
XERCOL1E	I	IC	5/16		ERCOLL1	7A5
XTRED00E	OT	IC	6/16		TRETD00	7H2
XTRED10E	OT	IC	6/16		TRETD10	7H4

SYMBOL NO. 8

WRITE PROTECT REGISTER,SEGMENT 3

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG3	08-05	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCLWP0	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A7
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
TINWP0	OT	019	4/4		INWP0	8H5
WPRPDB0	I	208	1/2		WPRDI0	7A0
WPSEG30	I	114	4/2		WPSEGI0	8A1
WPWRT0	I	017	1/2		WPWRI0	7A5
XCOMWTOD	I	IC	2/10		COMWRT0	8G0
XCSD01CD	I	IC	2/10		SD01C	8D0
XCSD11CD	I	IC	2/10		SD11C	8D0
XERCOL1D	I	IC	5/17		ERCOLL1	7A5
XTRED00D	OT	IC	6/17		TRETD00	7H2
XTRED10D	OT	IC	6/17		TRETD10	7H4

SYMBOL NO. 9

WRITE PROTECT REGISTER,SEGMENT 2

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG2	08-04	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCLWP0	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A7
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
TINWP0	OT	019	4/4		INWP0	8H5
WPRPOB0	I	208	1/2		WPRDI0	7A0
WPSEG20	I	114	4/2		WPSEGI0	8A1
WPWRT0	I	017	1/2		WPWRI0	7A5
XCOMWTOC	I	IC	2/11		COMWRT0	8G0
XCSD01CC	I	IC	2/11		SD01C	8D0
XCSD11CC	I	IC	2/11		SD11C	8D0
XERCOL1C	I	IC	5/18		ERCOLL1	7A5
XTRED00C	OT	IC	6/18		TRETD00	7H2
XTRED10C	OT	IC	6/18		TRETD10	7H4

SYMBOL NO. 10

WRITE PROTECT REGISTER,SEGMENT 1

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG1	08-03	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCLWP0	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A7
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
TINWP0	OT	019	4/4		INWP0	8H5
WPPROB0	I	208	1/2		WPRDI0	7A0
WPSEG10	I	114	4/2		WPSEGI0	8A1
WPWRT0	I	017	1/2		WPWRI0	7A5
XCOMWTOB	I	IC	2/12		COMWRT0	8G0
XCSD01CB	I	IC	2/12		SD01C	8D0
XCSD11CB	I	IC	2/12		SD11C	8D0
XERCOL1B	I	IC	5/19		ERCOLL1	7A5
XTRED00B	OT	IC	6/19		TRETD00	7H2
XTRED10B	OT	IC	6/19		TRETD10	7H4

SYMBOL NO. 11

WRITE PROTECT REGISTER,SEGMENT 0

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
WPRSEG0	08-02	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCLWP0	I	117	6/2		WPCLRI0	7A6
MODSEL00	I	216	4/1		M0SELI0	7A2
MODSEL10	I	008	4/1		M1SELI0	8A7
MODSEL20	I	011	4/1		M2SELI0	7A2
MODSEL30	I	318	4/1		M3SELI0	8A5
MODSEL40	I	308	4/1		M4SELI0	7A1
MODSEL50	I	118	4/1		M5SELI0	8A3
MODSEL60	I	315	4/1		M6SELI0	7A1
MODSEL70	I	218	4/1		M7SELI0	8A1
TINWP0	OT	019	4/4		INWP0	8H5
WPRRDB0	I	208	1/2		WPRDI0	7A0
WPSEG00	I	114	4/2		WPSEGI0	8A1
WPWRT0	I	017	1/2		WPWRI0	7A5
XCOMWTOA	I	IC	2/13		COMWRT0	8G0
XCSD01CA	I	IC	2/13		SD01C	8D0
XCSD11CA	I	IC	2/13		SD11C	8D0
XERCOL1A	I	IC	5/20		ERCOLL1	7A5
XTRED00A	OT	IC	6/20		TRETD00	7H2
XTRED10A	OT	IC	6/20		TRETD10	7H4

SYMBOL NO. 12

WRITE PROTECT REGISTER,
(BITS 19 AND 23), NOT PRESENTLY CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCW23.19	08-13	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		008			M1SELI0	8A7
		011			M2SELI0	7A2
		017			WPWRI0	7A5
		019			INWP0	8H5
		114			WPSEGI0	8A1
		117			WPCLRI0	7A6
		118			M5SELI0	8A3
		208			WPRDI0	7A0
		216			M0SELI0	7A2
		218			M7SELI0	8A1
		308			M4SELI0	7A1
		315			M6SELI0	7A1
		318			M3SELI0	8A5
XCOMWTOM	I	IC	2/2		COMWRT0	8G0
XCSD01CM	I	IC	2/2		SD01C	8D0
XCSD11CM	I	IC	2/2		SD11C	8D0
XERCOL1M	I	IC	5/9		ERCOLL1	7A5
XTRED00M	OT	IC	6/9		TRETD00	7H2
XTRED10M	OT	IC	6/9		TRETD10	7H4

SYMBOL NO. 13

WRITE PROTECT REGISTER,
(BITS 18 AND 22), NOT PRESENTLY CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCW22.18	08-12	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		008			M1SELI0	8A7
		011			M2SELI0	7A2
		017			WPWRI0	7A5
		019			INWP0	8H5
		114			WPSEGI0	8A1
		117			WPCLRI0	7A6
		118			M5SELI0	8A3
		208			WPRDI0	7A0
		216			M0SELI0	7A2
		218			M7SELI0	8A1
		308			M4SELI0	7A1
		315			M6SELI0	7A1
		318			M3SELI0	8A5
XCOMWTOL	I	IC	2/3		COMWRT0	8G0
XCSD01CL	I	IC	2/3		SD01C	8D0
XCSD11CL	I	IC	2/3		SD11C	8D0
XERCOL1L	I	IC	5/10		ERCOLL1	7A5
XTRED00L	OT	IC	6/10		TRETD00	7H2
XTRED10L	OT	IC	6/10		TRETD10	7H4

PART OF FS 4
SYMBOL(S) 7 8 9 10 11 12 13

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C2
ISSUE 1
BELL LABORATORIES
SD-1C902-03
84EB

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PART OF FS 4

WRITE PROTECT

SYMBOL NO. 14

WRITE PROTECT REGISTER.
(BITS 17 AND 21), NOT PRESENTLY CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCW21.17	08-11	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		008			M1SELIO	8A7
		011			M2SELIO	7A2
		017			WPWRIO	7A
		019			INWPO	8H5
		114			WPSEGIO	8A1
		117			WPCLRIO	7A6
		118			M5SELIO	8A3
		208			WPRDIO	7A0
		216			M0SELIO	7A2
		218			M7SELIO	8A1
		308			M4SELIO	7A1
		315			M6SELIO	7A1
		318			M3SELIO	8A5
XCOMWTOK	I	IC	2/4		COMWRTO	8G0
XCSD01CK	I	IC	2/4		SD01C	8D0
XCSD11CK	I	IC	2/4		SD11C	8D0
XERCOL1K	I	IC	5/11		ERCOLL1	7A5
XTRED00K	OT	IC	6/11		TRETD00	7H2
XTRED10K	OT	IC	6/11		TRETD10	7H4

SYMBOL NO. 15

WRITE PROTECT REGISTER.
(BITS 16 AND 20), NOT PRESENTLY CONNECTED

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NCW20.16	08-10	FA1060	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		008			M1SELIO	8A7
		011			M2SELIO	7A2
		017			WPWRIO	7A5
		019			INWPO	8H5
		114			WPSEGIO	8A1
		117			WPCLRIO	7A6
		118			M5SELIO	8A3
		208			WPRDIO	7A0
		216			M0SELIO	7A2
		218			M7SELIO	8A1
		308			M4SELIO	7A1
		315			M6SELIO	7A1
		318			M3SELIO	8A5
XCOMWTOJ	I	IC	2/5		COMWRTO	8G0
XCSD01CJ	I	IC	2/5		SD01C	8D0
XCSD11CJ	I	IC	2/5		SD11C	8D0
XERCOL1J	I	IC	5/12		ERCOLL1	7A5
XTRED00J	OT	IC	6/12		TRETD00	7H2
XTRED10J	OT	IC	6/12		TRETD10	7H4

PART OF FS 4
SYMBOL(S) 14 15

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE C2	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B4CC

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03/06/78

PART OF FS 4

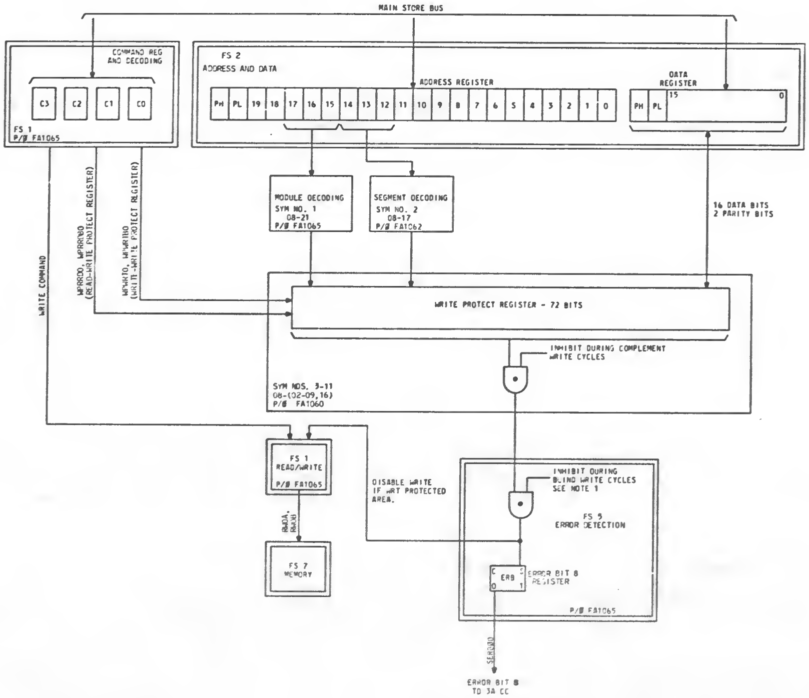
WRITE PROTECT

COMPOSITE DIAGRAM 1

FUNCTIONAL BLOCK DIAGRAM

NOTES:

1. BLIND WRITE COMMAND ONLY VALID WHEN ACCOMPANIED [illegible] MAIN STORE WIRING OPTION.



PART OF FS 4
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY

65

BELL LABORATORIES

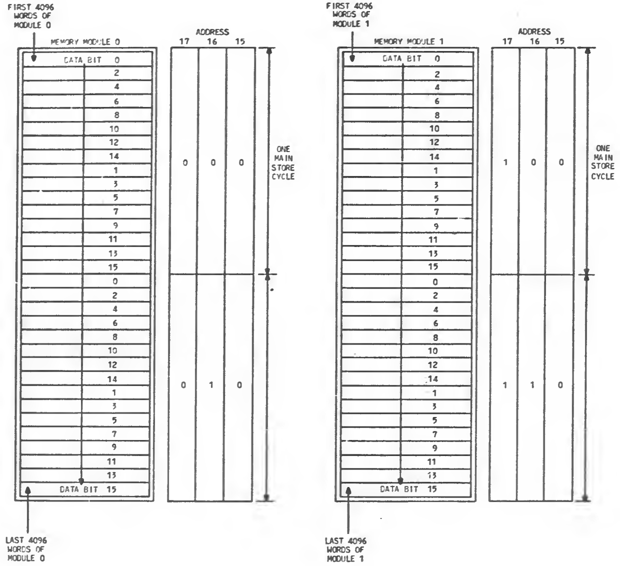
SD-1C902-03

PART OF FS 4

WRITE PROTECT

COMPOSITE DIAGRAM 2

MEMORY MAP ASSIGNMENT OF
WRITE PROTECT REGISTER
(SEE NOTE 1)



NOTES:

1. THIS COMPOSITE SHOWS THE ADDRESS AND DATA BITS FROM THE MAIN STORE BUS THAT ARE REQUIRED TO WRITE PROTECT ANY 4096 WORDS OF MEMORY. FOR EXAMPLE, THE FIRST 4096 WORDS OF MEMORY IN MODULE 0 ARE PROTECTED USING DATA BIT 0 WITH ADDRESS BITS 15,16 AND 17 SET TO ZEROES. A SINGLE MAIN STORE CYCLE CAN COVER THE WRITE PROTECTION OF 64K WORDS OF MEMORY.

PART OF FS 4
COMPOSITE DIAGRAM 2

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
AS

BELL LABORATORIES
SD-1C902-03

PART OF FS 4

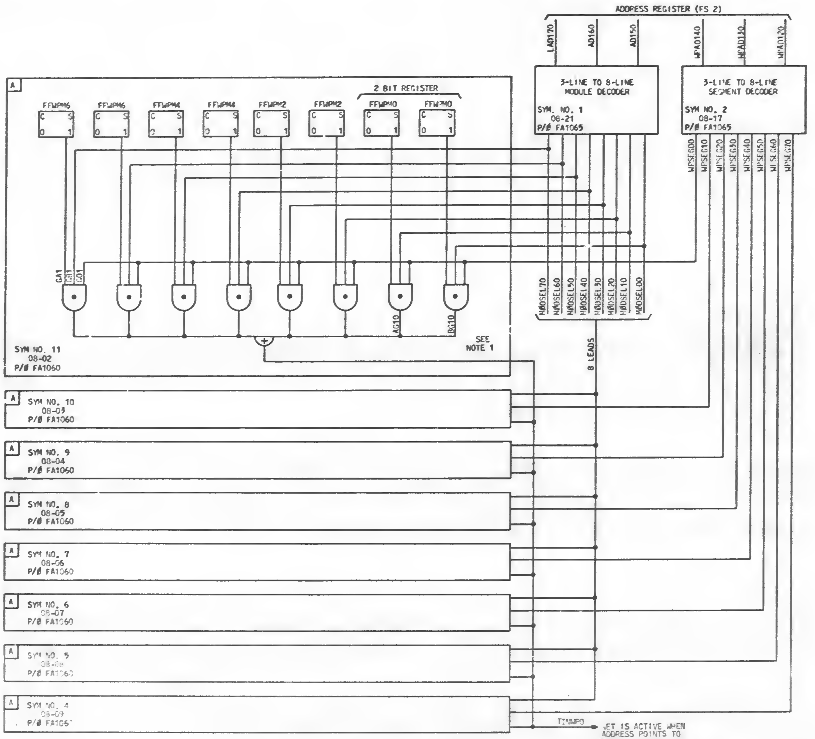
WRITE PROTECT

COMPOSITE DIAGRAM 3

TESTING NORMAL WRITES FOR
WRITE PROTECTED AREA

NOTES:

1. BOTH GATES AND FLIP-FLOPS SHOWN ARE INTERNAL TO A TWO BIT REGISTER.



NET IS ACTIVE WHEN
ADDRESS POINTS TO
WRITE PROTECTED AREA

PART OF FS 4
COMPOSITE DIAGRAM 3

MAIN STORE CONTROLLER AND MEMORY

②

DWG SIZE
65

ISSUE
1

BELL LABORATORIES

SD-1C902-03

84GC

PART OF FS 4

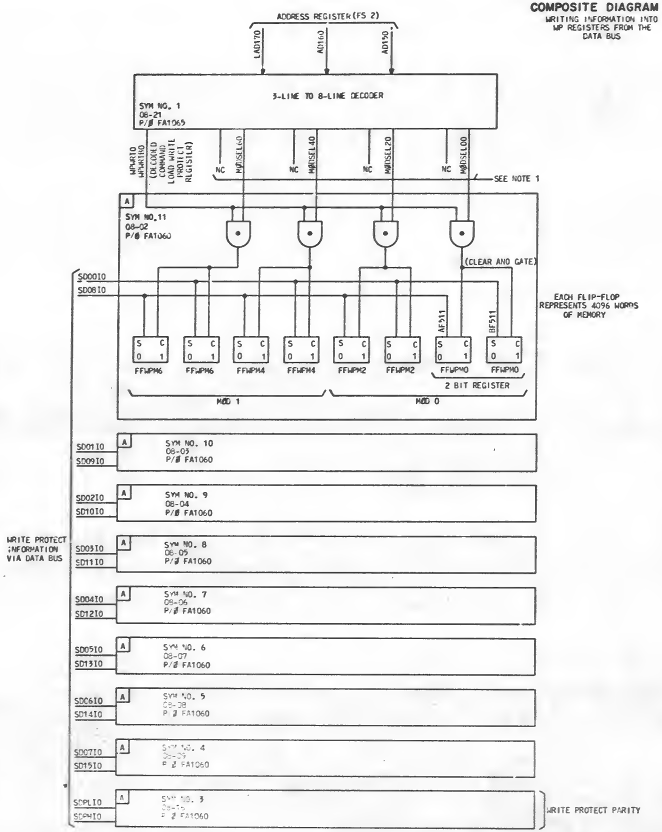
WRITE PROJECT

COMPOSITE DIAGRAM 4

WRITING INFORMATION INTO
WP REGISTERS FROM THE
DATA BUS

NOTES:

1. ONLY 4 OF THE 8 POSSIBLE MODULE SELECT LINES ARE USED FOR THIS FUNCTION.



PART OF FS 4
COMPOSITE DIAGRAM 4

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

ISSUE
1

BELL LABORATORIES

SD-IC902-03

846D

PART OF FS 4

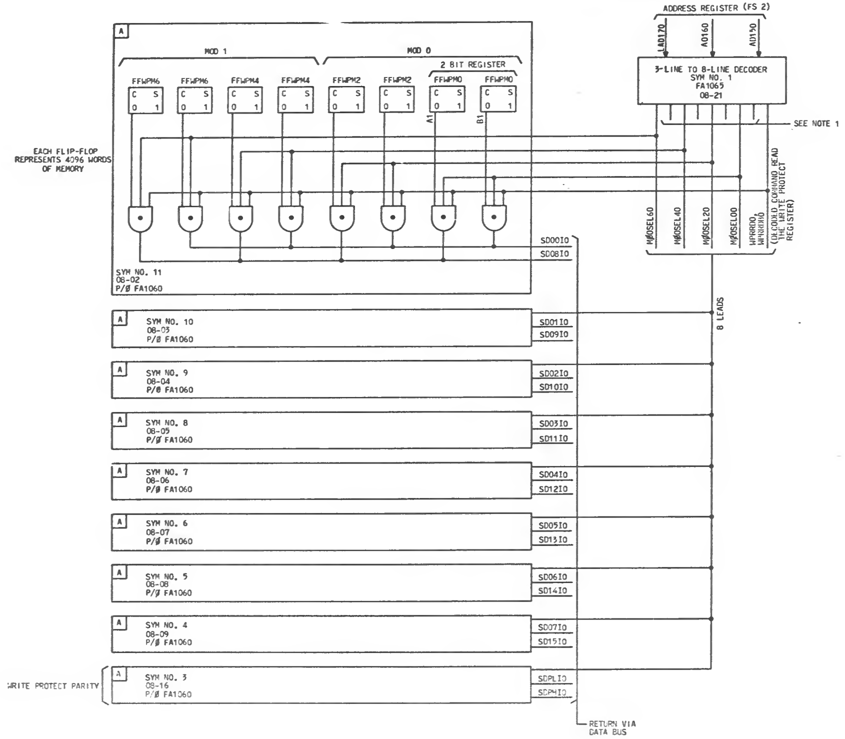
WRITE PROTECT

COMPOSITE DIAGRAM 5

SENDING CONTENTS OF WP
REGISTERS TO 3A CC

NOTES:

1. ONLY 4 OF THE 8 POSSIBLE MODULE SELECT LINES ARE USED FOR THIS FUNCTION.



PART OF FS 4
COMPOSITE DIAGRAM 5

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

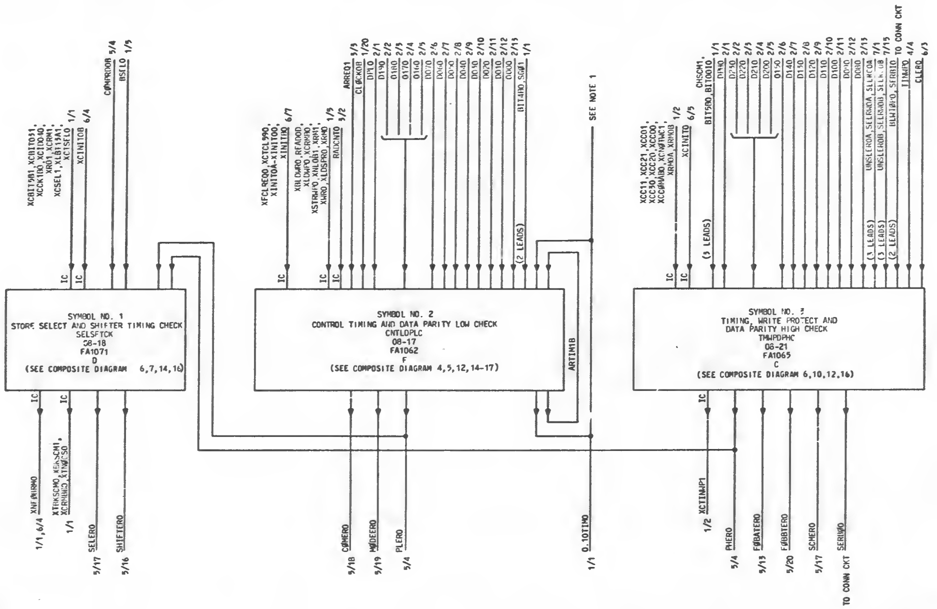
BELL LABORATORIES

SD-1C902-03

PART OF FS 5

ERROR DETECTION
INTERCONNECTION AND FLOW DIAGRAM

NOTES:
1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (OI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



PART OF FS 5
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

BELL LABORATORIES
SD-1C902-03
B5

E-7521 (12-73)

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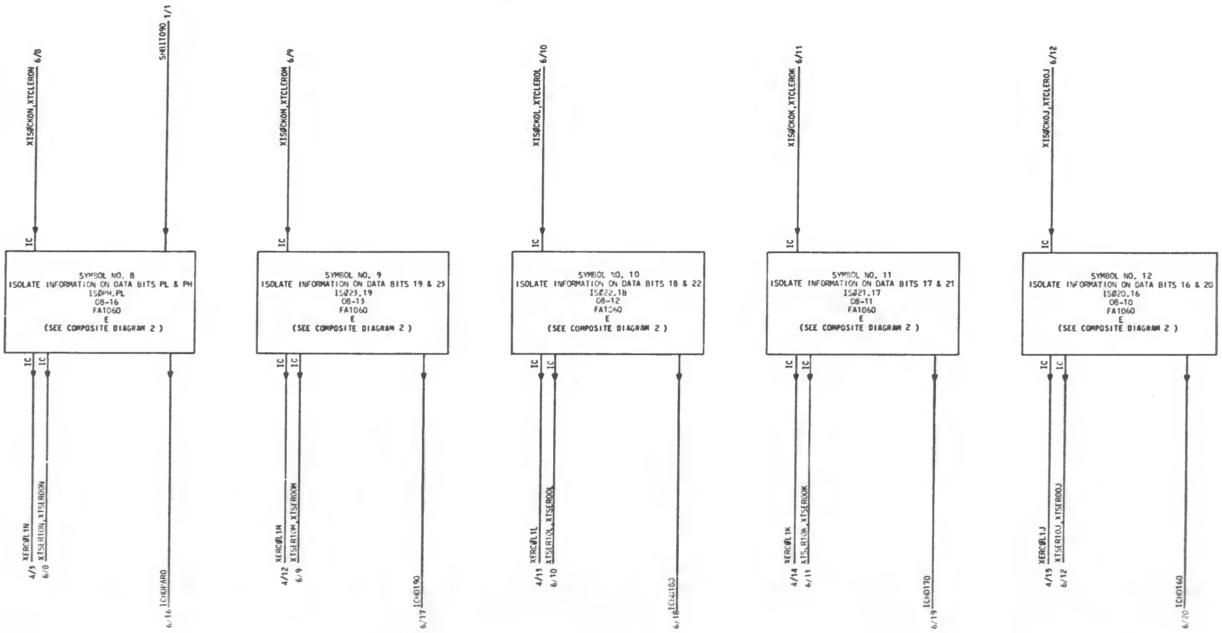
PART OF FS 5

ERROR DETECTION
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 5
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE
B5

ISSUE
1

BELL LABORATORIES

SD-1C902-03

B5AC

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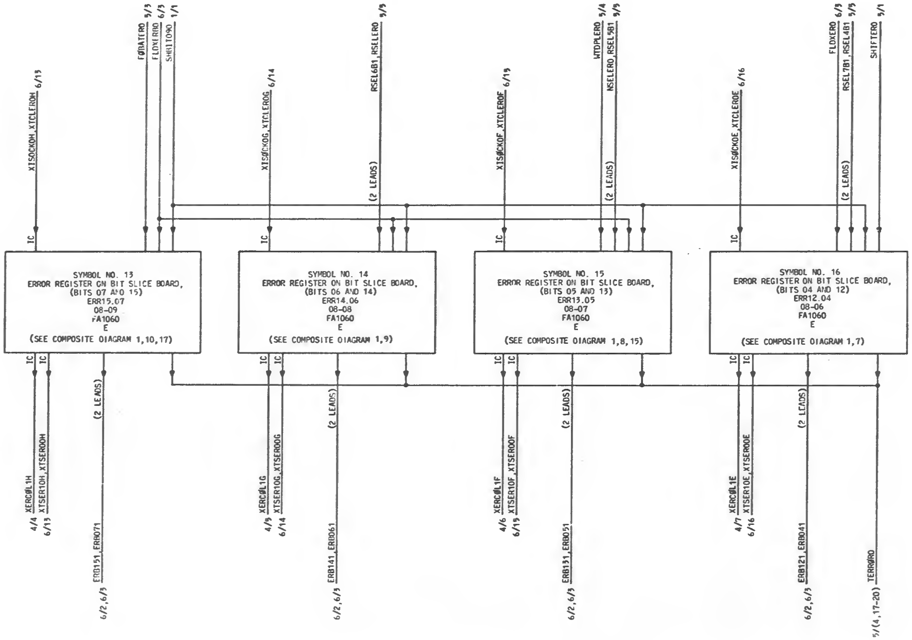
PART OF FS 5

ERROR DETECTION
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 5
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

(2)

BELL LABORATORIES	SD-1C902-03	B5AD

DWG SIZE 4S

ISSUE 1

E-7421 (12-73)

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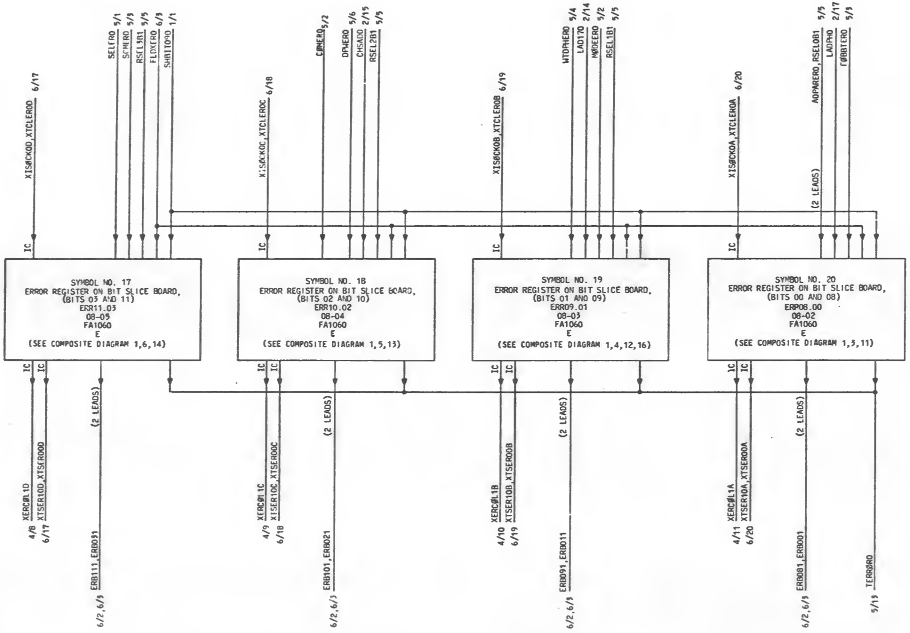
PART OF FS 5

ERROR DETECTION
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 5
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
E

BELL LABORATORIES

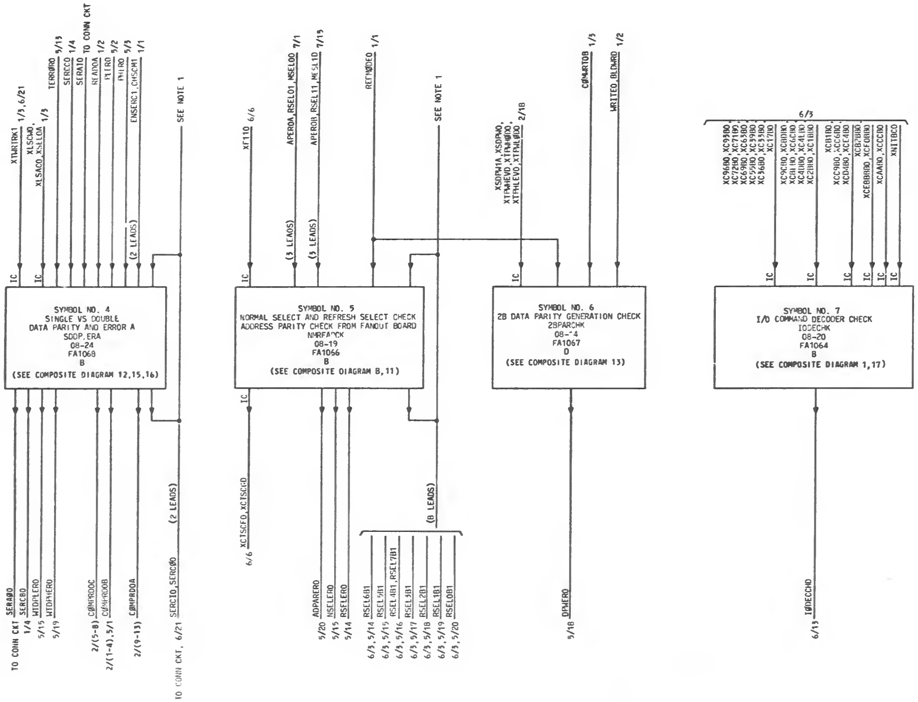
SD-1C902-03

PART OF FS 5

ERROR DETECTION
INTERCONNECTION AND FLOW DIAGRAM

NOTES:

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (OI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.



PART OF FS 5
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
6S	

BELL LABORATORIES | SD-1C902-03 | B5AB

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PART OF FS 5

ERROR DETECTION

SYMBOL NO. 1

STORE SELECT AND SHIFTER TIMING CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
SELSFTCK	08-18	FA1071	D	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		009			SEL0IF0	9H4
		310			0ISTIMI0	9A5
BSEL0	I	308	1/5		BSELI0	9A-
COMPRD0B	I	313	5/4		ENSCMI0	9A7
PHER0	I	118	5/3		PHERI0	9A6
PLER0	I	207	5/2		PLERI0	9A7
SELER0	O	007	5/17		SELER0	9H2
SHIFTER0	O	306	5/16		SHER0	9H2
XBKSCM1	O	IC	1/1		BKSCM1	9H7
XCBIT031	I	IC	1/1		BIT031	9A3
XCBIT5B1	I	IC	1/1		BIT05B1	9F0
XCCK1B0	I	IC	1/1		CK1B0	9G0
XCID040	I	IC	1/1		ID0.40	9F0
XCINIT0B	I	IC	6/4		INIT0B	9G0
XCRMBB0	O	IC	1/1		RMBB0	9H3
XCRM1	I	IC	1/1		RM1	9A3
XCSEL1	I	IC	1/1		SEL1	9A4
XCTSEL0	I	IC	1/1		TSEL0	9A3
XLBT13A1	I	IC	1/1		LBIT13A1	9A6
XNF0BRM0	O	IC	1/1,6/4		N0F0BRM0	9H6
XRD1	I	IC	1/1		RD1	9A6
XTBKSCM0	O	IC	1/1		TBKSCM0	9H6
XTN0CS0	OT	IC	1/1		TN0CS0	9A5

SYMBOL NO. 2

CONTROL TIMING AND DATA PARITY LOW CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CNTLDPLC	08-17	FA1062	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		013			CNT501	8G9
		016			CNT91	8H1
		214			TM0DCK1	6H4
ARRED1	I	209	3/3		ARREDI1	6A2
ARTIM1B	O	110			CNT81	8H3
	I	206			RTIMI1	6A5
BIT4B0	I	201	1/1		ERSTBI0	6A1
CLOCK0B	I	202	1/20		CKI0	9A7
COMER0	O	208	5/18		TCER0	7H3
DPL0	I	007	2/1		DPLI0	10A7
D000	I	117	2/13		D010	10A0
D010	I	217	2/12		D110	10A1
D020	I	017	2/11		D210	10A1
D030	I	316	2/10		D310	10A2
D040	I	210	2/9		D410	10A3
D050	I	216	2/8		D510	10A3
D060	I	116	2/7		D610	10A4
D070	I	219	2/6		D710	10A4
D160	I	019	2/5		D1610	10A5
D170	I	318	2/4		D1710	10A5
D180	I	010	2/3		D1810	10A6

SYMBOL NO. 2 (CONT)

CONTROL TIMING AND DATA PARITY LOW CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CNTLDPLC	08-17	FA1062	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
D190	I	108	2/2		D1910	10A6
M0DEER0	O	203	5/19		ER0	6G8
PLER0	O	313	5/1,5/4		TPER0	11H4
RADCNT0	I	1C304	3/2		RADCNT0	6A6
READ0D	I	IC002	1/5		RD0	7A2
SGD1	I	109	1/1		G0I1	6A1
XBLDWR0	1	IC	1/5		BL0WR0	7A1
XCRMB0	1	IC	1/5		RMB0	7A4
XCTCL590	I	IC	6/7		TCL590	8E0
XFCLRED0	I	IC	6/7		FCLRRED0	6A6
XINITB0	I	IC	6/7		INITB0	9D0
XINIT0A	I	IC	6/7		INIT0A	6A2
XINIT0B	I	IC	6/7		INIT0B	9D0
XINIT0C	I	IC	6/7		INIT0C	8E0
XINIT0D	I	IC	6/7		INIT0D	6A7
XLDSPR0	I	IC	1/5		LDSPR0	7A1
XL0WP0	I	IC	1/5		L0WP0	7A1
XNL0B1	I	IC	1/5		NL0B1	6E0
XRM0	I	IC	1/5		RM0	6E0
XPM1	I	IC	1/5		RM1	6E0
XSTRWP0	I	IC	1/5		STRWP0	7A0
XWR0	I	IC	1/5		WR0	7A1
0.10TIM0	OT	IC001	1/1		TTMACTI0	6A3
	ID	001	1/1		TTMACTI0	6A4
3T046D	I	014		202	CNTTSTI0	8A1

SYMBOL NO. 3

TIMING,WRITE PROTECT AND
DATA PARITY HIGH CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
TMWPDPHC	08-21	FA1065	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
BIT00I0	I	315	1/1		CLTCHI0	8A2
BIT5B0	I	015	1/1		ERSTBI0	8A4
BLWT0P0	I	003	TO CONN CKT		BLWT0PI0	9A4
CHSCM1	I	017	1/1		CHSCMI1	9A3
CLER0	I	203	6/3		CLERBI0	9A2
DPH0	I	001	2/1		DPHI0	7A4
D080	I	212	2/13		D810	6A7
D090	I	110	2/12		D910	6A6
D100	I	308	2/11		D1010	6A6
D110	I	303	2/10		D1110	6A5
D120	I	002	2/9		D1210	6A4
D130	I	101	2/8		D1310	6A4
D140	I	317	2/7		D1410	6A3
D150	I	116	2/6		D1510	6A3
D200	I	311	2/5		D2010	6A2
D210	I	109	2/4		D2110	6A2
D220	I	204	2/3		D2210	6A1
D230	I	102	2/2		D2310	6A1

SYMBOL NO. 3 (CONT)

TIMING,WRITE PROTECT AND
DATA PARITY HIGH CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
TMWPDPHC	08-21	FA1065	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
F0BATER0	O	208	5/13		TATER0	10H3
F0BBTER0	O	206	5/20		BTER0	8H4
PHER0	O	304	5/1,5/4		TPER0	7H4
SCMER0	O	217	5/17		SCMER0	9H3
SELRC0A	I	301	7/1		SELCSAI0	8A1
SELRC0B	I	318	7/15		SELCSBI0	8A2
SELRW0A	I	210	7/1		SELRWAI0	8A4
SELRW0B	I	009	7/15		SELRWBI0	8A4
SERBI0	I	111	TO CONN CKT		SERBI0	9A5
SEPBD0	O	013	TO CONN CKT		TSERB0	9H5
TINWP0	I	309	4/4		INWPI0	9A5
UNSLER0A	I	004	7/1		UNSELAI0	8A3
UNSLER0B	I	202	7/15		UNSELBI0	8A3
XCC0MAB0	I	IC	1/2		C0MAB0	10A4
XCC00	I	IC	1/2		C00	10A4
XCC01	I	IC	1/2		C01	9A4
XCC11	I	IC	1/2		C11	10A4
XCC20	I	IC	1/2		C20	10A3
XCC21	1	IC	1/2		C21	9A4
XCC30	I	IC	1/2		C30	10A2
XCINIT0	I	IC	6/5		INIT0	8E0
XCN0TWC1	I	IC	1/2		N0TWC1	9A3
XCTINWP1	O	IC	1/2		TINWP1	9H4
XRM0A	I	IC	1/2		RM0A	10A2
XRM0B	I	IC	1/2		RM0B	9E1

SYMBOL NO. 4

SINGLE VS DOUBLE
DATA PARITY AND ERROR A

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
SDDP.ERA	08-24	FA1068	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		008			TFER0	4H2
CHSCM1	I	219	1/1		ENCMR0I1	4A3
C0MPRD0A	O	116	2/9,2/10 2/11,2/12 2/13		PZER0A	4H4
C0MPRD0B	O	019	2/1,2/2 2/3,2/4 5/1		PZER0B	4H4
C0MPRD0C	O	006	2/5,2/6 2/7,2/8		PZER0C	4H3
ENSERC1	I	015	1/1		ENSERCI1	4A4
PHER0	I	218	5/3		PHERI0	4A1
PLER0	I	317	5/2		PLEPI0	4A1
READ0A	I	315	1/2		RDI0	4A2
SERAI0	I	318	TO CONN CKT		SERAI0	4A7
SERA00	O	117	TO CONN CKT		TSERA0	4G7
SERCB0	O	310	1/4		SERC0	4H5
SERCC0	I	308	1/4		SERCI0	4A5
SERCI0	IO	309	6/21 TO CONN CKT		TSERCI0	4A5
	OT	IC309	5/4		TSERCI0	4A5

SYMBOL NO. 4 (CONT)

SINGLE VS DOUBLE
DATA PARITY AND ERROR A

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
SDDP.EPA	08-24	FA1068	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
SERC00	OI	002	6/21 TO CONN CKT		TSERC0	4H4
SERCD0	O	IC002	5/4		TSERC0	4H5
TERR0R0	I	007	5/13		ERI0	4A7
WTDPHER0	O	109	5/19		WRTPHER0	4H1
WTDPLER0	O	108	5/15		WRTPLER0	4H2
XLSAC0	I	IC	1/3		LSAC0	4A3
XLSCW0	I	IC	1/3		LSCW0	4A0
XSEL0A	I	IC	1/3		SEL0	4A5
XTWRTPK1	I	IC	1/3		TWRTPCK1	4A0

PART OF FS 5
SYMBOL(S) 1 2 3 4

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE E2
ISSUE 1

BELL LABORATORIES
SD-1C902-03
B5CA

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PART OF FS 5

ERROR DETECTION

SYMBOL NO. 5

NORMAL SELECT AND REFRESH SELECT CHECK;
ADDRESS PARITY CHECK FROM FANOUT BOARD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
NMRFADCK	08-19	FA1066	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		003			RSEL2I1	5A4
		013			MSEL7IO	3A1
		014			MSEL4IC	3A
		016			RSEL6I1	5A0
		018			RSEL3I1	5A4
		113			MSEL6IC	3A1
		115			MSEL2IO	3A3
		116			RSEL4I1	5A1
		214			MSEL3IO	3A3
		216			RSEL5I1	5A1
		313			MSEL5IO	3A2
		315			RSEL7I1	5A0
ADPARERO	O	208	5/20		TRARERO	5H6
APEROA	I	310	7/1		APARERIO	5A5
APEROB	I	211	7/15		BPARERIO	5A6
MSEL00	I	219	7/1		MSEL0IO	3A4
MSEL10	I	318	7/15		MSEL1IO	3A3
NSELERO	O	209	5/15		NSELERO	5H4
REFMODEO	I	206	1/1		RMIO	5A2
RSELERO	O	108	5/14		RSELERO	5H2
RSEL0B1	OI	010	6/3 5/20		RSEL01	5H5
RSEL01	I	103	7/1		RSEL0I1	5A5
RSEL1B1	OI	306	6/3 5/19		RSEL11	5H5
RSEL11	I	203	7/15		RSEL1I1	5A5
RSEL2B1	OI	106	6/3 5/18		RSEL21	5H4
RSEL3B1	OI	309	6/3 5/17		RSEL31	5H4
RSEL4BI	OI	109	6/3 5/16		RSEL41	5H1
RSEL5B1	OI	009	6/3 5/15		RSEL51	5H1
RSEL6B1	OI	308	6/3 5/14		RSEL61	5H0
RSEL7B1	OI	210	6/3 5/16		RSEL71	5H0
XCTSCFO	OT	IC	6/6		TSCFO	4B7
XCTSCGO	OT	IC	6/6		TSCGO	4B4
XF110	I	IC	6/6		F110	3E0

SYMBOL NO. 6

2B DATA PARITY GENERATION CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
2BPARCHK	08-14	FA1067	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
BLOWRO	I	214	1/2		BLDWRTIO	7A4
COMWRTOB	I	115	1/3		COMWRTIO	7A5
OPWERO	O	003	5/18		TPWERO	7H3
REFMODEO	I	213	1/1		RMIO	7A5
WRITEO	I	215	1/2		WRTIO	7A6
XSOPWO	I	IC	2/18		SDPWO	7F0
XSDPW1A	I	IC	2/18		SDPW1A	7F0
XTPWHEVO	I	IC	2/18		TPWHEVO	7B0
XTPWHODO	1	IC	2/18		TPWHODO	7B0
XTPWLEVO	1	IC	2/18		TPWLEVO	7B0
XTPWLOOO	1	IC	2/18		TPWLODO	7B0

SYMBOL NO. 7

I/O COMMAND DECODER CHECK

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IODECHK	08-20	FA1064	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
IODECCHO	O	214	6/13		XORFGO	9H5
XCAAB0	1	IC	6/3		AAB0	6B0
XCB1B0	I	IC	6/3		B1B0	6D0
XCB2BB0	1	IC	6/3		B2BB0	6D0
XCCCB0	I	IC	6/3		CCB0	6B0
XCC6B0	1	IC	6/3		C6B0	6B0
XCC9B0	I	IC	6/3		C9B0	6B0
XC04B0	I	IC	6/3		04B0	6C0
XCE4B0	I	IC	6/3		E4B0	6A0
XCE8BBB0	I	IC	6/3		E8BBB0	6B0
XCF0BB0	1	IC	6/3		F0BB0	7B0
XC1BB0	I	IC	6/3		1BB0	6C0
XC17B0	1	IC	6/3		17B0	6C0
XC2BB0	I	IC	6/3		2BB0	6B0
XC33B0	I	IC	6/3		33B0	6D0
XC36B0	I	IC	6/3		36B0	6D0
XC39B0	I	IC	6/3		39B0	6D0
XC40B0	I	IC	6/3		40B0	6D0
XC4EB0	I	IC	6/3		4EB0	6A0
XC55B0	I	IC	6/3		55B0	6C0
XC6CB0	1	IC	6/3		6CB0	6B0
XC63B0	I	IC	6/3		63B0	6B0
XC69B0	1	IC	6/3		69B0	6C0
XC71B0	I	IC	6/3		71B0	6D0
XC72B0	I	IC	6/3		72B0	6D0
XC8DB0	I	IC	6/3		8DB0	6B0
XC8EB0	I	IC	6/3		8EB0	6C0
XC9CB0	I	IC	6/3		9CB0	6C0
XC93B0	I	IC	6/3		93B0	6C0
XC96B0	I	IC	6/3		96B0	6C0
XNITBCO	I	IC	6/3		INITBCO	6C0

SYMBOL NO. 8

ISOLATE INFORMATION ON DATA BITS RL AND RH

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ISOPH.RL	08-16	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		007			ERO	9H4
		016			EXERLDIO	9A1
		104			IOER11	9H5
		108			EXIN0IO	9A1
		116			ERIN0IO	9A3
		212			EXIN1IO	9A5
		305			IOER01	9H1
		313			ERIN1IO	9A3
ICHDPARO	O	219	6/16		ER00	9H3
SHBIT090	1	316	1/1		ERCOLLIO	9A0
XERCOL1N	O	IC	4/3		ERCOLL1	9B6
XISOCKON	1	IC	6/8		ISOCKO	9D0
XTCLERON	I	IC	6/8		TCLERO	9E0
XTSER00N	OT	IC	6/8		TSTER00	9F0
XTSER10N	OT	IC	6/8		TSTER10	9F7

SYMBOL NO. 9

ISOLATE INFORMATION ON DATA BITS 19 AND 23

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IS023.19	08-13	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		007			ERO	9H4
		016			EXERLDIO	9A1
		104			IDER11	9H5
		108			EXIN0IO	9A1
		116			ERIN0IO	9A3
		212			EXIN1IO	9A5
		305			IDER01	9H1
		313			ERIN1IO	9A3
		316			ERCOLLIO	9A0
ICHD190	O	219	6/17		ER00	9H3
XERCOL1M	O	IC	4/12		ERCOLL1	9B6
XISOCKOM	I	IC	6/9		ISOCKO	9D0
XTCLEROM	I	IC	6/9		TCLERO	9E0
XTSER00M	OT	IC	6/9		TSTER00	9F0
XTSER10M	OT	IC	6/9		TSTER10	9F7

SYMBOL NO. 10

ISOLATE INFORMATION ON DATA BITS 18 AND 22

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IS022.18	08-12	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		007			ERO	9H4
		016			EXERLDIO	9A1
		104			IOER11	9H5
		108			EXIN0IO	9A1
		116			ERIN0IO	9A3
		212			EXIN1IO	9A5
		305			IDER01	9H1
		313			ERIN1IO	9A3
		316			ERCOLLIO	9A0
ICHD180	O	219	6/18		ER00	9H3
XERCOL1L	O	IC	4/13		ERCOLL1	9B6
XISOCKOL	I	IC	6/10		ISOCKO	9D0
XTCLEROL	I	IC	6/10		TCLERO	9E0
XTSER00L	OT	IC	6/10		TSTER00	9F0
XTSER10L	OT	IC	6/10		TSTER10	9F7

SYMBOL NO. 11

ISOLATE INFORMATION ON DATA BITS 17 AND 21

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IS021.17	08-11	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		007			ERO	9H4
		016			EXERLDIO	9A1
		104			IDER11	9H5
		108			EXIN0IO	9A1
		116			ERIN0IO	9A3
		212			EXIN1IO	9A5
		305			IOER01	9H1
		313			ERIN1IO	9A3
		316			ERCOLLIO	9A0
ICHD170	O	219	6/19		ER00	9H3
XERCOL1K	O	IC	4/14		ERCOLL1	9B6
XISOCKOK	I	IC	6/11		ISOCKO	9D0
XTCLEROK	I	IC	6/11		TCLERO	9E0
XTSER00K	OT	IC	6/11		TSTER00	9F0
XTSER10K	OT	IC	6/11		TSTER10	9F7

RART OF FS 5
SYMBOL(S) 5 6 7 8 9 10 11

MAIN STORE CONTROLLER AND MEMORY

	DWG SIZE CZ	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B5CE

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PART OF FS 5

ERROR DETECTION

SYMBOL NO. 12

ISOLATE INFORMATION ON DATA BITS 16 AND 20

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IS020.16	08-10	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		007			ER0	9H4
		016			EXERLDIO	9A1
		104			IOER11	9H
		108			EXIN0IO	9A1
		116			ERIN0IO	9A3
		212			EXIN1IO	9A5
		305			IOER01	9H1
		313			ERIN1IO	9A3
		316			ERCOLLIO	9A0
ICHD160	0	219	6/20		ER00	9H3
XERCOL1J	0	IC	4/15		ERCOLL1	9B6
XISOCK0J	I	IC	6/12		ISOCK0	9D0
XTCLER0J	I	IC	6/12		TCLER0	9E0
XTSER00J	OT	IC	6/12		TSTER00	9F0
XTSER10J	OT	IC	6/12		TSTER10	9F7

SYMBOL NO. 13

ERROR REGISTER ON BIT SLICE BOARD,
(BITS 07 AND 15)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR15.07	08-09	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		108			EXIN0IO	9A1
		212			EXIN1IO	9A5
		219			ER00	9H3
		313			ERIN1IO	9A3
ERB071	0	305	6/2,6/3		IOER01	9H1
ERB151	0	104	6/2,6/3		IOER11	9H5
FLDXERB0	I	016	6/3		EXERLDIO	9A1
FOBATER0	I	116	5/3		ERIN0IO	9A3
SHBIT090	I	316	1/1		ERCOLLIO	9A0
TERROR0	OT	007	5/14,5/15 5/16,5/17 5/18,5/19 5/20		ER0	9H4
	I		5/4			
XERCOL1H	0	IC	4/4		ERCOLL1	9B6
XISOCK0H	I	IC	6/13		ISOCK0	9D0
XTCLER0H	I	IC	6/13		TCLER0	9E0
XTSER00H	OT	IC	6/13		TSTER00	9F0
XTSER10H	OT	IC	6/13		TSTER10	9F7

SYMBOL NO. 14

ERROR REGISTER ON BIT SLICE BOARD,
(BITS 06 AND 14)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR14.06	08-08	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		212			EXIN1IO	9A5
		219			ER00	9H3
		313			ERIN1IO	9A3
ERB061	0	305	6/2,6/3		IDER01	9H1
ERB141	0	104	6/2,6/3		IDER11	9H5
FLDXERB0	I	016	6/3		EXERLDIO	9A1
RSELER0	I	116	5/5		ERIN0IO	9A3
RSEL6B1	I	108	5/5		EXIN0IO	9A1
SHBIT090	I	316	1/1		ERCOLLIO	9A0
TERROR0	OT	007	5/13		ER0	9H4
XERCOL1G	0	IC	4/5		ERCOLL1	9B6
XISOCK0G	I	IC	6/14		ISOCK0	9D0
XTCLER0G	I	IC	6/14		TCLER0	9E0
XTSER00G	OT	IC	6/14		TSTER00	9F0
XTSER10G	OT	IC	6/14		TSTER10	9F7

SYMBOL NO. 15

ERROR REGISTER ON BIT SLICE BOARD,
(BITS 05 AND 13)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR13.05	08-07	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		212			EXIN1IO	9A5
		219			ER00	9H3
ERB051	0	305	6/2,6/3		IOER01	9H1
ERB131	0	104	6/2,6/3		IOER11	9H5
FLDXERBC	I	016	6/3		EXERLDIO	9A1
NSELER0	I	116	5/5		ERIN0IO	9A3
RSEL5B1	I	108	5/5		EXIN0IO	9A1
SHBIT090	I	316	1/1		ERCOLLIO	9A0
TERROR0	OT	007	5/13		ER0	9H4
WTDPLER0	I	313	5/4		ERIN1IO	9A3
XERCOL1F	0	IC	4/6		ERCOLL1	9B6
XISOCK0F	I	IC	6/15		ISOCK0	9D0
XTCLER0F	I	IC	6/15		TCLER0	9E0
XTSER00F	OT	IC	6/15		TSTER00	9F0
XTSER10F	OT	IC	6/15		TSTER10	9F7

SYMBOL NO. 16

ERROR REGISTER ON BIT SLICE BOARD,
(BITS 04 AND 12)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR12.04	08-06	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		219			ER00	9H3
		313			ERIN1IO	9A3
ERB041	0	305	6/2,6/3		IDER01	9H1
ERB121	0	104	6/2,6/3		IDER11	9H5
FLDXER0	I	016	6/3		EXERLDIO	9A1
RSEL4B1	I	108	5/5		EXIN0IO	9A1
RSEL7B1	I	212	5/5		EXIN1IO	9A5
SHBIT090	I	316	1/1		ERCOLLIO	9A0
SHIFTER0	I	116	5/1		ERIN0IO	9A3
TERROR0	OT	007	5/13		ER0	9H4
XERCOL1E	0	IC	4/7		ERCOLL1	9B6
XISOCK0E	I	IC	6/16		ISOCK0	9D0
XTCLER0E	I	IC	6/16		TCLER0	9E0
XTSER00E	OT	IC	6/16		TSTER00	9F0
XTSER10E	OT	IC	6/16		TSTER10	9F7

SYMBOL NO. 17

ERROR REGISTER ON BIT SLICE BOARD,
(BITS 03 AND 11)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR11.03	08-05	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		212			EXIN1IO	9A5
		219			ER00	9H3
ERB031	0	305	6/2,6/3		IOER01	9H1
ERB111	0	104	6/2,6/3		IDER11	9H5
FLDXER0	I	016	6/3		EXERLDIO	9A1
RSEL3B1	I	108	5/5		EXIN0IO	9A1
SCMER0	I	116	5/3		ERIN0IO	9A3
SELER0	I	313	5/1		ERIN1IO	9A3
SHBIT090	I	316	1/1		ERCOLLIO	9A0
TERROR0	OT	007	5/13		ER0	9H4
XERCOL1D	0	IC	4/8		ERCOLL1	9B6
XISOCK0D	I	IC	6/17		ISOCK0	9D0
XTCLER0D	I	IC	6/17		TCLER0	9E0
XTSER00D	OT	IC	6/17		TSTER00	9F0
XTSER10D	OT	IC	6/17		TSTER10	9F7

SYMBOL NO. 18

ERROR REGISTER ON BIT SLICE BOARD.
(BITS 02 AND 10)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR10.02	08-04	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		219			ER00	9H3
CHSAD0	I	212	2/15		EXIN1IO	9A5
COMER0	I	116	5/2		ERIN0IO	9A3
DPWER0	I	313	5/6		ERIN1IO	9A3
ERB021	0	305	6/2,6/3		IOER01	9H1
ERB101	0	104	6/2,6/3		IOER11	9H5
FLOXER0	I	016	6/3		EXERLDIO	9A1
RSEL2B1	I	108	5/5		EXIN0IO	9A1
SHBIT090	I	316	1/1		ERCOLLIO	9A0
TERROR0	OT	007	5/13		ER0	9H4
XERCOL1C	0	IC	4/9		ERCOLL1	9B6
XISOCK0C	I	IC	6/18		ISOCK0	9D0
XTCLER0C	I	IC	6/18		TCLER0	9E0
XTSER00C	OT	IC	6/18		TSTER00	9F0
XTSER10C	OT	IC	6/18		TSTER10	9F7

SYMBOL NO. 19

ERROR REGISTER ON BIT SLICE BOARD,
(BITS 01 AND 09)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR09.01	08-03	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		219			ER00	9H3
ERB011	0	305	6/2,6/3		IOER01	9H1
ERB091	0	104	6/2,6/3		IOER11	9H5
FLOXER0	I	016	6/3		EXERLDIO	9A1
LAD170	I	212	2/14		EXIN1IO	9A5
MODEER0	I	116	5/2		ERIN0IO	9A3
RSEL1B1	I	108	5/5		EXIN0IO	9A1
SHBIT090	I	316	1/1		ERCOLLIO	9A0
TERROR0	OT	007	5/13		ER0	9H4
WTDPHER0	I	313	5/4		ERIN1IO	9A3
XERCOL1B	0	IC	4/10		ERCOLL1	9B6
XISOCK0B	I	IC	6/19		ISOCK0	9D0
XTCLER0B	I	IC	6/19		TCLER0	9E0
XTSER00B	OT	IC	6/19		TSTER00	9F0
XTSER10B	OT	IC	6/19		TSTER10	9F7

PART OF FS 5

SYMBOL(S) 12 13 14 15 16 17 18 19

MAIN STORE CONTROLLER AND MEMORY		DWG SIZE E2	ISSUE 1
BELL LABORATORIES	SD-1C902-03		85CC

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ERROR DETECTION

SYMBOL NO. 20

ERROR REGISTER ON BIT SLICE BOARD.
(BITS 00 AND 08)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ERR08.00	08-02	FA1060	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		219			ER00	9H3
ADPAPER0	I	313	5/5		ERIN110	9A3
ERB001	O	305	6/2,6/3		ICER01	9H
ERB081	O	104	6/2,6/3		ICER11	9H5
FLDXER0	I	016	6/3		EXERLDI0	9A1
FCEBTER0	I	116	5/3		ERIN0I0	9A3
LADPH0	I	212	2/17		EXIN1I0	9A5
RSEL0B1	I	108	5/5		EXIN0I0	9A1
SHBIT090	I	316	1/1		ERCOLLI0	9A0
TERROR0	OT	007	5/13		ER0	9H4
XERCOL1A	O	IC	4/11		ERCOLL1	9B6
XISOCK0A	I	IC	6/20		ISOCK0	9D0
XTCLER0A	I	IC	6/20		TCLER0	9E0
XTSER00A	OT	IC	6/20		TSTER00	9F0
XTSER10A	OT	IC	6/20		TSTER10	9F7

PART OF FS 5
SYMBOL(S) 20

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE E2 | ISSUE 1

BELL LABORATORIES | SD-1C902-03 | 85CD

PRINTED IN U. S. A.

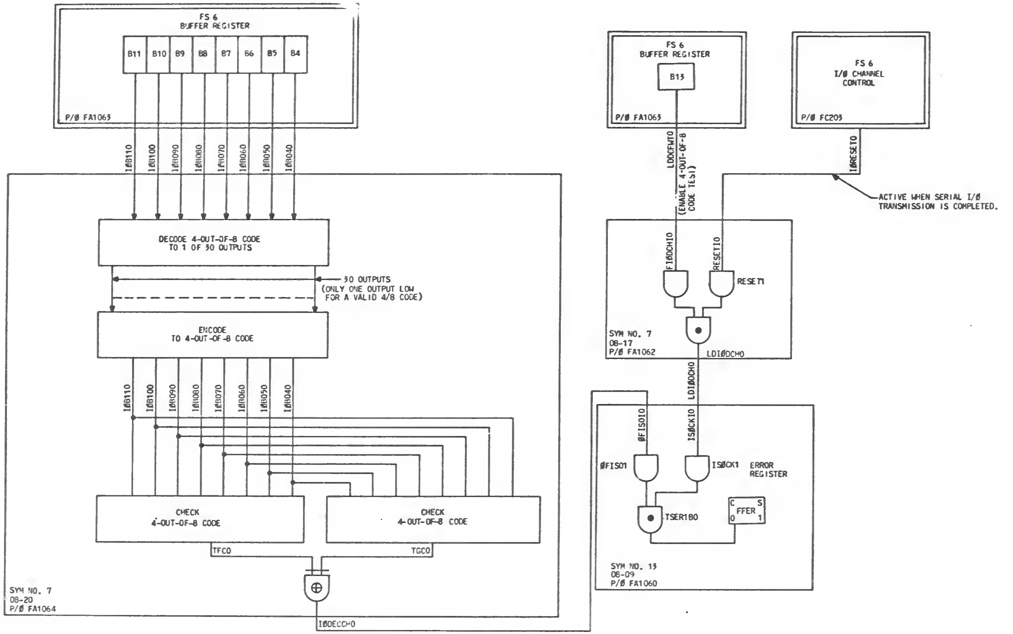
03/06/73

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 17

SERIAL I/O CHANNEL 4/8 CHECK



PART OF FS 5
COMPOSITE DIAGRAM 17

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE
6S

ISSUE
1

BELL LABORATORIES

SD-1C902-03

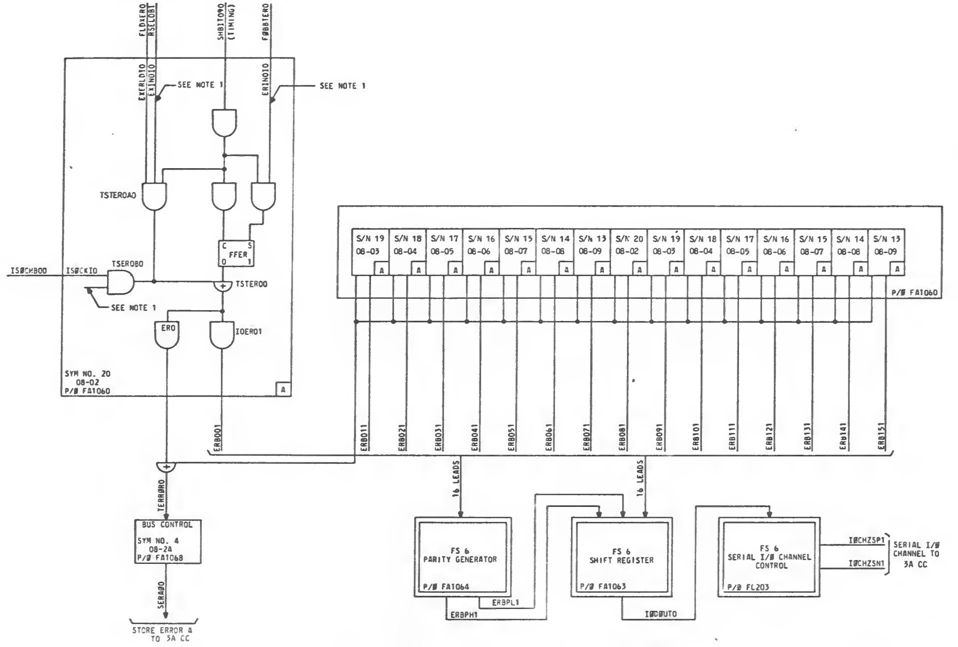
B5GL

PART OF FS 5
ERROR DETECTION

COMPOSITE DIAGRAM 1
ERROR REGISTER

NOTES:

1. INPUTS TO ERROR REGISTER SHOWN IN COMPOSITE DIAGRAM 2.



PART OF FS 5
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 6S	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B5 A

PART OF FS 5
ERROR DETECTION

COMPOSITE DIAGRAM 2
ERROR REGISTER ASSIGNMENTS

BIT	LOC
0	08-02
1	08-03
2	08-04
3	08-05
4	08-06
5	08-07
6	08-08
7	08-09
8	08-02
9	08-03
10	08-04
11	08-05
12	08-06
13	08-07
14	08-08
15	08-09

ERROR REGISTER
(ERROR LOAD)

TRMM0D	PIN	NET	FA-CODE	SOURCE TRMM0D	PIN
ERIN0IO	116	F0BBTERO	1065	BTERO	206
		M0DEERO	1062	ERO	203
		C0MERO	1062	TCERO	208
		SCMERO	1065	SCMERO	217
		SHIFTERO	1071	SHERO	306
		NSELERO	1066	NSELERO	209
		RSELERO	1066	RSELERO	108
		F0BATERO	1065	TATERO	208
ERIN1IO	313	ADPARERO	1066	TPARERO	208
		WTDPHERO	1068	WRTPHERO	109
		OPWERO	1067	TPWERO	003
		SELERO	1071	SELERO	007
		NC			
		WTDPLERO	1068	WRTPLERO	108
		NC			
		NC			

ERROR LOAD

THE INPUTS TO THE ERROR REGISTER FROM THESE SOURCES ARE THE SELF-CHECKING FEATURES OF THE MAIN STORE AND ARE DETAILED IN COMPOSITE DIAGRAMS 3 THROUGH 15.

ERROR REGISTER
(EXTERNAL LOAD)

TRMM0D	PIN	NET	FA-CODE	SOURCE TRMMOD	PIN
EXIN0IO	108	RSEL0B1	1066,1064	RSEL01	010,202
		RSEL1B1	1066,1064	RSEL11	306,008
		RSEL2B1	1066,1064	RSEL21	106,102
		RSEL3B1	1066,1064	RSEL31	309,207
		RSEL4B1	1066,1064	RSEL41	109,301
		RSEL5B1	1066,1064	RSEL51	009,007
		RSEL6B1	1066,1064	RSEL61	308,101
		NC			
EXIN1IO	212	LADPHO	1062,1066	LADPHIO	102,218
		LA0170	1065,FC203	A0170	316,304
		CHSADO	1062	SA190181	100
		NC			
		RSEL7B1	1066,1064	RSEL71	210,209
		NC			
		NC			
		NC			

EXTERNAL LOAD

BITS 0 THROUGH 6, 12

INDICATES THAT BOTH MEMORY MODULES CAN BE SELECTED DURING A REFRESH CYCLE. ERROR BIT 6 IN THE ERROR LOAD IS A SINGLE INDICATION THAT ONE OR BOTH OF THE MEMORY MODULES DID NOT GET SELECTED DURING A REFRESH CYCLE.

BIT 8

INDICATES THE STATE OF THE ADDRESS PARITY HIGH REGISTER.

BIT 9

INDICATES THE STATE OF THE ADDRESS BIT 17 REGISTER.

BIT 10

INDICATES THE STATES OF THE ADDRESS BIT 18 AND 19 REGISTERS. THIS SINGLE LEAD IS THE "OR" OF THE TWO ADDRESS BITS.

ERROR REGISTER
(ISOLATION LOAD)

TRMM0D	PIN	NET	FA-CODE	SOURCE TRMM0D	PIN
IC	IC	IC	DATA BUS FA1060		0-8
					1-9
					2-10
					3-11
					4-12
					5-13
					6-14
					7-15
0FISOIO	115	ICH0160	1060	LOC 08-10-219	
		ICH0170	1060	LOC 08-11-219	
		ICH0180	1060	LOC 08-12-219	
		ICH0190	1060	LOC 08-13-219	
		ICHDPARO	1060	LOC 08-16-219	
		ICHEPARO	1067	TBUSCKO	318
		ICHTIMO	1068	TBUSCKO	216
		I0DECCHO	1064	X0RFGO	214

ISOLATION LOAD

BITS 0 THROUGH 7

INDICATES THE STATE OF THE DATA BUS FOR THE FIRST 16 DATA BITS. EACH OF THE BITS IN THE ERROR REGISTER IS THE "OR" OF TWO DATA BITS AS FOLLOWS:

ERROR REG BIT	DATA BIT
0	0,8
1	1,9
2	2,10
3	3,11
4	4,12
5	5,13
6	6,14
7	7,15

BITS 8 THROUGH 12

SAME AS ABOVE, BUT FOR A WIDE STORE (24 DATA BITS) AS FOLLOWS:

ERROR REG BIT	DATA BIT
8	16,20
9	17,21
10	18,22
11	19,23
12	PL,PH

BIT 13

INDICATES THE STATE OF THE DATA BUS FOR DATA PARITY BITS IN A WIDE STORE. THIS SINGLE LEAD IS THE "OR" OF DATA PARITY LOW, DATA PARITY HIGH, AND EXTENDED DATA PARITY HIGH.

BIT 14

INDICATES THE STATE OF THREE BUS LEADS:
ERROR C
STORE COMPLETE
STORE BUSY
THIS SINGLE LEAD IS THE "OR" OF THE THREE BUS LEADS.

BIT 15

INDICATES THE RESULTS OF CHECKING THE SERIAL I/0 CHANNEL 4-OUT-OF-8-CODE. SEE COMPOSITE DIAGRAM 17.

PART OF FS 5
COMPOSITE DIAGRAM 2

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
65

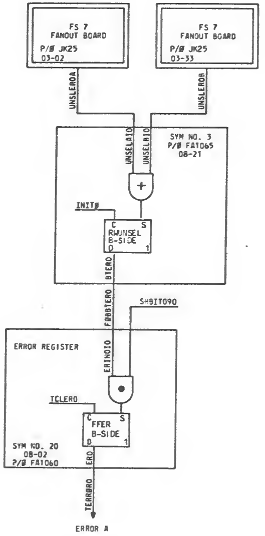
BELL LABORATORIES
SD-1C902-03

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 3

ERROR BIT 0

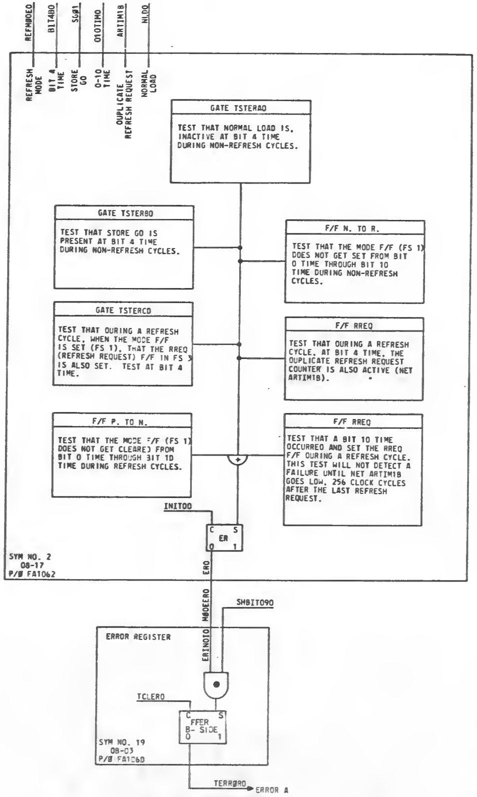


ERROR BIT 0

TEST DURING A MEMORY ACCESS CYCLE THAT A CHIP SELECT SIGNAL IS NOT ENABLED IN AN UNSELECTED MEMORY MODULE. ONLY A SINGLE, SELECTED, MEMORY MODULE SHOULD BE ACCESSED EXCEPT DURING A REFRESH CYCLE WHEN BOTH MODULES ARE SELECTED.

COMPOSITE DIAGRAM 4

ERROR BIT 1



GATE TSTERA0

TEST THAT NORMAL LOAD IS, INACTIVE AT BIT 4 TIME DURING NON-REFRESH CYCLES.

GATE TSTERB0

TEST THAT STORE GO IS PRESENT AT BIT 4 TIME DURING NON-REFRESH CYCLES.

F/F N. TO R.

TEST THAT THE MODE F/F (FS 1) DOES NOT GET SET FROM BIT 0 TIME THROUGH BIT 10 TIME DURING NON-REFRESH CYCLES.

GATE TSTERC0

TEST THAT DURING A REFRESH CYCLE, WHEN THE MODE F/F IS SET (FS 1), THAT THE RREQ (REFRESH REQUEST) F/F IN FS 3 IS ALSO SET. TEST AT BIT 4 TIME.

F/F RREQ

TEST THAT DURING A REFRESH CYCLE, AT BIT 4 TIME, THE DUPLICATE REFRESH REQUEST COUNTER IS ALSO ACTIVE (NET ARTIM1B).

F/F R. TO N.

TEST THAT THE MODE F/F (FS 1) DOES NOT GET CLEARED FROM BIT 0 TIME THROUGH BIT 10 TIME DURING REFRESH CYCLES.

F/F RREQ

TEST THAT A BIT 10 TIME OCCURRED AND SET THE RREQ F/F DURING A REFRESH CYCLE. THIS TEST WILL NOT DETECT A FAILURE UNTIL NET ARTIM1B GOES LOW, 256 CLOCK CYCLES AFTER THE LAST REFRESH REQUEST.

ERROR BIT 1

FUNCTIONAL TEST THAT MONITORS THE CONTROL OF A MAIN STORE CYCLE. SEE COMPOSITE FOR DETAILS.

PART OF FS 5
COMPOSITE DIAGRAMS 3,4

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
63

BELL LABORATORIES

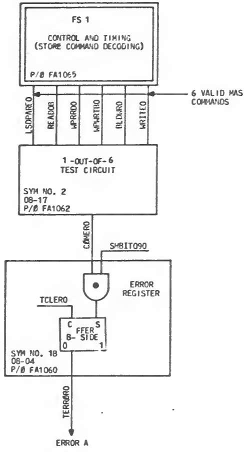
SD-1C902-03

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 5

ERROR BIT 2

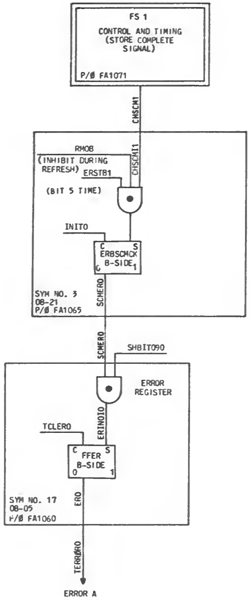


ERROR BIT 2

TEST THAT ONE AND ONLY ONE, VALID MAIN STORE COMMAND WAS DECODED FROM THE COMMAND REGISTER DURING A MAIN STORE CYCLE.

COMPOSITE DIAGRAM 6

ERROR BIT 3

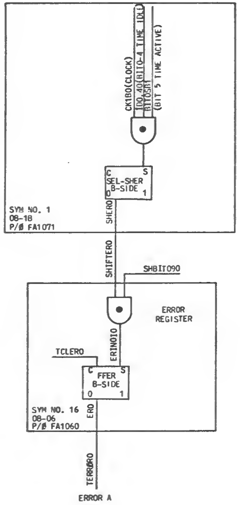


ERROR BIT 3

TEST THAT THE STORE COMPLETE SIGNAL IS NOT ACTIVE AT BIT 5 TIME DURING NON-REFRESH CYCLES. THE STORE COMPLETE SIGNAL SHOULD NOT GO ACTIVE UNTIL BIT 11 TIME.

COMPOSITE DIAGRAM 7

ERROR BIT 4



ERROR BIT 4

TEST FOR MULTIPLE BITS IN THE TIMING SHIFT REGISTER. THE CIRCUITRY TESTS AT BIT 5 TIME, THAT BITS 0 THRU 4 OF THE TIMING SHIFT REGISTER ARE INACTIVE.

PART OF FS 5
COMPOSITE DIAGRAMS 5,6,7

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

BELL LABORATORIES

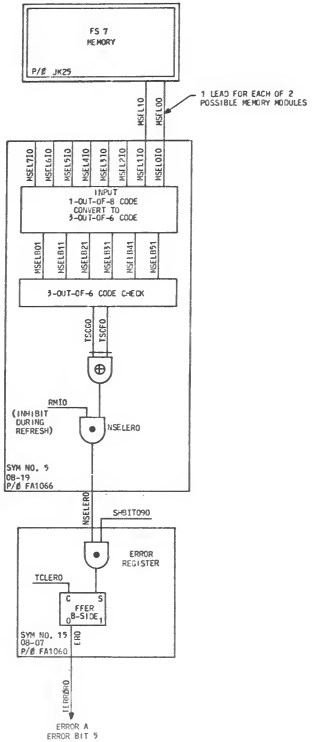
SD-1C902-03

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 8

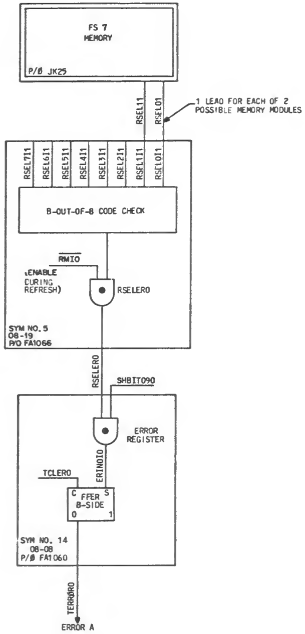
ERROR BIT 5



TEST FOR MORE THAN ONE MEMORY MODULE RESPONDING TO A MAIN STORE MEMORY ACCESS.

COMPOSITE DIAGRAM 9

ERROR BIT 6



ERROR BIT 6

TEST THAT BOTH MEMORY MODULES RESPOND DURING A REFRESH CYCLE

PART OF FS 5
COMPOSITE DIAGRAMS 8,9

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE
65

BELL LABORATORIES

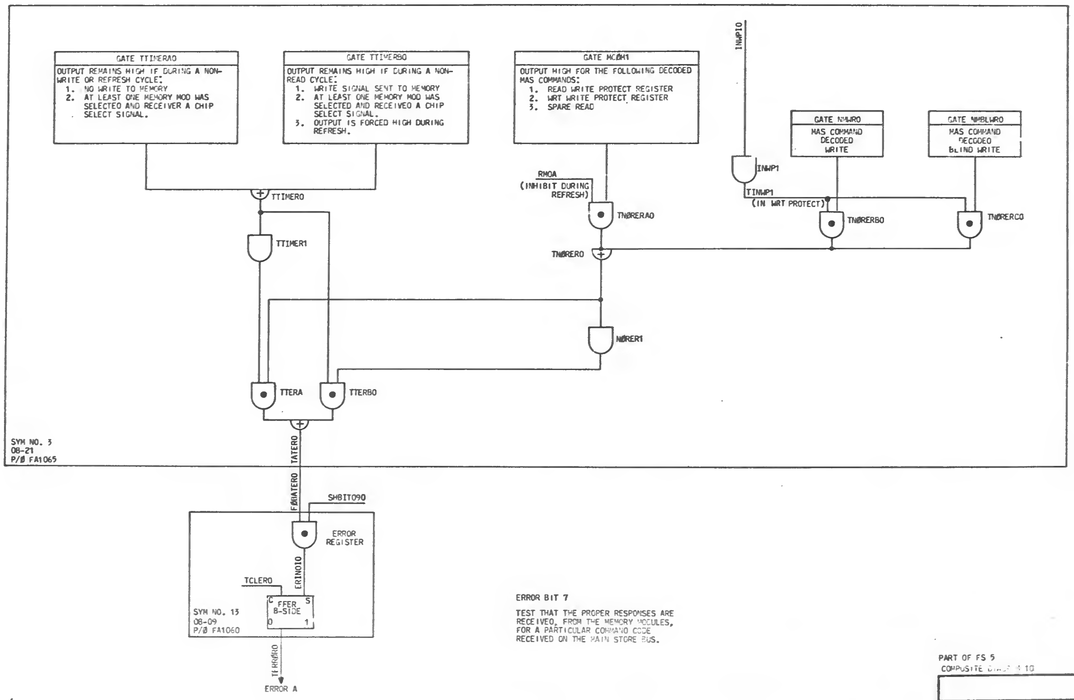
SD-1C902-03

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 10

ERROR BIT 7


GATE TTIMERAO
OUTPUT REMAINS HIGH IF DURING A NON-WRITE OR REFRESH CYCLE:
1. NO WRITE TO MEMORY
2. AT LEAST ONE MEMORY MOD WAS SELECTED AND RECEIVER A CHIP SELECT SIGNAL.
GATE TTIMERBO
OUTPUT REMAINS HIGH IF DURING A NON-READ CYCLE:
1. WRITE SIGNAL SENT TO MEMORY
2. AT LEAST ONE MEMORY MOD WAS SELECTED AND RECEIVED A CHIP SELECT SIGNAL.
3. OUTPUT IS FORCED HIGH DURING REFRESH.
GATE MC0M1
OUTPUT HIGH FOR THE FOLLOWING DECODED MAS COMMANDS:
1. READ WRITE PROTECT REGISTER
2. WRT WRITE PROTECT REGISTER
3. SPARE READ
INWPIO
GATE NMWRO
MAS COMMAND DECODED WRITE
GATE NMBLWRO
MAS COMMAND DECODED BLIND WRITE
INWP1
TINWP1
(IN WRT PROTECT)
TN0RERBO
TN0RERCO
RMOA
(INHIBIT DURING REFRESH)
TN0RERAO
TN0RERO
TTIMERO
TTIMER1
N0RER1
TTERA
TTERBO
TATERO
SYM NO. 3
08-21
P/0 FA1065
F0BATERO
SHBIT090
ERROR REGISTER
ERINO10
TCLERO
C
S
FFER B-SIDE
0
1
SYM NO. 13
08-09
P/0 FA1060
TFRR0RO
ERROR A

ERROR BIT 7

TEST THAT THE PROPER RESPONSES ARE RECEIVED, FROM THE MEMORY MODULES, FOR A PARTICULAR COMMAND CODE RECEIVED ON THE MAIN STORE BUS.

PART OF FS 5
COMPOSITE DIAG 10

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE 6S
ISSUE 1

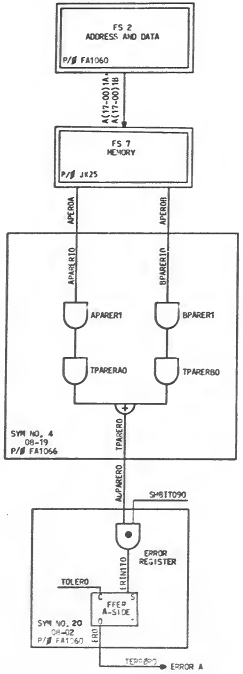
BELL LABORATORIES
SD-1C902-03
85GF

PART OF FS 5

ERROR DETECTION

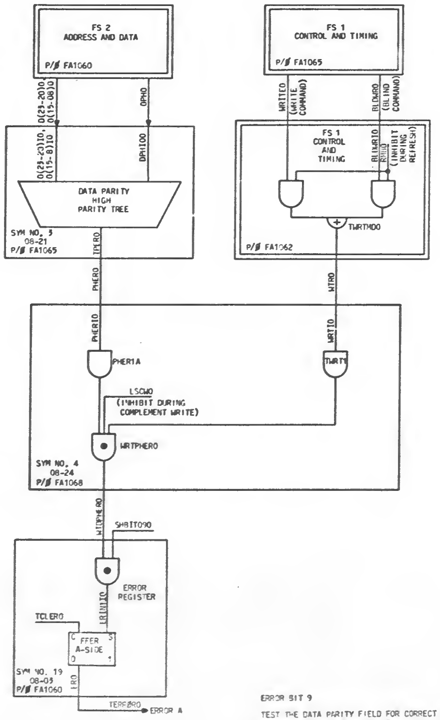
COMPOSITE DIAGRAM 11

ERROR BIT 8



COMPOSITE DIAGRAM 12

ERROR BIT 9



ERROR BIT 8

TEST FOR CORRECT ADDRESS PARITY DURING ANY MEMORY ACCESS CYCLE. ADDRESS PARITY MUST BE CORRECT FOR NORMAL OR REFRESH CYCLES.

ERROR BIT 9

TEST THE DATA PARITY FIELD FOR CORRECT PARITY DURING WRITE AND BLIND WRITE CYCLES. INHIBIT TEST DURING COMPLEMENT WRITE AND REFRESH CYCLES.

PART OF FS 5
COMPOSITE DIAGRAMS 11, 12

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

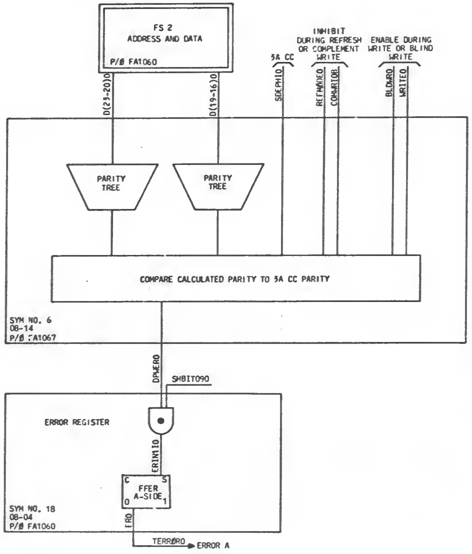
BELL LABORATORIES
SD-1C902-03

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 13

ERROR BIT 10

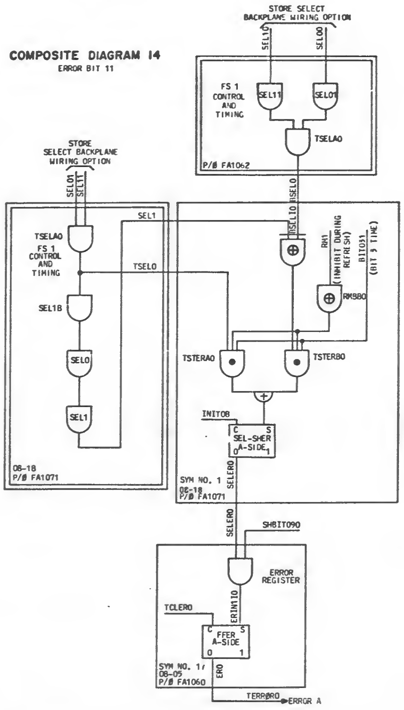


ERROR BIT 10

TEST THAT THE EXTENDED DATA PARITY HIGH BIT RECEIVED BY THE MAIN STORE CORRESPONDS TO THAT CALCULATED OVER THE SAME DATA FIELD IN THE MAIN STORE. TEST IS PERFORMED ONLY DURING WRITE AND BLIND WRITE CYCLES AND IS INHIBITED DURING THE COMPLEMENT WRITE AND REFRESH CYCLES. THIS TEST AVAILABLE ONLY ON WIDE STORES (24 DATA BITS)

COMPOSITE DIAGRAM 14

ERROR BIT 11



ERROR BIT 11

ADDRESS BITS 18 AND 19 SELECT A PARTICULAR MAIN STORE TO RESPOND TO A COMMAND. THE DECODING OF THESE TWO ADDRESS BITS IS DONE ON TWO DIFFERENT CIRCUIT PACKS, FA1071 AND FA1062. THE RESULTS OF THE DECODING IS COMPARED TO DETERMINE A MATCH.

PART OF FS 5
COMPOSITE DIAGRAM 13, 14

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
4S

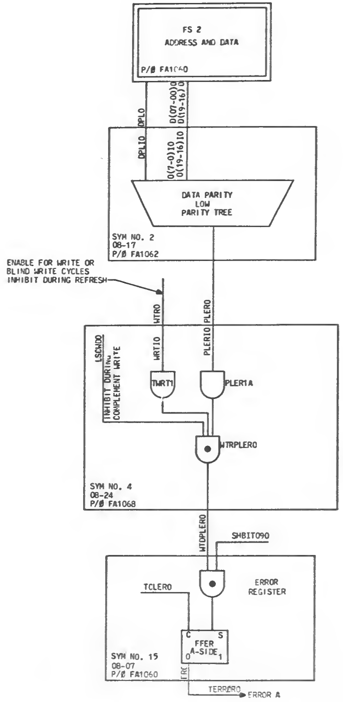
BELL LABORATORIES
SD-1C902-03

PART OF FS 5

ERROR DETECTION

COMPOSITE DIAGRAM 15

ERROR BIT 13



ERROR BIT 13

TEST THE DATA PARITY LOW FIELD FOR CORRECT PARITY DURING WRITE AND BLIND WRITE CYCLES, INHIBIT TEST DURING COMPLEMENT WRITE AND REFRESH CYCLES.

PART OF FS 5
COMPOSITE DIAGRAM 15

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
6S

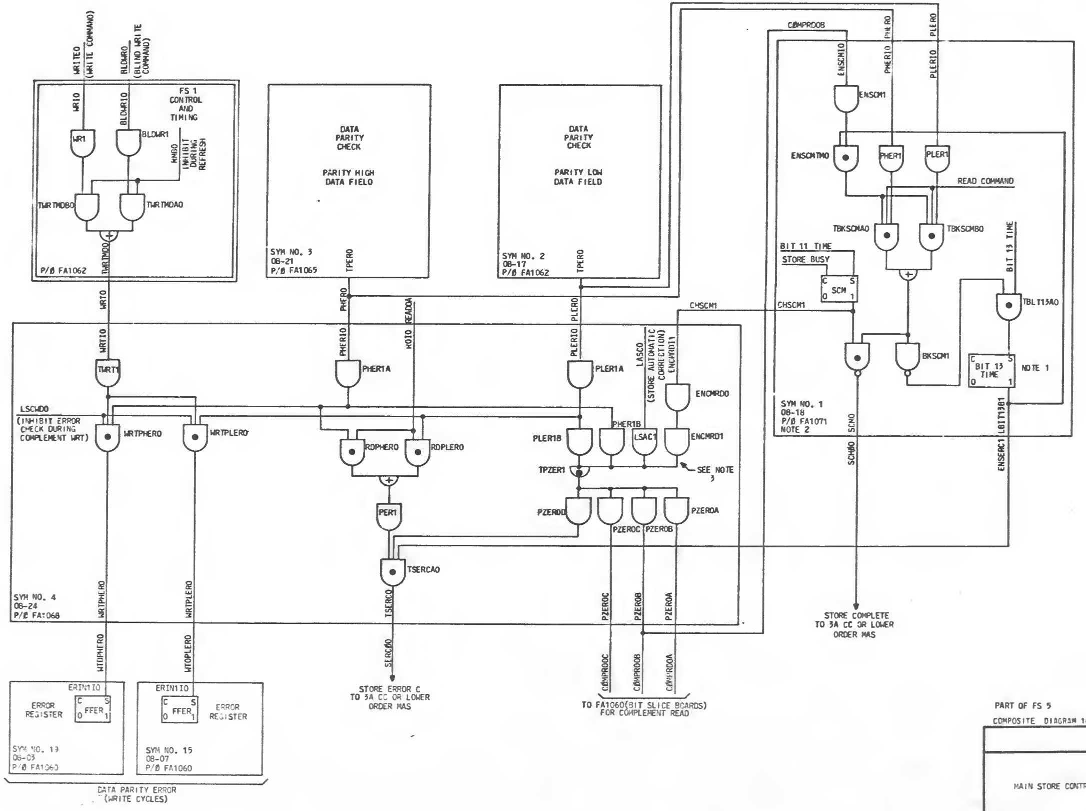
BELL LABORATORIES
SD-1C902-03

PART OF FS 5
ERROR DETECTION

COMPOSITE DIAGRAM 16
DATA PARITY CHECKS AND CONTROL

NOTE:

1. BIT 13 TIME F/F IS MADE UP OF GATES TLBT13B0 AND LBIT13A1.
2. SOME OF THE LOGIC IN THIS BOX IS PART OF FS 1.
3. ONLY ACTIVE FOR MAIN STORE AUTOMATIC CORRECTION CYCLES



PART OF FS 5

COMPOSITE DIAGRAM 16

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE

ISSUE 1

BELL LABORATORIES

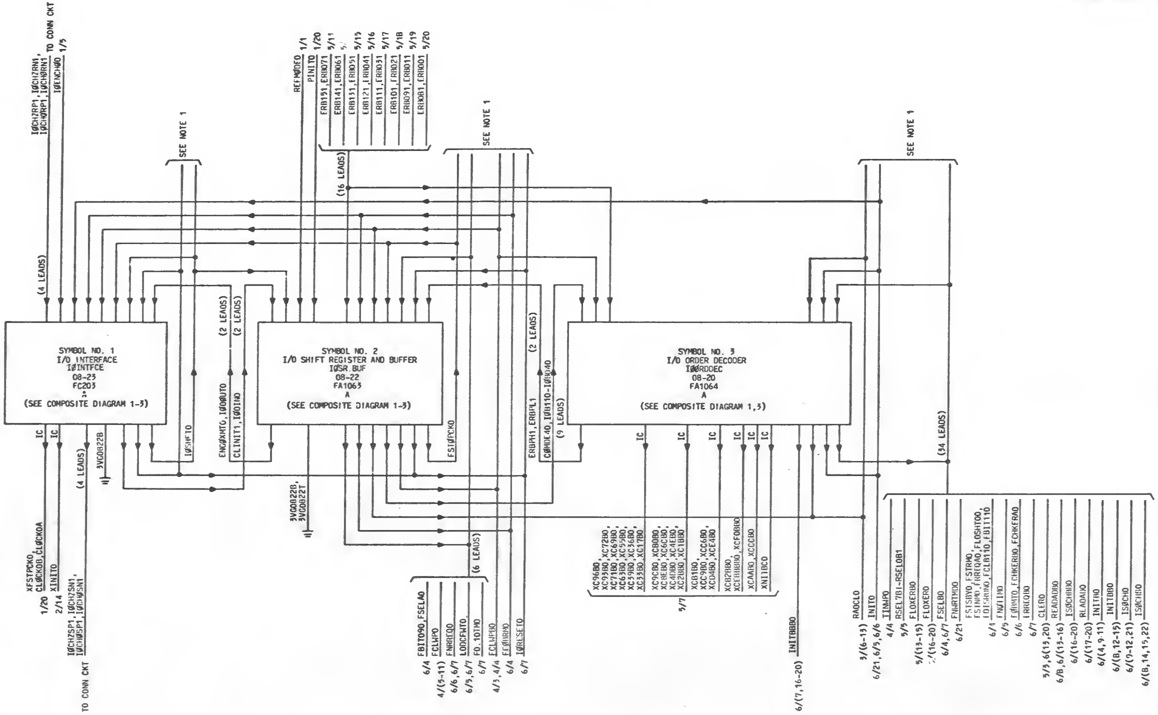
SD-IC902-03

PART OF FS 6

DIAGNOSTIC ACCESS
INTERCONNECTION AND FLOW DIAGRAM

NOTES:

1. LEADS SHOWN COULD FUNCTION AS EITHER OUTPUTS OR INPUTS (CI). ALL OFF-PAGE REFERENCING WILL BE SHOWN ON OUTPUT LEAD.

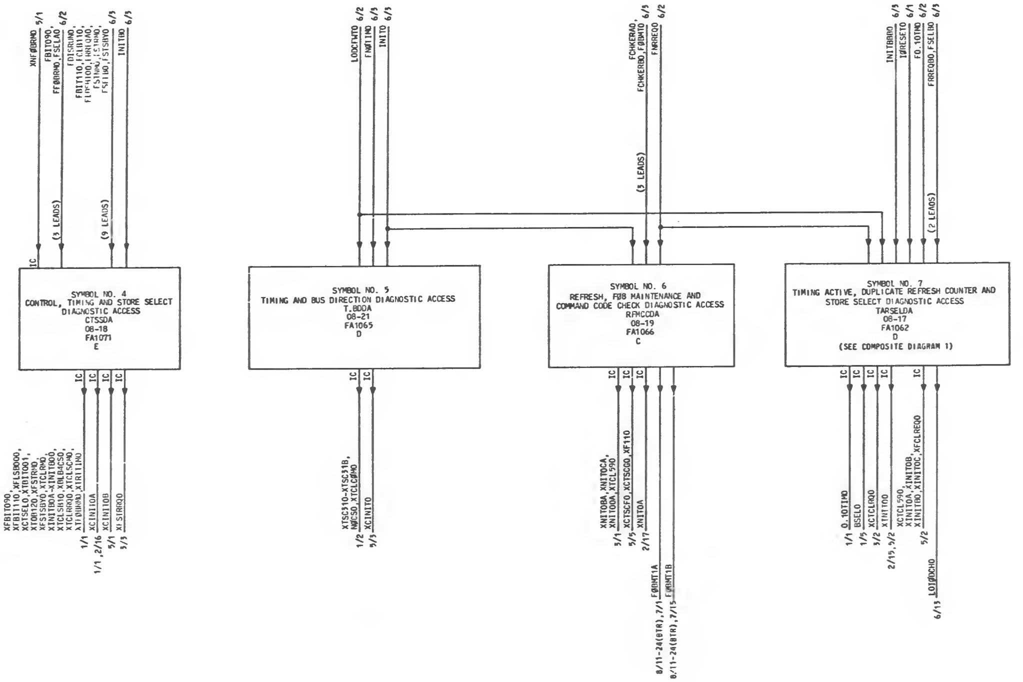


PART OF FS 6
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE BS	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B6AA

PART OF FS 6

DIAGNOSTIC ACCESS
INTERCONNECTION AND FLOW DIAGRAM


XNFØBRMO 5/1
FBIT090, FFØBRMO, FSELAO 6/2
FDISRUNO, FBIT110, FCLB110, FLPCH100, FRRLQAO, FSTRMO, FSTSBYO, FSTTBO 6/3
INITBO 6/3
IC
(3 LEADS)
(9 LEADS)
SYMBOL NO. 4
CONTROL, TIMING AND STORE SELECT DIAGNOSTIC ACCESS
CTSSDA
08-18
FA1071
E
IC
IC
IC
IC
XFBIT090, XFBIT110, XFLSB000, XCTSELO, XTBIT001, XTOB120, XFSTRMO, XFSTSBYO, XTCLRMO, XINITBOA-XINITBOO, XTCLSH10, XBLB4CSO, XTCLRRQO, XTCLSCMO, XTFØURMO, XTRITIMO 1/1
XCINITOA 1/1, 2/16
XCINITOB 5/1
XFSTRRQO 3/3
LODCFWTO 6/2
FNØTIMO 6/3
INITO 6/3
SYMBOL NO. 5
TIMING AND BUS DIRECTION DIAGNOSTIC ACCESS
T.BDDA
08-21
FA1065
D
IC
IC
XTSC310-XTSC31B, NØCSO, XTCLCØMO 1/2
XCINITO 3/3
FCHKERAO, FCHKERBO, FØBMTO 6/3
FNRREQO 6/2
(3 LEADS)
SYMBOL NO. 6
REFRESH, FØB MAINTENANCE AND COMMAND CODE CHECK DIAGNOSTIC ACCESS
RFMCCDA
08-19
FA1066
C
IC
IC
IC
XNITOBA, XNITOCA, XNITOOA, XTCL590 3/1
XCTSCFO, XCTSCGO, XF110 5/5
XNITOA 2/17
FØBMT1A 8/11-24(BTR), 7/1
FØBMT1B 8/11-24(BTR), 7/15
INITBBBO 6/3
IØRESETO 6/1
FO.10TMO 6/2
FRREQBO, FSELBO 6/3
(2 LEADS)
SYMBOL NO. 7
TIMING ACTIVE, DUPLICATE REFRESH COUNTER AND STORE SELECT DIAGNOSTIC ACCESS
TARSELDA
08-17
FA1062
D
(SEE COMPOSITE DIAGRAM 1)
IC
IC
IC
IC
IC
0.10TIMO 1/1
BSELO 1/5
XCTCLRQO 3/2
XINITOO 2/15, 5/2
XCTCL590, XINITOA, XINITOB, XINITBO, XINITOC, XFCLREQO 5/2
LOIØDCHO 6/13

PART OF FS 6
INTERCONNECTION AND FLOW DIAGRAM

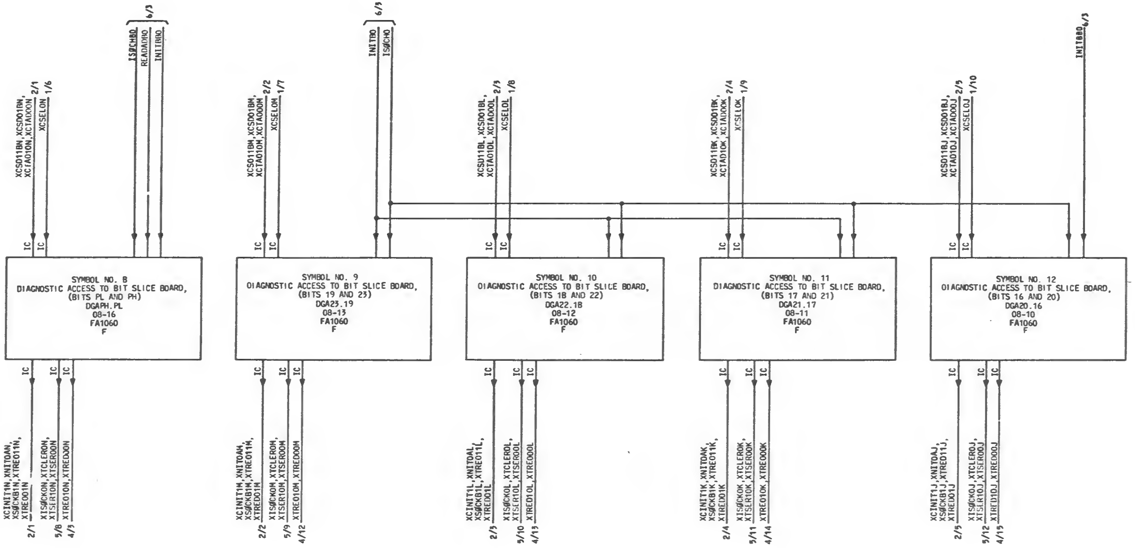
MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
65	1

BELL LABORATORIES | SD-1C902-03 | B6AB

PART OF FS 6

DIAGNOSTIC ACCESS
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 6
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

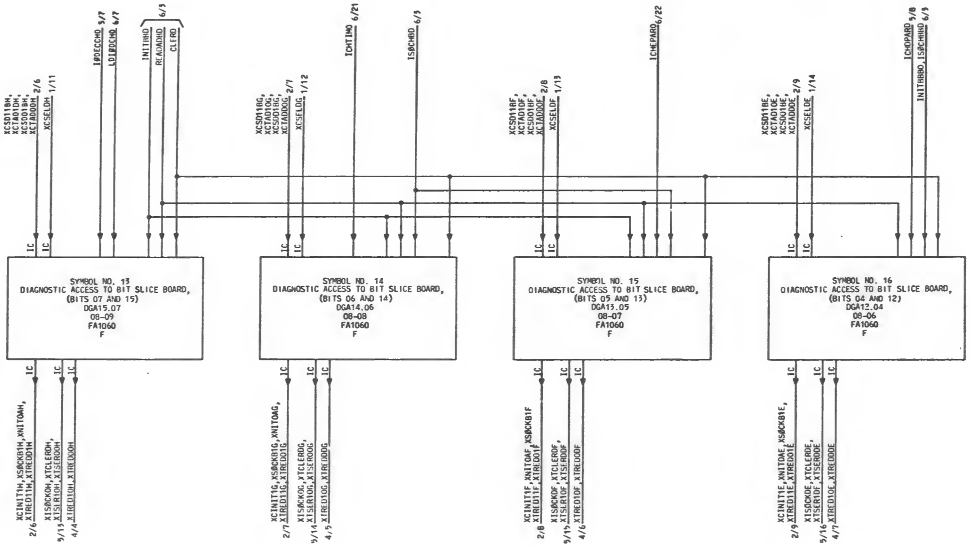
DWG SIZE
6S

ISSUE
1

BELL LABORATORIES
SD-1C902-03
B6AC

PART OF FS 6

DIAGNOSTIC ACCESS
INTERCONNECTION AND FLOW DIAGRAM


XCSD11BH, XCTAD1DH, XCSD01BH, XCTAD00H 2/6
XCSELDH 1/11
IØDECCHO 5/7
LDIØDCHO 6/7
INITBBD
READADBD
CLERD
6/3
ICHTIMO 6/21
ISØCHBD 6/3
XCSD11BG, XCTAD1OG, XCSDD1BG, XCTADDOG 2/7
XCSELDG 1/12
XCSD11BF, XCTAD1DF, XCSD01BF, XCTADOOF 2/8
XCSELDF 1/13
ICHEPARO 6/22
XCSD11BE, XCTAD1OE, XCSDO1BE, XCTADDDE 2/9
XCSELDE 1/14
ICHOPARD 5/8
INITBBBO, ISØCHIIID 6/3
IC
SYMBOL NO. 13
DIAGNOSTIC ACCESS TO BIT SLICE BOARD,
(BITS 07 AND 15)
DGA15.07
08-09
FA1060
F
SYMBOL NO. 14
DIAGNOSTIC ACCESS TO BIT SLICE BOARD,
(BITS 06 AND 14)
DGA14.06
08-08
FA1060
F
SYMBOL NO. 15
OIAGNOSTIC ACCESS TO BIT SLICE BOARD,
(BITS 05 AND 13)
DGA13.05
08-07
FA1060
F
SYMBOL NO. 16
OIAGNOSTIC ACCESS TO BIT SLICE BOARD,
(BITS 04 AND 12)
DGA12.04
08-06
FA1060
F
XCINIT1H, XSØCKB1H, XNITOAH, XTRED11H, XTREDD1H 2/6
XISØCKOH, XTCLERDH, XTSLR1OH, XTSEROOH 5/13
XTRED1OH, XTREDOOH 4/4
XCINIT1G, XSØCKB1G, XNITOAG, XTRED11G, XTREDO1G 2/7
XISØCKOG, XTCLERDG, XTSER1OG, XTSEROOG 5/14
XTRED1OG, XTREDDDG 4/5
XCINIT1F, XNITOAF, XSØCKB1F, XTRED11F, XTREDO1F 2/8
XISØCKDF, XTCLERDF, XTSLR1OF, XTSERDDF 5/15
XTRED1DF, XTREDODF 4/6
XCINIT1E, XNITDAE, XSØCKB1E, XTRED11E, XTREDO1E 2/9
XISØCKDE, XTCLERDE, XTSER1DF, XTSERDDE 5/16
XTRED1OE, XTREDDDE 4/7

PART OF FS 6
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

②

DWG SIZE
ßS

ISSUE
1

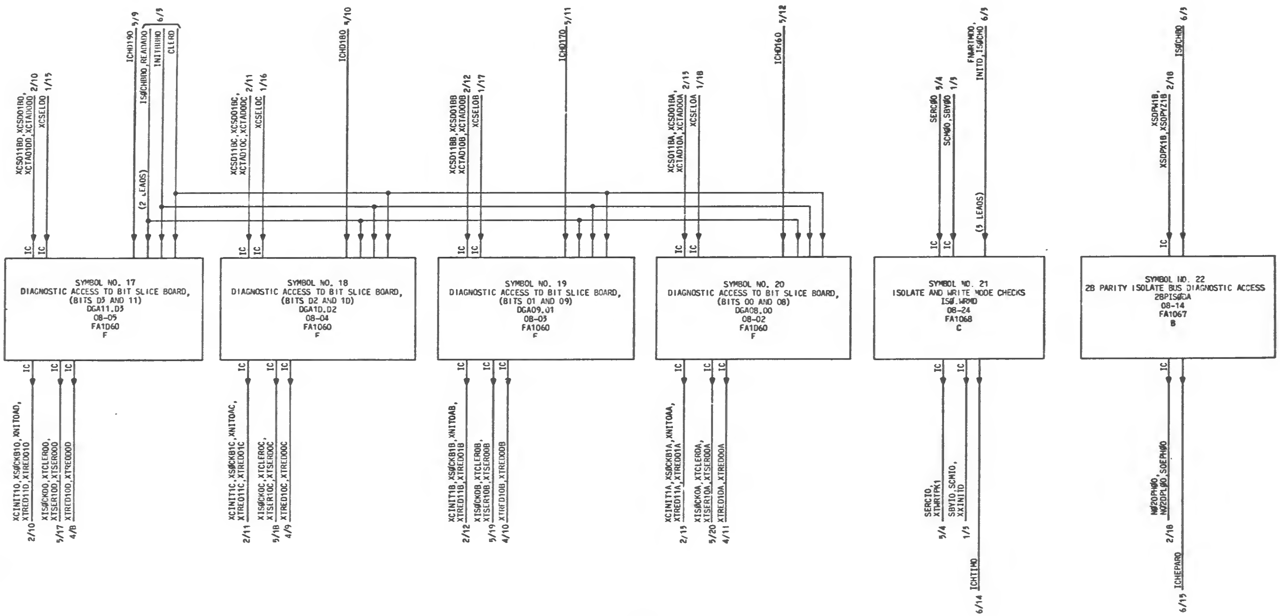
BELL LABORATORIES

SD-1C902-03

86AD

PART OF FS 6

DIAGNOSTIC ACCESS
INTERCONNECTION AND FLOW DIAGRAM


XCSO11BD, XCSD01BD, XCTAD1DD, XCTAD0D0 2/10
XCSELDD 1/15
ICHD190 5/9
IS0CHBD0, READAD0
INITBBH0
CLERO
6/5
(2 LEADS)
ICHD180 5/10
XCSD11BC, XCSD01BC, XCTAD10C, XCTAD0DC 2/11
XCSELOC 1/16
XCSD11BB, XCSD01BB, XCTAD10B, XCTAD00B 2/12
XCSEL0B 1/17
ICHD170 5/11
XCSO11BA, XCSD01BA, XCTAD10A, XCTAD0OA 2/15
XCSELOA 1/18
ICHD160 5/12
SERC00 5/4
SCM00, SBY00 1/5
FNWRTMD0, INITD, IS0CHO 6/5
(5 LEADS)
XSDPW1B, XSDPX1B, XSOPYZ1B 2/18
IS0CHB0 6/5
IC
SYMBOL NO. 17
DIAGNOSTIC ACCESS TD BIT SLICE BOARD,
(BITS D3 AND 11)
DGA11.D3
08-05
FA1D60
F
SYMBOL NO. 18
DIAGNOSTIC ACCESS TD BIT SLICE BOARD,
(BITS D2 AND 1D)
DGA1D.D2
08-04
FA1060
F
SYMBOL NO. 19
DIAGNOSTIC ACCESS TD BIT SLICE BOARD,
(BITS 01 AND 09)
DGA09.01
08-03
FA1060
F
SYMBOL NO. 20
DIAGNOSTIC ACCESS TO BIT SLICE BOARD,
(BITS 00 AND 08)
DGA08.00
08-02
FA1D60
F
SYMBOL NO. 21
ISOLATE AND WRITE MODE CHECKS
IS0.WRMD
08-24
FA1068
C
SYMBOL NO. 22
2B PARITY ISOLATE BUS DIAGNOSTIC ACCESS
2BPIS0DA
08-14
FA1067
B
XCINIT10, XS0CKB10, XNITOAD, XTRED11D, XTRED010 2/10
XIS0CKD0, XTCLER00, XTSER10D, XTSER000 5/17
XTRED10D, XTREDD0D 4/8
XCINIT1C, XS0CKB1C, XNITOAC, XTRED11C, XTRED01C 2/11
XIS0CKOC, XTCLEROC, XTSER10C, XTSERD0C 5/18
XTRED10C, XTRED00C 4/9
XCINIT1B, XS0CKB1B, XNITOAB, XTRED11B, XTRED01B 2/12
XIS0CKDB, XTCLEROB, XTSER10B, XTSER00B 5/19
XTRED10B, XTREDD0B 4/10
XCINIT1A, XS0CKB1A, XNITOAA, XTRED11A, XTRED01A 2/15
XIS0CKOA, XTCLERDA, XTSER10A, XTSER00A 5/20
XTRED1DA, XTREDOOA 4/11
SERCIO, XTWRTPK1 5/4
SBYIO, SCMIO, XXINITD 1/5
ICHTIM0 6/14
N020PH00, N02DPL00, SOEPH00 2/18
ICHEPARO 6/15

PART OF FS 6
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE 65

ISSUE 1

BELL LABORATORIES

SD-1C902-03

B6AE

PART OF FS 6

DIAGNOSTIC ACCESS

SYMBOL NO. 1

I/O INTERFACE

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOINTFCE	08-23	FC203	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLINIT1	0	010	6/2		CLINIT1	3B2
CLOCKCA	OT	IC204	1/20		TCKOA	2E2
CLOCKOB	OT	IC202	1/20		TCKOB	2E
ENGOXMTO	I	102	6/2		SHSTPIO	3B1
FCLWFBO	I	013	6/2		BUFBOIO	3B1
FFOBRMO	I	207	6/2		FCKIO	2B2
FSTOPCKO	I	106	6/2		FSTPCKIO	2B2
INITO	I	303	6/3		INITIO	2B2
IOCHORN1	I	218	TO CONN CKT		IONI1	3B5
IOCHORP1	I	318	TO CONN CKT		IOPI1	3B6
IOCHOSN1	0	213	TO CONN CKT		OON1	3G7
IOCHOSP1	0	313	TO CONN CKT		OOP1	3G7
IOCHZRN1	I	316	TO CONN CKT		IZNI1	3B5
IOCHZRP1	I	317	TO CONN CKT		IZPI1	3B5
IOCHZSN1	0	214	TO CONN CKT		OZN1	3G7
IOCHZSP1	0	314	TO CONN CKT		OZP1	3G6
IODINO	0	014	6/2		OINO	3G5
IODOUTO	I	113	6/2		DOUTIO	3B6
IOENCHCO	I	003	1/5		CLOINIO	3B0
IORESETO	OI	306	6/2,6/7		RESETO	3G1
IOSHFTO	OI	103	6/2		SHFTO	3G5
XFSTPCKO	0	IC	1/20		FSTPCKO	2E2
XINITO	0	IC	2/14		INITO	2D2
3T046B	GRD	0GD		202	GRD	
	GRD	001		202		
	GRD	101		202		
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	201		202		
	GRD	301		202		
	GRD	319		202	GRD	
3VG0822B	GRD	200	6/2		GRD	
3V046B	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 2

I/O SHIFT REGISTER AND BUFFER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOSR.BUF	08-22	FA1063	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		010			PINITOB	11H1
		109			XMITERB1	4H1
		212			CNT.300	2H3
		306			PARDIIO	5H2
CLINIT1	I	012	6/1		CLINIT1	11A5
COMDE40	0	308	6/3		E4B0	2H0
ENGDXMTO	0	219	6/1		SHSTPDA	2H5
ERBPH1	I	019	6/3		ERPHI1	5A3
ERBPL1	I	216	6/3		ERPLI1	6A7

SYMBOL NO. 2 (CONT)

I/O SHIFT REGISTER AND BUFFER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOSR.BUF	08-22	FA1063	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
ERB001	I	013	5/20		ERB0I1	8A2
ERB011	I	213	5/19		ERB1I1	7A9
ERB021	I	113	5/18		ERB2I1	7A7
ERB031	I	313	5/17		ERB3I1	7A6
ERB041	I	215	5/16		ERB4I1	7A4
ERB051	I	115	5/15		ERB5I1	7A2
ERB061	I	315	5/14		ERB6I1	7A1
ERB071	I	016	5/13		ERB7I1	6A8
ERB081	I	116	5/20		ERB8I1	6A5
ERB091	I	316	5/19		ERB9I1	6A4
ERB101	I	017	5/18		ERB10I1	6A2
ERB111	I	317	5/17		ERB11I1	6A1
ERB121	I	018	5/16		ERB12I1	5A9
ERB131	I	218	5/15		ERB13I1	5A7
ERB141	I	118	5/14		ERB14I1	5A6
ERB151	I	318	5/13		ERB15I1	5A4
FBIT090	OI	117	6/4		B150	9H2
FCLWPB0	OI	111	4/3,4/4 6/1,6/3		B080	11H6
FCLWPO	OI	311	4/5,4/6 4/7,4/8 4/9,4/10 4/11		B00	11H7
FFOBRMO	OI	011	6/1,6/4		B10	11H5
FNRREQO	OI	007	6/6,6/7		B30	11H2
FSELAO	OI	217	6/4		B140	9H2
FSTOPCKO	OI	206	6/1		B20	11H3
FO.10TMO	OI	314	6/7		B120	9H4
IOB040	0	202	6/3		B40	10H6
IOB050	0	301	6/3		B50	10H6
IOB060	0	303	6/3		B60	10H4
IOB070	0	103	6/3		B70	10H4
IOB080	0	300	6/3		B80	10H2
IOB090	0	100	6/3		B90	10H1
IOB100	0	114	6/3		B100	9H6
IOB110	0	214	6/3		B110	9H6
IODINO	I	006	6/1		OINIO	5A3
IODOUTO	0	204	6/1		DOUTO	8H5
IORESETO	IO	312	6/1		TRESETIO	11A0
IOSHFTO	I	105	6/1		SHIFTIO	3A5
LDDCFMTO	OI	015	6/5,6/7		B130	9H4
PINITO	I	110	1/20		PINITIO	11A1
RADCLO	0	309	6/3 3/6,3/7 3/8,3/9 3/10,3/11 3/12,3/13		PINITOA	11H1
PERMODEO	I	112	1/1		RMIO	11A4
3T046A	GRD	0GD		202	GRD	
	GRD	005		202	GRD005	2D9
	GRD	102		202	GRD102	2D9
	GRD	107		202	GRD107	2D9
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	205		202	GRD205	2D9
	GRD	302		202	GRD302	2D9
	GRD	307		202	GRD307	2E9
	GRD	319		202	GRD	
3VG0822B	GRD	003	6/1		GRD003	2C9
	GRD	008	6/1		GRD008	2E9
	GRD	104	6/1		GRD104	2C9
	GRD	108	6/1		GRD108	2E9

SYMBOL NO. 2 (CONT)

I/O SHIFT REGISTER AND BUFFER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOSR.BUF	08-22	FA1063	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3VG0822B	GRD	203	6/1		GRD203	2C9
	GRD	207	6/1		GRD207	2E9
	GRD	208	6/1		GRD208	2E9
	GRD	304	6/1		GRD304	2C9
3VG0822T	GRD	014			GRD014	2F9
	GRD	211			GRD211	2F9
	GRD	310			GRD310	2F9
	GRD	319			GRD	
3V046A	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 3

I/O ORDER DECODER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOORDDEC	08-20	FA1064	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLERO	OI	305	5/3,6/13 6/14,6/15 6/16,6/17 6/18,6/19 6/20		6C0	4G4
COMDE40	I	219	6/2		E410	5A5
ERBPH1	0	112	6/2		ERPARH1	10G8
ERBPL1	C	019	6/2		ERPARL1	10G4
ERB001	I	015	5/20		ERB0I1	10A1
ERB011	I	314	5/19		ERB1I1	10A1
ERB021	I	017	5/18		ERB2I1	10A1
ERB031	I	316	5/17		ERB3I1	10A2
ERB041	I	118	5/16		ERB4I1	10A2
ERB051	I	218	5/15		ERB5I1	10A3
ERB061	I	018	5/14		ERB6I1	10A3
ERB071	I	317	5/13		ERB7I1	10A4
ERB081	I	117	5/20		ERB8I1	10A5
ERB091	I	217	5/19		ERB9I1	10A5
ERB101	I	116	5/18		ERB10I1	10A6
ERB111	I	216	5/17		ERB11I1	10A6
ERB121	I	016	5/16		ERB12I1	10A7
ERB131	I	315	5/15		ERB13I1	10A7
ERB141	I	115	5/14		ERB14I1	10A8
ERB151	I	215	5/13		ERB15I1	10A8
FBIT110	OI	109	6/4		CC0	3H1
FCHKERAO	OI	210	6/6		2B0	3H7
FCHKERBO	OI	003	6/6		170	3H8
FCLB110	OI	103	6/4		720	4G6
FCLWPBO	I	318	6/2		WPINITIO	5A6
FDISPUNO	OI	206	6/4		630	4G8
FLDSHTOO	OI	006	6/4		C60	4G2
FLDXEPBO	OI	010	5/13,5/14 5/15		82B0	5H4
FLDXEPO	OI	110	5/16,5/17 5/18,5/19 5/20		820	5H4
FNOTIMO	OI	104	6/5		930	4G7

SYMBOL NO. 3 (CONT)

I/O ORDER DECODER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOORDDEC	08-20	FA1064	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FNWRTMOO	OI	303	6/21		810	[illegible]
FOEMTO	OI	111	6/6		360	[illegible]
FRREQAO	OI	304	6/4		800	[illegible]
FRREQBO	OI	204	6/7		180	[illegible]
FSELBO	OI	306	6/4,6/7		920	[illegible]
FSTNMO	OI	105	6/4		4E0	[illegible]
FSTRMO	OI	011	6/4		D40	[illegible]
FSTSBYO	OI	004	6/4		390	[illegible]
INITBBB0	0	212	6/7,6/16 6/17,6/18 6/19,6/20		INITBBB0	[illegible]
INITBB0	OI	012	6/8,6/12 6/13,6/14 6/15		INITBB0	[illegible]
INITB0	OI	311	6/4,6/9 6/10,6/11		INITO	[illegible]
INITO	OI	310	6/1,6/5 6/6,6/21		TINITG	[illegible]
IOB040	I	113	6/2		B410	[illegible]
IOB050	I	313	6/2		B510	[illegible]
IOB060	I	213	6/2		B610	[illegible]
IOB070	I	014	6/2		B710	[illegible]
IOB080	I	208	6/2		B810	[illegible]
IOB090	I	009	6/2		B910	[illegible]
IOB100	I	308	6/2		B1010	[illegible]
IOB110	I	108	6/2		B1110	[illegible]
ISOCHBB0	OI	211	6/16,6/17 6/18,6/19 6/20		E8B0	[illegible]
ISOCHB0	OI	312	6/8,6/14 6/15,6/22		E8B0	[illegible]
ISOCHO	OI	114	6/9,6/10 6/11,6/12 6/21		E80	[illegible]
RADCLO	OI	106	6/2		0F0	[illegible]
READADB0	OI	309	6/8,6/13 6/14,6/15 6/16		F080	[illegible]
READADO	OI	013	6/17,6/18 6/19,6/20		F08	[illegible]
RSEL0B1	OI	202	5/5		330	[illegible]
RSEL1B1	OI	008	5/5		400	[illegible]
RSEL2B1	OI	002	5/5		550	[illegible]
RSEL3B1	OI	207	5/5		690	[illegible]
RSEL4B1	OI	301	5/5		730	[illegible]
RSEL5B1	OI	007	5/5		8E0	[illegible]
RSEL6B1	OI	101	5/5		960	[illegible]
RSEL7B1	OI	209	5/5		AA0	[illegible]
TINWPO	OI	203	4/4		C90	[illegible]
XCAAB0	0	IC	5/7		AAB0	[illegible]
XCB1B0	0	IC	5/7		B1B0	[illegible]
XCB2BB0	0	IC	5/7		B2B0	[illegible]
XCCCB0	0	IC	5/7		CCB0	[illegible]

PART OF FS 6

SYMBOL(S) 1 2 3

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE

BELL LABORATORIES

SD-1C902-03

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PART OF FS 6

DIAGNOSTIC ACCESS

SYMBOL NO. 3 (CONT)

I/O ORDER DECODER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOORDDEC	08-20	FA1064	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XCC6B0	0	IC	5/7		C6B0	4G2
XCC9B0	0	IC	5/7		C9B0	4G6
XCD4B0	0	IC	5/7		D4B0	3G.
XCE4B0	0	IC	5/7		E4B0	5H5
XCE8BBB0	0	IC	5/7		E8BBB0	5H3
XCF0BB0	0	IC	5/7		F0BB0	5H1
XC1BB0	0	IC	5/7		1BB0	4G3
XC17B0	0	IC	5/7		17B0	3G8
XC2BB0	0	IC	5/7		2BB0	3G7
XC33B0	0	IC	5/7		33B0	3G6
XC36B0	0	IC	5/7		36B0	4G0
XC39B0	0	IC	5/7		39B0	4G5
XC40B0	0	IC	5/7		40B0	3G6
XC4EB0	0	IC	5/7		4EB0	4G4
XC55B0	0	IC	5/7		55B0	3G5
XC6CB0	0	IC	5/7		6CB0	4G4
XC63B0	0	IC	5/7		63B0	4G8
XC69B0	0	IC	5/7		69B0	3G4
XC71B0	0	IC	5/7		71B0	3G4
XC72B0	0	IC	5/7		72B0	4G6
XC80B0	0	IC	5/7		80B0	4G2
XC8EB0	0	IC	5/7		8EB0	3G3
XC9CB0	0	IC	5/7		9CB0	4G9
XC93B0	0	IC	5/7		93B0	4G7
XC96B0	0	IC	5/7		96B0	3G2
XNITBC0	0	IC	5/7		INITBC0	5G8
3T046C	GRD	0GD		202	GRD	
	GRD	005		202	GRD005	2H5
	GRD	102		202	GRD102	2H5
	GRD	107		202	GRD107	2H5
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	205		202	GRD205	2H4
	GRD	302		202	GRD302	2H4
	GRD	307		202	GRD307	2H4
	GRD	319		202	GRD	
3V046C	PWR	000		202	+3	
	PWR	119		202	+3	

SYMBOL NO. 4

CONTROL, TIMING AND STORE SELECT
DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CTSS0A	08-18	FA1071	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FBIT090	I	312	6/2		FBIT09I0	11A0
FBIT110	I	013	6/3		FBIT11I0	11A3
FCLB110	I	017	6/3		FDBT2I0	10A5
FOISRUN0	I	215	6/3		FDCIDLI0	11A3
FFOBRM0	I	113	6/2		FFOBRMI0	11A1
FLOSHT00	I	317	6/3		FLSHCOI0	11A0
FRREQA0	I	218	6/3		FSTRRQI0	11A6
FSELA0	I	018	6/2		FSELI0	11A5
FSELB0	I	304	6/3		BLB4CSI0	10A6
FSTNM0	I	016	6/3		FCLRMI0	10A0
FSTRM0	I	315	6/3		FSTRMI0	11A2
FSTSBY0	I	117	6/3		FSTSBYI0	11A2
INITB0	I	216	6/3		INITI0	10A1
XBLB4CS0	0	IC	1/1		BLB4CS0	10H6
XCINITOA	0	IC	1/1,2/16		INITOA	10H1
XCINITOB	0	IC	5/1		INITOB	1006
XCTSEL0	OT	IC	1/1		TSEL0	11G5
XFBIT090	0	IC	1/1		FBIT090	11G0
XFBIT110	0	IC	1/1		FBIT110	11G3
XFLSB000	0	IC	1/1		FLSHBC00	11G0
XFSTRM0	0	IC	1/1		FSTRM0	11G2
XFSTRR00	0	IC	3/3		FSTRRQ0	11G6
XFSTSBY0	0	IC	1/1		FSTSBY0	11G2
XINITB0A	0	IC	1/1		INITB0A	10H3
XINITB0B	0	IC	1/1		INITB0B	10H3
XINITB0C	0	IC	1/1		INITB0C	10H4
XINITB00	0	IC	1/1		INITBC0	10H4
XNFOBRM0	I	IC	5/1		NOFOBRM0	11A1
XTBIT001	OT	IC	1/1		TBIT001	11G3
XTCLRM0	OT	IC	1/1		TCLRM0	10H1
XTCLRR00	OT	IC	1/1		TCLRRQ0	10H2
XTCLSB10	OT	IC	1/1		TCLSB10	10H4
XTCLSCM0	OT	IC	1/1		TCLSCM0	10H1
XTDB120	0	IC	1/1		TDB120	10H5
XTFOBRM0	OT	IC	1/1		TFOBRM0	11G1
XTRTTIM0	OT	IC	1/1		TRTTIM0	10H1

SYMBOL NO. 5

TIMING AND BUS DIRECTION DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
T.BDDA	08-21	FA1065	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FNOTIM0	I	018	6/3		FNOTIMI0	11A4
INIT0	I	010	6/3		INITI0	11A2
LDOCFWT0	I	114	6/2		FBUSWTI0	11A4
NOCS0	OT	IC307	1/2		TNOCS0	11H3
XCINIT0	0	IC	5/3		INIT0	11H1
XTCLCOM0	OT	IC	1/2		TCLCOM0	11H2
XTSC31B	OT	IC	1/2		TSC31B	11H5
XTSC31C	OT	IC	1/2		TSC31C	11H5
XTSC310	OT	IC	1/2		TSC310	11H4

SYMBOL NO. 6

REFRESH, FOB MAINTENANCE AND
COMMAND CODE CHECK DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
RFMCCDA	08-19	FA1066	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCHWERA0	I	118	6/3		F11I0	6A4
FCHWERB0	I	112	6/3		F00I0	6A5
FNRREQ0	I	004	6/2		FNRREQI0	6A3
FOBMT0	I	001	6/3		FOBMTI0	6A0
FOBMT1A	0	300	7/1 8/11-24(BTR)		FOBMT1A	6H1
FOBMT1B	0	100	7/15 8/11-24(BTR)		FOBMT1B	6H0
INIT0	I	204	6/3		INITI0	6A2
XCTSCF0	OT	IC	5/5		TSCF0	6H5
XCTSCG0	OT	IC	5/5		TSCG0	6H5
XF110	0	IC	5/5		F110	6H4
XNITOA	0	IC	2/17		INITOA	6H3
XNITOBA	0	IC	3/1		INITOB	6H3
XNITOCA	0	IC	3/1		INITOC	6H2
XNITODA	0	IC	3/1		INITOD	6H2
XTCL590	0	IC	3/1		TCL590	6H4

SYMBOL NO. 7

TIMING ACTIVE, DUPLICATE REFRESH COUNTER AND
STORE SELECT DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
TARSELDA	08-17	FA1062	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
BSEL0	OT	IC111	1/5		TSEL0	4H6
FNRREQ0	I	317	6/2		FNRREQI0	4A5
FRREQB0	I	308	6/3		FCLRRQI0	4A5
FSELB0	I	207	6/3		FSELI0	4A6
F0.10TM0	I	107	6/2		FTMACTI0	4A6
INITBBB0	I	009	6/3		INITI0	4A2
IORESET0	I	003	6/1		RESETI0	4A7
LDOCFWT0	I	302	6/2		FIODCHI0	4A6
LDIODCH0	0	005	6/13		LDIODCH0	4H7
XCTCLR00	OT	IC	3/2		TCLRR00	4F1
XCTCL590	0	IC	5/2		TCL590	4H5
XFCLREQ0	0	IC	5/2		FCLRREQ0	4H5
XINITB0	0	IC	5/2		INITB0	4H3
XINITOA	0	IC	5/2		INITOA	4H2
XINITOB	0	IC	5/2		INITOB	4H2
XINITOC	0	IC	5/2		INITOC	4H3
XINITOD	0	IC	2/15,5/2		INITOD	4H3
0.10TIM0	OT	IC001	1/1		TTMACTI0	4H6

SYMB

DIAGNOSTIC ACCES
(BIT

DESIG	EQPT LOC	CODE
DGAPH.PL	08-16	FA [illegible]

FS INFO			
LEAD DESIG	FUNC	TERM.	DESTINATION
		115	
		203	
INITBB0	I	012	[illegible]
ISOCHB0	I	217	[illegible]
READADB0	I	307	6/3
XCINIT1N	0	IC	[illegible]
XCSD01BN	I	IC	[illegible]
XCSD11BN	I	IC	[illegible]
XCSEL0N	[illegible]	IC	[illegible]
XCTAD00N	I	IC	[illegible]
XCTAD10N	I	IC	[illegible]
XISOCKGN	0	IC	[illegible]
XNITOAN	0	IC	
XSOCKB1N	0	IC	
XTCLER0N	0	IC	[illegible]
XTRED00N	OT	IC	[illegible]
XTRED01N	0	IC	
XTRED10N	OT	IC	[illegible]
XTRED11N	0	IC	[illegible]
XTSER00N	OT	IC	[illegible]
XTSER10N	OT	IC	[illegible]

PART OF FS 6
SYMBOL(S) 3 4 5 6 7 8

MAIN STORE CONTROLLER

BELL LABORATORIES SD-1

PART OF FS 6

DIAGNOSTIC ACCESS

SYMBOL NO. 3 (CONT)

I/O ORDER DECODER

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
IOORDDEC	08-20	FA1064	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
XCC680	O	IC	5/7		C680	4G2
XCC980	O	IC	5/7		C980	4G6
XCD480	O	IC	5/7		D480	3G
XCE480	O	IC	5/7		E480	5H5
XCE88880	O	IC	5/7		E88880	5H3
XCF0880	O	IC	5/7		F0880	5H1
XC1880	O	IC	5/7		1880	4G3
XC1780	O	IC	5/7		1780	3G8
XC2880	O	IC	5/7		2880	3G7
XC3380	O	IC	5/7		3380	3G6
XC3680	O	IC	5/7		3680	4G0
XC3980	O	IC	5/7		3980	4G5
XC4D80	O	IC	5/7		4D80	3G6
XC4E80	O	IC	5/7		4E80	4G4
XC5580	O	IC	5/7		5580	3G5
XC6C80	O	IC	5/7		6C80	4G4
XC6380	O	IC	5/7		6380	4G8
XC6980	O	IC	5/7		6980	3G4
XC7180	O	IC	5/7		7180	3G4
XC7280	O	IC	5/7		7280	4G6
XC8D80	O	IC	5/7		8080	4G2
XC8E80	O	IC	5/7		8E80	3G3
XC9C80	O	IC	5/7		9C80	4G9
XC9380	O	IC	5/7		9380	4G7
XC9680	O	IC	5/7		9680	3G2
XNITBCO	O	IC	5/7		INITBCO	5G8
3T046C	GRD	0GD		202	GRD	
	GRD	005		202	GRD005	2H5
	GRD	102		202	GRD102	2H5
	GRD	107		202	GRD107	2H5
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	205		202	GRD205	2H4
	GRD	302		202	GRD302	2H4
	GRD	307		202	GRD307	2H4
	GRD	319		202	GRD	
3V046C	RWR	000		202	+3	
	RWR	119		202	+3	

SYMBOL NO. 4

CONTROL, TIMING AND STORE SELECT DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
CTSSDA	08-18	FA1071	E	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FBIT090	I	312	6/2		FBIT0910	11A0
FBIT110	I	013	6/3		FBIT1110	11A3
FCLB110	I	017	6/3		FOB1210	10A5
FOISRUNO	I	215	6/3		FOCIDLIO	11A3
FFOBRMO	I	113	6/2		FFOBRMIO	11A1
FLOSHTOO	I	317	6/3		FLSHOOIO	11A0
FRREQAO	I	218	6/3		FSTRROIO	11A6
FSELAO	I	018	6/2		FSELIO	11A5
FSELBO	I	304	6/3		BLB4CSIO	10A6
FSTNMO	I	016	6/3		FCLRMIO	10A0
FSTRMO	I	315	6/3		FSTRMIO	11A2
FSTSBYO	I	117	6/3		FSTSBYIO	11A2
INITBO	I	216	6/3		INITIO	10A1
XBLB4CSO	O	IC	1/1		BLB4CSO	10H6
XCINITOA	O	IC	1/1,2/16		INITOA	10H1
XCINITOB	O	IC	5/1		INITOB	10D6
XCTSELO	OT	IC	1/1		TSELO	11G5
XFBIT090	O	IC	1/1		FBIT090	11G0
XFBIT110	O	IC	1/1		FBIT110	11G3
XFLSBOOO	O	IC	1/1		FLSHBOOO	11G0
XFSTRMO	O	IC	1/1		FSTRMO	11G2
XFSTRROO	O	IC	3/3		FSTRROO	11G6
XFSTSBYO	O	IC	1/1		FSTSBYO	11G2
XINITBOA	O	IC	1/1		INITBOA	10H3
XINITBOB	O	IC	1/1		INITBOB	10H3
XINITBOC	O	IC	1/1		INITBOC	10H4
XINITBOD	O	IC	1/1		INITBOD	10H4
XNFOBRMO	I	IC	5/1		NOFOBRMO	11A1
XTBIT001	OT	IC	1/1		TBIT001	11G3
XTCLRMO	OT	IC	1/1		TCLRMO	10H1
XTCLRROO	OT	IC	1/1		TCLRROO	10H2
XTCLSB10	OT	IC	1/1		TCLSB10	10H4
XTCLSCMO	OT	IC	1/1		TCLSCMO	10H1
XTOB120	O	IC	1/1		TDB120	10H5
XTFOBRMO	OT	IC	1/1		TFOBRMO	11G1
XTRTTIMO	OT	IC	1/1		TRTTIMO	10H1

SYMBOL NO. 5

TIMING AND BUS DIRECTION DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
T.BDDA	08-21	FA1065	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FNOTIMO	I	018	6/3		FNOTIMIO	11A4
INITO	I	010	6/3		INITIO	11A2
LDDCFWTO	I	114	6/2		FBUSWTIO	11A4
NOCSO	OT	IC307	1/2		TNOCSO	11H3
XCINITO	O	IC	5/3		INITO	11H1
XTCLCOMO	OT	IC	1/2		TCLCOMO	11H2
XTSC318	OT	IC	1/2		TSC318	11H5
XTSC31C	OT	IC	1/2		TSC31C	11H5
XTSC310	OT	IC	1/2		TSC310	11H4

SYMBOL NO. 6

REFRESH, FDB MAINTENANCE AND COMMAND CODE CHECK DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
RFMCCDA	08-19	FA1066	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
FCHKERAO	I	118	6/3		F11IO	6A4
FCHKERBO	I	112	6/3		FOOIO	6A5
FNRREGO	I	004	6/2		FNRREQIO	6A3
FOBMTO	I	001	6/3		FOBMTIO	6A0
FOBMT1A	O	300	7/1 8/11-24(BTR)		FOBMT1A	6H1
FOBMT1B	O	100	7/15 8/11-24(BTR)		FOBMT1B	6H0
INITO	I	204	6/3		INITIO	6A2
XCTSCFO	OT	IC	5/5		TSCFO	6H5
XCTSCGO	OT	IC	5/5		TSCGO	6H5
XF110	O	IC	5/5		F110	6H4
XNITOA	O	IC	2/17		INITOA	6H3
XNITOBA	O	IC	3/1		INITOB	6H3
XNITOCA	O	IC	3/1		INITOC	6H2
XNITODA	O	IC	3/1		INITOD	6H2
XTCL590	O	IC	3/1		TCL590	6H4

SYMBOL NO. 7

TIMING ACTIVE, DUPLICATE REFRESH COUNTER AND STORE SELECT DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
TARSELDA	08-17	FA1062	0	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
BSELO	OT	IC111	1/5		TSELO	4H6
FNRREOO	I	317	6/2		FNRREOIO	4A5
FRREQBO	I	308	6/3		FCLRROIO	4A5
FSELBO	I	207	6/3		FSELIO	4A6
FO.10TMO	I	107	6/2		FTMACTIO	4A6
INITBBBO	I	009	6/3		INITIO	4A2
IORESETO	I	003	6/1		RESETIO	4A7
LDDCFWTO	I	302	6/2		FIODCHIO	4A6
LDIODCHO	O	005	6/13		LDIODCHO	4H7
XCTCLRDO	OT	IC	3/2		TCLRROO	4F1
XCTCL590	O	IC	5/2		TCL590	4H5
XFCLREQO	O	IC	5/2		FCLRREQO	4H5
XINITBO	O	IC	5/2		INITBO	4H3
XINITOA	O	IC	5/2		INITOA	4H2
XINITOB	O	IC	5/2		INITOB	4H2
XINITOC	O	IC	5/2		INITOC	4H3
XINITOD	O	IC	2/15,5/2		INITOD	4H3
O.10TIMO	OT	IC001	1/1		TTMACTIO	4H6

SYMBOL NO. 8

DIAGNOSTIC ACCESS TO BIT SLICE BOARDS, (BITS PL AND PH)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGAPH.RL	08-16	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		115			[illegible]	[illegible]
		203			[illegible]	[illegible]
INITBBO	I	012	6/3		[illegible]	[illegible]
ISOCHBO	I	217	6/3		[illegible]	[illegible]
READADBO	I	307	6/3		[illegible]	[illegible]
XCINITIN	O	IC	2/1		[illegible]	[illegible]
XCSD01BN	I	IC	2/1		[illegible]	[illegible]
XCSD11BN	I	IC	2/1		[illegible]	[illegible]
XCSELON	I	IC	1/6		[illegible]	[illegible]
XCTAD00N	I	IC	2/1		[illegible]	[illegible]
XCTAD10N	I	IC	2/1		[illegible]	[illegible]
XISDCKON	O	IC	5/8		[illegible]	[illegible]
XNITOAN	O	IC	2/1		[illegible]	[illegible]
XSDCKB1N	O	IC	2/1		[illegible]	[illegible]
XTCLERON	O	IC	5/8		[illegible]	[illegible]
XTRED00N	OT	IC	4/3		[illegible]	[illegible]
XTRED01N	O	IC	2/1		[illegible]	[illegible]
XTRE010N	OT	IC	4/3		[illegible]	[illegible]
XTRE011N	O	IC	2/1		[illegible]	[illegible]
XTSER00N	OT	IC	5/8		[illegible]	[illegible]
XTSER10N	OT	IC	5/8		[illegible]	[illegible]

RART OF FS 6

SYMBOL(S) 3 4 5 6 7 8

MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES | SD-1C902-03

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PART OF FS 6

DIAGNOSTIC ACCESS

SYMBOL NO. 9

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 19 AND 23)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA23.19	08-13	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		115			OFISOI0	10A5
		203			CLERI0	10A0
		307			RDADI0	10[illegible]
INITB0	I	012	6/3		INITI0	10A1
ISOCH0	I	217	6/3		ISOCKI0	10A4
XCINIT1M	0	IC	2/2		INIT1	10H1
XCSD01BM	I	IC	2/2		SD01B	10A3
XCSD11BM	I	IC	2/2		S011B	10A4
XCSEL0M	I	IC	1/7		SEL0	10A1
XCTAD00M	I	IC	2/2		TAD00	10A2
XCTAD10M	I	IC	2/2		TAD10	10A2
XISOCK0M	0	IC	5/9		ISOCK0	10D6
XNIT0AM	0	IC	2/2		INIT0A	10H1
XSOCKB1M	0	IC	2/2		ISOCKB1	10F6
XTCLER0M	0	IC	5/9		TCLER0	10H0
XTRED00M	OT	IC	4/12		TRETD00	10A2
XTRED01M	0	IC	2/2		TRETD01	10H2
XTRED10M	OT	IC	4/12		TRETD10	10A3
XTRED11M	0	IC	2/2		TRETD11	10H2
XTSER00M	OT	IC	5/9		TSTER00	10H6
XTSER10M	OT	IC	5/9		TSTER10	10G6

SYMBOL NO. 10

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 18 AND 22)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA22.18	08-12	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		115			OFISOI0	10A5
		203			CLERI0	10A0
		307			RDADI0	10A1
INITB0	I	012	6/3		INITI0	10A1
ISOCH0	I	217	6/3		ISOCKI0	10A4
XCINIT1L	0	IC	2/3		INIT1	10H1
XCSD01BL	I	IC	2/3		SD01B	10A3
XCSD11BL	I	IC	2/3		SD11B	10A4
XCSEL0L	I	IC	1/8		SEL0	10A1
XCTAD00L	I	IC	2/3		TAD00	10A2
XCTAD10L	I	IC	2/3		TAD10	10A2
XISOCK0L	0	IC	5/10		ISOCK0	10D6
XNIT0AL	0	IC	2/3		INIT0A	10H1
XSOCKB1L	0	IC	2/3		ISOCKB1	10F6
XTCLER0L	0	IC	5/10		TCLER0	10H0
XTRED00L	OT	IC	4/13		TRETD00	10A2
XTRED01L	0	IC	2/3		TRETD01	10H2
XTRED10L	OT	IC	4/13		TRETD10	10A3
XTRED11L	0	IC	2/3		TRETD11	10H2
XTSER00L	OT	IC	5/10		TSTER00	10H6
XTSER10L	OT	IC	5/10		TSTER10	10G6

SYMBOL NO. 11

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 17 AND 21)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA21.17	08-11	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		115			OFISOI0	10A5
		203			CLERI0	10A0
		307			RDADI0	10A1
INITB0	I	012	6/3		INITI0	10A1
ISOCH0	I	217	6/3		ISOCKI0	10A4
XCINIT1K	0	IC	2/4		INIT1	10H1
XCSD01BK	I	IC	2/4		SD01B	10A3
XCSD11BK	I	IC	2/4		S011B	10A4
XCSEL0K	I	IC	1/9		SEL0	10A1
XCTAD00K	I	IC	2/4		TAD00	10A2
XCTAD10K	I	IC	2/4		TAD10	10A2
XISOCK0K	0	IC	5/11		ISOCK0	10D6
XNIT0AK	0	IC	2/4		INIT0A	10H1
XSOCKB1K	0	IC	2/4		ISOCKB1	10F6
XTCLER0K	0	IC	5/11		TCLER0	10H0
XTRED00K	OT	IC	4/14		TRETD00	10A2
XTRED01K	0	IC	2/4		TRETD01	10H2
XTRED10K	OT	IC	4/14		TRETD10	10A3
XTRED11K	0	IC	2/4		TRETD11	10H2
XTSER00K	OT	IC	5/11		TSTER00	10H6
XTSER10K	OT	IC	5/11		TSTER10	10G6

SYMBOL NO. 12

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 16 AND 20)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA20.16	08-10	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		115			OFISOI0	10A5
		203			CLERI0	10A0
		307			RDADI0	10A1
INITBB0	I	012	6/3		INITI0	10A1
ISOCH0	I	217	6/3		ISOCKI0	10A4
XCINIT1J	0	IC	2/5		INIT1	10H1
XCSD01BJ	I	IC	2/5		SD01B	10A3
XCSD11BJ	I	IC	2/5		SD11B	10A4
XCSEL0J	I	IC	1/10		SEL0	10A1
XCTAD00J	I	IC	2/5		TAD00	10A2
XCTAD10J	I	IC	2/5		TAD10	10A2
XISOCK0J	0	IC	5/12		ISOCK0	10D6
XNIT0AJ	0	IC	2/5		INIT0A	10H1
XSOCKB1J	0	IC	2/5		ISOCKB1	10F6
XTCLER0J	0	IC	5/12		TCLER0	10H0
XTRED00J	OT	IC	4/15		TRETD00	10A2
XTRED01J	0	IC	2/5		TRETD01	10H2
XTRED10J	OT	IC	4/15		TRETD10	10A3
XTRED11J	0	IC	2/5		TRETD11	10H2
XTSER00J	OT	IC	5/12		TSTER00	10H6
XTSER10J	OT	IC	5/12		TSTER10	10G6

SYMBOL NO. 13

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 07 AND 15)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA15.07	08-09	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
INITBB0	I	012	6/3		INITI0	10A1
ICDECCH0	I	115	5/7		OFISOI0	10A5
LDICDCH0	I	217	6/7		ISOCKI0	10A4
READADB0	I	307	6/3		RDADI0	10A1
XCINIT1H	0	IC	2/6		INIT1	10H1
XCSD01BH	I	IC	2/6		SD01B	10A3
XCSD11BH	I	IC	2/6		S011B	10A4
XCSEL0H	I	IC	1/11		SEL0	10A1
XCTAD00H	I	IC	2/6		TAD00	10A2
XCTAD10H	I	IC	2/6		TAD10	10A2
XISOCK0H	0	IC	5/13		ISOCK0	10D6
XNIT0AH	0	IC	2/6		INIT0A	10H1
XSOCKB1H	0	IC	2/6		ISOCKB1	10F6
XTCLER0H	0	IC	5/13		TCLER0	10H0
XTRED00H	OT	IC	4/4		TRETD00	10A2
XTRED01H	0	IC	2/6		TRETD01	10H2
XTRED10H	OT	IC	4/4		TRETD10	10A3
XTRED11H	0	IC	2/6		TRETD11	10H2
XTSER00H	OT	IC	5/13		TSTER00	10H6
XTSER10H	OT	IC	5/13		TSTER10	10G6

SYMBOL NO. 14

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 06 AND 14)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA14.06	08-08	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICHTIM0	I	115	6/21		OFISOI0	10A5
INITBB0	I	012	6/3		INITI0	10A1
ISOCHB0	I	217	6/3		ISOCKI0	10A4
READADB0	I	307	6/3		RDADI0	10A1
XCINIT1G	0	IC	2/7		INIT1	10H1
XCSD01BG	I	IC	2/7		SD01B	10A3
XCSD11BG	I	IC	2/7		S011B	10A4
XCSEL0G	I	IC	1/12		SEL0	10A1
XCTAD00G	I	IC	2/7		TAD00	10A2
XCTAD10G	I	IC	2/7		TAD10	10A2
XISOCK0G	0	IC	5/14		ISOCK0	10D6
XNIT0AG	0	IC	2/7		INIT0A	10H1
XSOCKB1G	0	IC	2/7		ISOCKB1	10F6
XTCLER0G	0	IC	5/14		TCLER0	10H0
XTRED00G	OT	IC	4/5		TRETD00	10A2
XTRED01G	0	IC	2/7		TRETD01	10H2
XTRED10G	OT	IC	4/5		TRETD10	10A3
XTRED11G	0	IC	2/7		TRETD11	10H2
XTSER00G	OT	IC	5/14		TSTER00	10H6
XTSER10G	OT	IC	5/14		TSTER10	10G6

SYMBOL NO. 15

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 05 AND 13)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA13.05	08-07	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICHEPAR0	I	115	6/22		OFISOI0	10A5
INITBB0	I	012	6/3		INITI0	10A1
ISOCHB0	I	217	6/3		ISOCKI0	10A4
READADB0	I	307	6/3		RDADI0	10A1
XCINIT1F	0	IC	2/8		INIT1	10H1
XCSD01BF	I	IC	2/8		SD01B	10A3
XCSD11BF	I	IC	2/8		S011B	10A4
XCSEL0F	I	IC	1/13		SEL0	10A1
XCTAD00F	I	IC	2/8		TAD00	10A2
XCTAD10F	I	IC	2/8		TAD10	10A2
XISOCK0F	0	IC	5/15		ISOCK0	10D6
XNIT0AF	0	IC	2/8		INIT0A	10H1
XSOCKB1F	0	IC	2/8		ISOCKB1	10F6
XTCLER0F	0	IC	5/15		TCLER0	10H0
XTRED00F	OT	IC	4/6		TRETD00	10A2
XTRED01F	0	IC	2/8		TRETD01	10H2
XTRED10F	OT	IC	4/6		TRETD10	10A3
XTRED11F	0	IC	2/8		TRETD11	10H2
XTSER00F	OT	IC	5/15		TSTER00	10H6
XTSER10F	OT	IC	5/15		TSTER10	10G6

PART OF FS 6
SYMBOL(S) 9 10 11 12 13 14 15

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C2
ISSUE 1
BELL LABORATORIES
SD-1C902-03
B6CC

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PART OF FS 6

DIAGNOSTIC ACCESS

SYMBOL NO. 16

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 04 AND 12)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA12.04	08-06	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICHDPAR0	I	115	5/8		DFIS0I0	10A5
INITBBB0	I	012	6/3		INITI0	10[illegible]
ISOCHBB0	I	217	6/3		ISOCKI0	10A4
READADB0	I	307	6/3		RDADI0	10A1
XCINIT1E	D	IC	2/9		INIT1	10H1
XCSD01BE	I	IC	2/9		SD01B	10A3
XCSD11BE	I	IC	2/9		SD11B	10A4
XCSEL0E	I	IC	1/14		SEL0	10A1
XCTAD00E	I	IC	2/9		TAD00	10A2
XCTAD10E	I	IC	2/9		TAD10	10A2
XISOCK0E	D	IC	5/16		ISOCK0	10D6
XNITOAE	D	IC	2/9		INITOA	10H1
XSOCKB1E	D	IC	2/9		ISOCKB1	10F6
XTCLER0E	D	IC	5/16		TCLER0	10H0
XTRED00E	DT	IC	4/7		TRETD00	10A2
XTRED01E	O	IC	2/9		TRETD01	10H2
XTRED10E	DT	IC	4/7		TRETD10	10A3
XTRED11E	D	IC	2/9		TRETD11	10H2
XTSER00E	DT	IC	5/16		TSTER00	10H6
XTSER10E	DT	IC	5/16		TSTER10	10G6

SYMBOL NO. 18

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 02 AND 10)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA10.02	08-04	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICHD180	I	115	5/10		DFIS0I0	10A5
INITBBB0	I	012	6/3		INITI0	10A1
ISOCHBB0	I	217	6/3		ISOCKI0	10A4
READAD0	I	307	6/3		RDADI0	10A1
XCINIT1C	O	IC	2/11		INIT1	10H1
XCSD01BC	I	IC	2/11		SD01B	10A3
XCSD11BC	I	IC	2/11		SD11B	10A4
XCSEL0C	I	IC	1/16		SEL0	10A1
XCTAD00C	I	IC	2/11		TAD00	10A2
XCTAD10C	I	IC	2/11		TAD10	10A2
XISOCK0C	D	IC	5/18		ISOCK0	10D6
XNITOAC	D	IC	2/11		INITOA	10H1
XSOCKB1C	D	IC	2/11		ISOCKB1	10F6
XTCLER0C	D	IC	5/18		TCLER0	10H0
XTRED00C	OT	IC	4/9		TRETD00	10A2
XTRED01C	D	IC	2/11		TRETD01	10H2
XTRED10C	DT	IC	4/9		TRETD10	10A3
XTRED11C	D	IC	2/11		TRETD11	10H2
XTSER00C	DT	IC	5/18		TSTER00	10H6
XTSER10C	DT	IC	5/18		TSTER10	10G6

SYMBOL NO. 20

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 00 AND 08)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA08.00	08-02	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICH0160	I	115	5/12		DFIS0I0	10A5
INITBBB0	I	012	6/3		INITI0	10A1
ISOCHBB0	I	217	6/3		ISOCKI0	10A4
READAD0	I	307	6/3		RDADI0	10A1
XCINIT1A	O	IC	2/13		INIT1	10H1
XCSD01BA	I	IC	2/13		SD01B	10A3
XCSD11BA	I	IC	2/13		SD11B	10A4
XCSEL0A	I	IC	1/18		SEL0	10A1
XCTAD00A	I	IC	2/13		TAD00	10A2
XCTAD10A	I	IC	2/13		TAD10	10A2
XISOCK0A	O	IC	5/20		ISOCK0	10D6
XNITOAA	O	IC	2/13		INITOA	10H1
XSOCKB1A	D	IC	2/13		ISOCKB1	10F6
XTCLER0A	D	IC	5/20		TCLER0	10H0
XTRED00A	DT	IC	4/11		TRETD00	10A2
XTRED01A	D	IC	2/13		TRETD01	10H2
XTRED10A	OT	IC	4/11		TRETD10	10A3
XTRED11A	O	IC	2/13		TRETD11	10H2
XTSER00A	DT	IC	5/20		TSTER00	10H6
XTSER10A	DT	IC	5/20		TSTER10	10G6

SYMBOL NO. 22

2B PARITY ISOLATE BUS DIAGNOSTIC ACCESS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
2BPISODA	08-14	FA1067	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
ICHEPAR0	O	318	6/15		TBUSCK0	2H6
ISOCHB0	I	109	6/3		ISOCKI0	2A3
NO2DPH00	DT	IC216	2/18		TGSDPX0	2H3
NO2DPL00	OT	IC209	2/18		TGSDPYZ0	2H4
SDEPH00	OT	IL015	2/18		TGSDPW0	2H2
XSDPW1B	I	IC	2/18		SDPW1B	2A4
XSDPX1B	I	IC	2/18		SDPX1B	2A5
XSDPY21B	I	IC	2/18		SDPY21B	2A5

SYMBOL NO. 17

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 03 AND 11)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA11.03	08-05	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICHD190	I	115	5/9		OFIS0I0	10A5
INITBBB0	I	012	6/3		INITI0	10A1
ISOCHBB0	I	217	6/3		ISOCKI0	10A4
READAD0	I	307	6/3		RDADI0	10A1
XCINIT1D	O	IC	2/10		INIT1	10H1
XCSD01BD	I	IC	2/10		SD01B	10A3
XCSD11BD	I	IC	2/10		SD11B	10A4
XCSEL0D	I	IC	1/15		SEL0	10A1
XCTAD00D	I	IC	2/10		TAD00	10A2
XCTAD10D	I	IC	2/10		TAD10	10A2
XISOCK0D	D	IC	5/17		ISOCK0	10D6
XNITOAD	O	IC	2/10		INITOA	10H1
XSOCKB1D	O	IC	2/10		ISOCKB1	10F6
XTCLER0D	O	IC	5/17		TCLER0	10H0
XTRED00D	OT	IC	4/8		TRETD00	10A2
XTRED01D	O	IC	2/10		TRETD01	10H2
XTRED10D	OT	IC	4/8		TRETD10	10A3
XTRED11D	O	IC	2/10		TRETD11	10H2
XTSER00D	DT	IC	5/17		TSTER00	10H6
XTSER10D	OT	IC	5/17		TSTER10	10G6

SYMBOL NO. 19

DIAGNOSTIC ACCESS TO BIT SLICE BOARD.
(BITS 01 AND 09)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
DGA09.01	08-03	FA1060	F	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CLER0	I	203	6/3		CLERI0	10A0
ICHD170	I	115	5/11		OFIS0I0	10A5
INITBBB0	I	012	6/3		INITI0	10A1
ISOCHBB0	I	217	6/3		ISOCKI0	10A4
READAD0	I	307	6/3		RDADI0	10A1
XCINIT1B	D	IC	2/12		INIT1	10H1
XCSD01BB	I	IC	2/12		SD01B	10A3
XCSD11BB	I	IC	2/12		SD11B	10A4
XCSEL0B	I	IC	1/17		SEL0	10A1
XCTAD00B	I	IC	2/12		TAD00	10A2
XCTAD10B	I	IC	2/12		TAD10	10A2
XISOCK0B	D	IC	5/19		ISOCK0	10D6
XNITOAB	D	IC	2/12		INITOA	10H1
XSOCKB1B	D	IC	2/12		ISOCKB1	10F6
XTCLER0B	O	IC	5/19		TCLER0	10H0
XTRED00B	OT	IC	4/10		TRETD00	10A2
XTRED01B	D	IC	2/12		TRETD01	10H2
XTRED10B	OT	IC	4/10		TRETD10	10A3
XTRED11B	D	IC	2/12		TRETD11	10H2
XTSER00B	OT	IC	5/19		TSTER00	10H6
XTSER10B	DT	IC	5/19		TSTER10	10G6

SYMBOL NO. 21

ISOLATE AND WRITE MODE CHECKS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ISO.WRMD	08-24	FA1068	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		004			LR2A	5H5
		101			LR2B	5H5
		103			IBUF1AI	5A7
		104			IBUF2BI	5A5
		105			IBUF2AI	5A5
		118			LR1A	5H6
		201			IBUF1BI	5A6
		206			LR1D	5G7
		208			LR1C	5G7
		213			LR1B	5H6
FNWRTMD0	I	211	6/3		FNWTMDI0	5A0
ICHTIM0	O	216	6/14		TBUSCK0	5H4
INIT0	I	100	6/3		INITI0	5A4
ISOCH0	I	215	6/3		ISOCKI0	5A1
SBYI0	OT	IC115	1/3		TSBYI0	5H1
SBY00	I	IC106	1/3		SBYB0	5A4
SCMI0	OT	IC111	1/3		TSCMI0	5H1
SCM00	I	IC202	1/3		GSCM0	5A3
SERCI0	DT	IC309	5/4		TSERCI0	5H2
SERC00	I	IC002	5/4		TSERC0	5A2
XTWRTPK1	DT	IC	1/3		TWRTPCK1	5H0
XXINIT0	O	IC	1/3		INIT0	5H4

PART OF FS 6
SYMBOL(S) 16 17 18 19 20 21 22

MAIN STORE CONTROLLER AND MEMORY

DWG. SIZE	ISSUE
[illegible]	1

BELL LABORATORIES | SD-1C902-03 | B6CD

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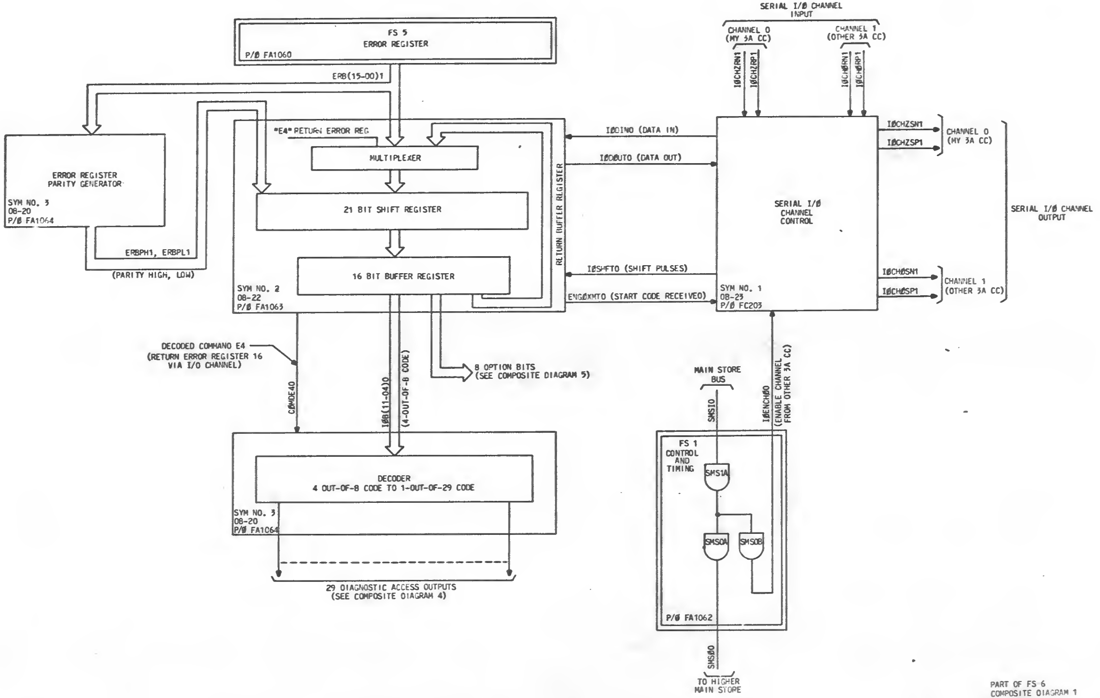
[illegible]

PART OF FS 6

DIAGNOSTIC ACCESS

COMPOSITE DIAGRAM I

SERIAL I/0 CHANNEL



PART OF FS 6
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 4S	ISS 1
BELL LABORATORIES	SD-1C902-03	B6G

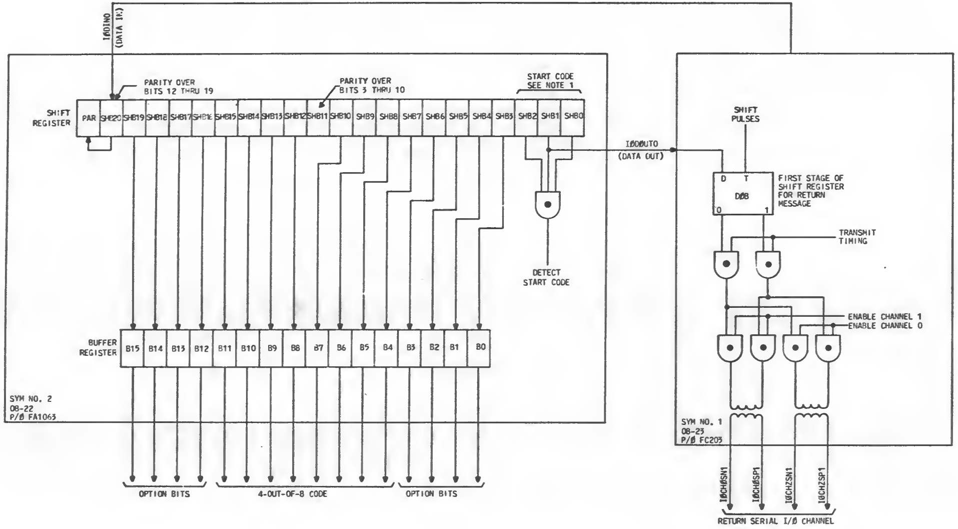
PART OF FS 6
DIAGNOSTIC ACCESS

COMPOSITE DIAGRAM 2
REGISTER LAYOUT

NOTES:

1. START CODES:
   MAIN STORES RECEIVING
   SHB2 SHB1 SHB0
   1 0 1

   MAIN STORE TRANSMISSION
   SHB2 SHB1 J08
   0 1 1



PART OF FS 6
COMPOSITE DIAGRAM 2

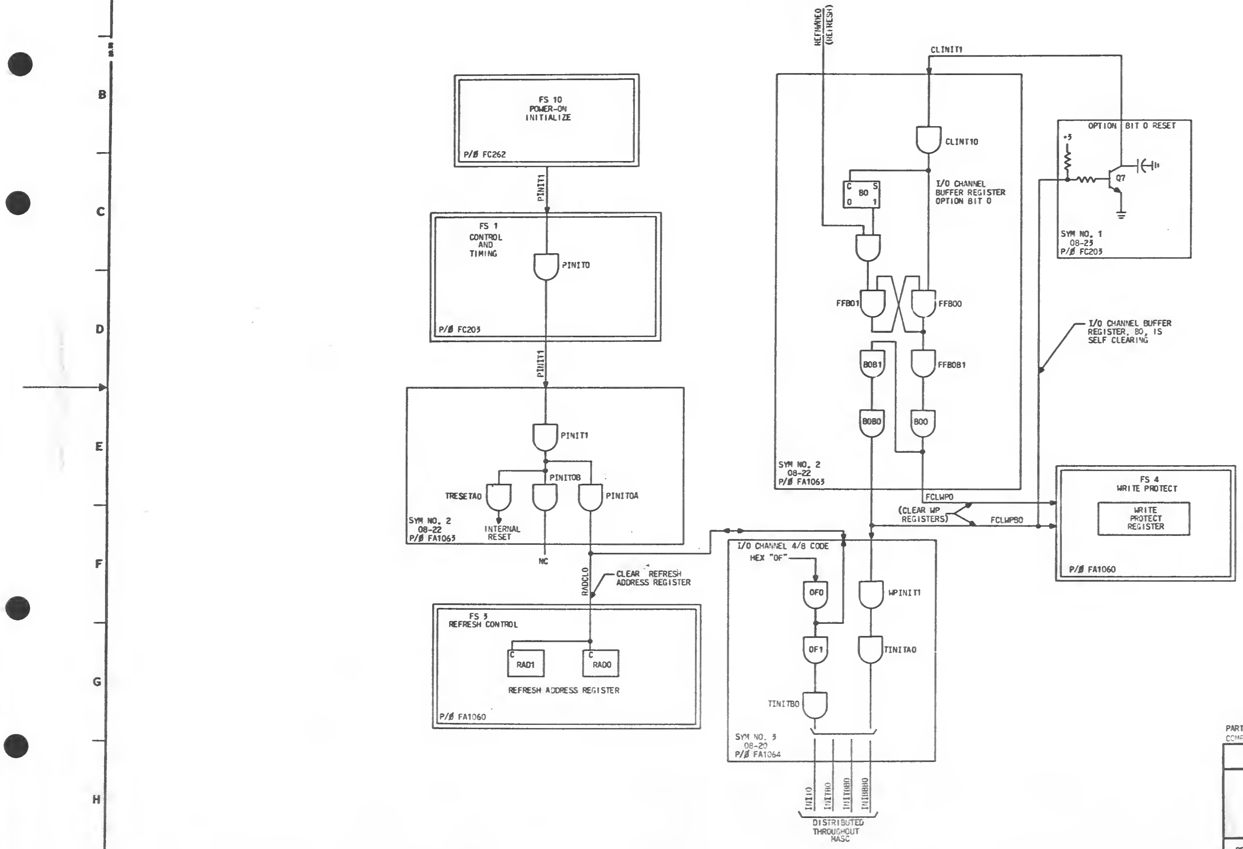
MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 6S	ISSUE 1
BELL LABORATORIES	SD-1C902-03	86G8

PART OF FS 6

DIAGNOSTIC ACCESS

COMPOSITE DIAGRAM 3

INITIALIZE CIRCUITRY



PART OF FS 6
COMPOSITE DIAGRAM 3

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 6S
BELL LABORATORIES	SD-1C902-03

PART OF FS 6
DIAGNOSTIC ACCESS

NOTES:

1. THE 4-OUT-OF-8 CODES ARE LISTED FOR IDENTIFICATION AS THE HEXIDECIMAL EQUIVALENT OF THE 8 BITS.
2. THE OPTION BITS ARE LISTED COINCIDING WITH THEIR ACTUAL [illegible] IN THE BUFFER REGISTER, SHOWN IN COMPOSITE DIAGRAM 2.

COMPOSITE DIAGRAM 4
4-OUT-OF-8 CODE FUNCTIONS

HEX CODE	NET NAME	FUNCTION
0F	RADCLO	INITIALIZE COMPLETE MAS EXCEPT THE WRITE PROTECT REGISTERS AND THE SERIAL I/O CHANNEL CIRCUITRY
17	FCHKEPBO	FORCE ERROR IN THE NORMAL SELECT CHECK, FA1062. FORCES ERROR IN 3-OUT-OF-6 DECODING
1B	FRREQBO	FORCE ERROR IN REFRESH REQUEST CHECK, FA1062. THIS CHECK VERIFIES THAT THE REAL AND DUPLICATE REFRESH COUNTERS ARE IN SYNC
2B	FCHKERAO	FORCE ERROR IN THE NORMAL SELECT CHECK, FA1066. FORCES ERROR IN 3-OUT-OF-6 ENCODING
33	RSEL0B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060
36	F0RMTO	ENABLE UNSELCS CHECK CIRCUIT ON EACH FANOUT BOARD, JK25 UNSELECT ERROR WILL OCCUR IF A MEMORY MODULE IS ACCESSED. RESULTS APPEAR ON FA1066.
39	FSTSBYO	FORCE STORE BUSY, SET SBY F/F ON FA1071
40	RSEL1B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060
4E	FSTNMO	FORCE MODE F/F CLEAR,FA1071.
55	RSEL2B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060.
63	FOISRUNO	FORCE IDLE F/F FROM BEING CLEARED AT SHIFTER BIT 0 TIME, FA1071.
69	RSEL3B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060.
6C	CLERO	CLEAR ERROR REGISTERS ON ALL FA1060 CIRCUIT PACKS. CLEAR ERROR B, FA1063.
71	RSEL4B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060.
72	FCLB110	FORCE SHIFTER BIT 12 INACTIVE, FA1071. BIT 12 TIMES NORMALLY CLEARS THE MODE F/F.
8D	FRREQAO	FORCE REFRESH REQUEST, RREQ, F/F SET, FA1071.
8E	RSEL5B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060.
93	FN0TIMO	FORCE NO CHIP SELECT TO MEMORY AND NO DATA STROBE,.
96	RSEL6B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060.
9C	FSEL30	FORCE DUPLICATE MAS SELECT CIRCUIT ON FA1062 IN THE SELECTED CONDITION. WILL ALSO BLOCK CS F/F ON FA1071 FROM BEING SET AT SHIFTER BIT 4 TIME.
AA	RSEL7B1	FORCE ERROR IN THE REFRESH SELECT CHECK, FA1066. WILL ALSO FORCE INPUT TO ERROR REGISTER, FA1060.
B1	FNWRTMGO	DISABLE DATA PARITY CHECKS DURING WRITE AND BLIND WRITE COMMANDS, FA1068. DISABLE DATA TO MEMORY (MEMORY CAN ONLY BE WRITTEN TO ONES), FA1068.
B2	FLDXFRO FLDXERBO	LOAD ERROR REGISTER FROM EXTERNAL LOAD, FA1060.
C6	FLDSHTOO	FORCE SHIFTER BIT 0 ACTIVE, FA1071.
C9	TINWPO	FORCE WRITE PROTECT ERROR, FA1065.
CC	FBIT110	FORCE SHIFTER BIT 11 ACTIVE, FA1071. BIT 11 IS USED TO INCREMENT THE REFRESH ADDRESS REGISTER, FA1062.
04	FSTRMO	FORCE THE MODE F/F SET (REFRESH ACTIVE), FA1071.
E4		LOAD ERROR REGISTER, FA1060, INTO SERIAL I/O CHANNEL, FA1063.
E8	IS0CHO IS0CHPO IS0CHEBO	LOAD ISOLATION CHECK INFORMATION INTO THE ERROR REGISTER, FA1060. THE ISOLATION CHECK INFORMATION IS USED TO TEST THE FUNCTIONING OF THE PARALLEL DATA BUS AND THE MAS RETURN SIGNALS SBY, SCM AND ERROR C.
F0	REA[illegible]AO REA[illegible]O	FORCE THE PARALLEL ADDRESS BUS STATE TO BE RETURNED VIA THE PARALLEL DATA BUS.

COMPOSITE DIAGRAM 5
OPTION BIT FUNCTIONS

OPTION BIT	NET NAME	FUNCTION
0	FCLWPO FCLWPBO	CLEAR WRITE PROTECT REGISTERS, FA1060. CLEAR REFRESH REQUEST COUNTERS, FA1062 AND FA1066. CLEAR ALL F/F AND REGISTER IN MASC. DOES NOT CLEAR THE REFRESH ADDRESS REGISTER.
1	FF0BRMO	FORCE REFRESH MODE TO FANOUT BOARDS (JK25), FA1071, DOES NOT SET MODE F/F. FORCE SINGLE STEP CLOCK INPUT ON FC203.
2	FST0PCKO	FORCE MAS CLOCK TO STOP, FC203.
3	FNRREQO	FORCE NO REFRESH REQUEST, FA1062 AND FA1066.
12	FO.10TMO	FORCE ACTIVE TIMING TO FANOUT BOARDS (JK25), FA1068. TEST CHECK CIRCUITS THAT DETECT MODE CHANGE DURING A MAS CYCLE - REFRESH TO NORMAL OR NORMAL TO REFRESH, FA1062.
13	LODCFWTO	LOAD RESULTS OF SERIAL I/0 CHANNEL 4-OUT-OF-8 CODE CHECK INTO THE ERROR REGISTER, FA1062, FORCE BI-DIRECTIONAL DATA BUS TO THE 3A CC TO MAS DIRECTION.
14	FSELAO	FORCE MAS SELECTED, FA1071.
15	FBIT090	FORCE THE SHIFTER BIT 9 ACTIVE, FA1071. SHIFTER BIT 9 IS USED ON THE FA1060 CIRCUIT PACKS TO LOAD THE ERROR REGISTER.

)

PART OF FS 6
COMPOSITE DIAGRAMS 4, 5

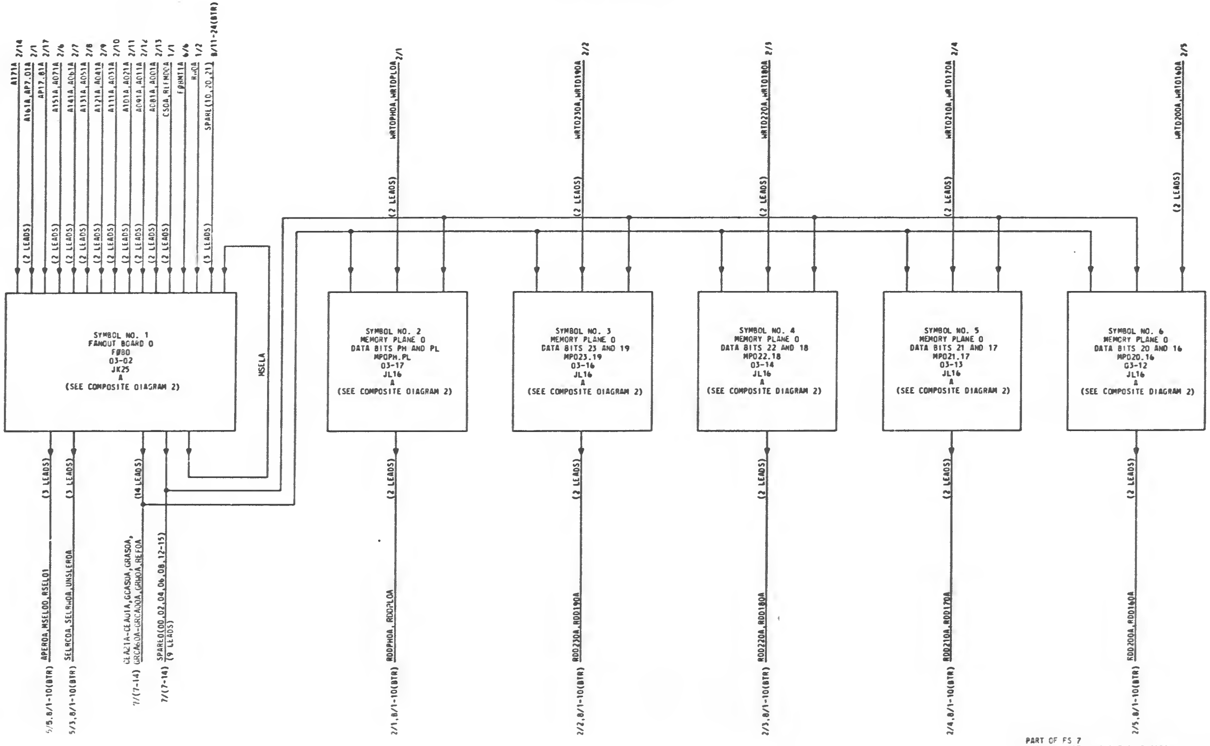
MAIN STORE CONTROLLER AND MEMORY

BELL LABORATORIES

SD-1C902-03

PART OF FS 7

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



PART OF FS 7
INTERCONNECTION AND FLOW DIAGRAM

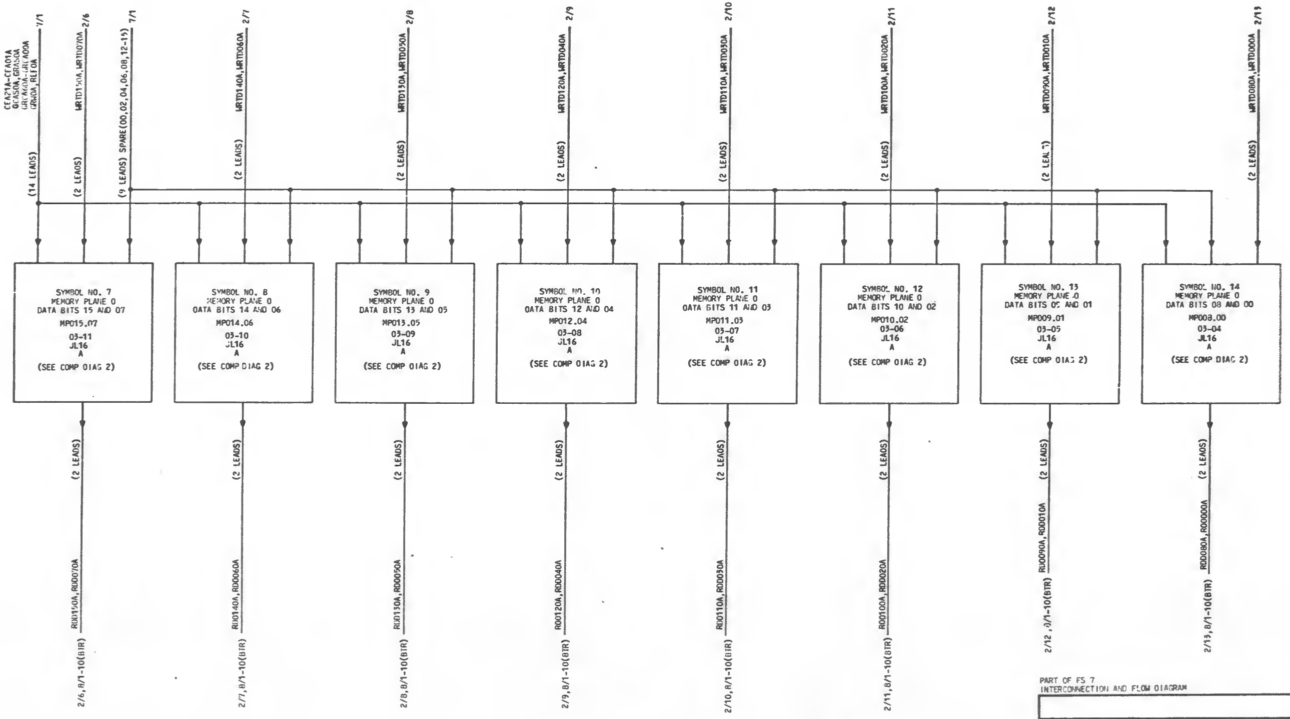
MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
6S	1

BELL LABORATORIES SD-1C902-03 B7AA

PART OF FS 7

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



PART OF FS 7
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE 65

ISSUE 1

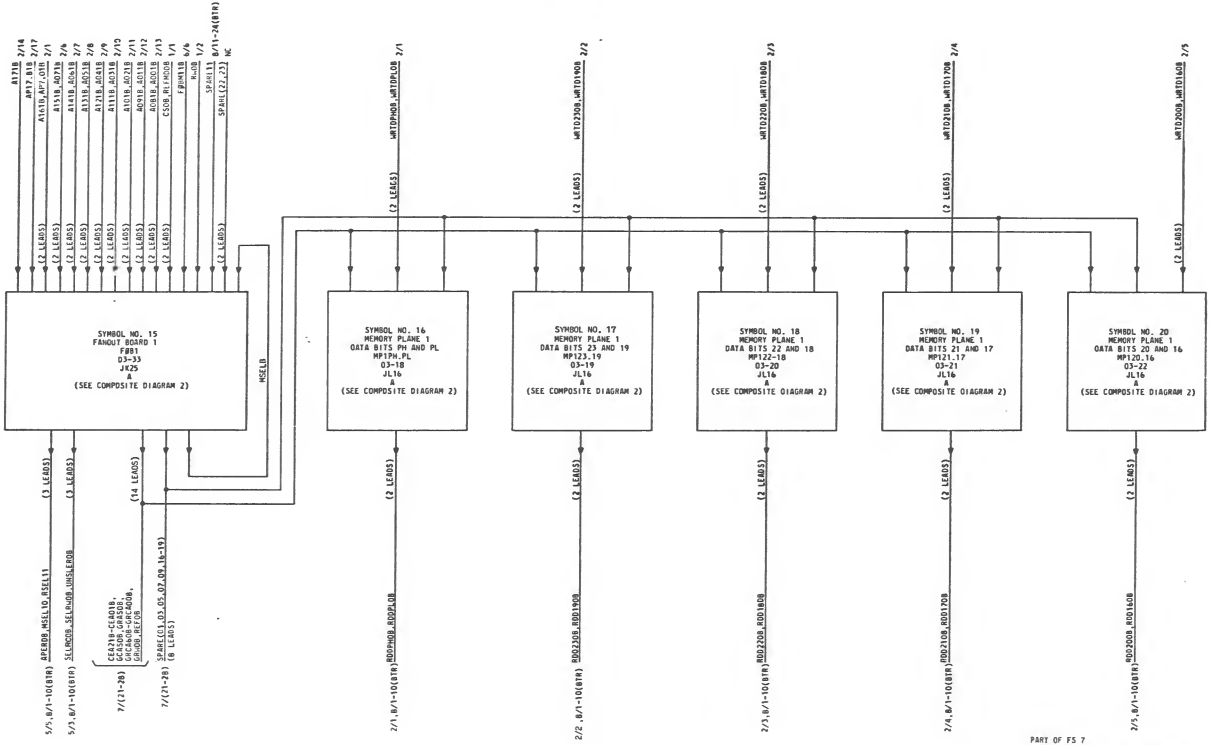
BELL LABORATORIES

SD-1C902-03

B7AB

PART OF FS 7

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



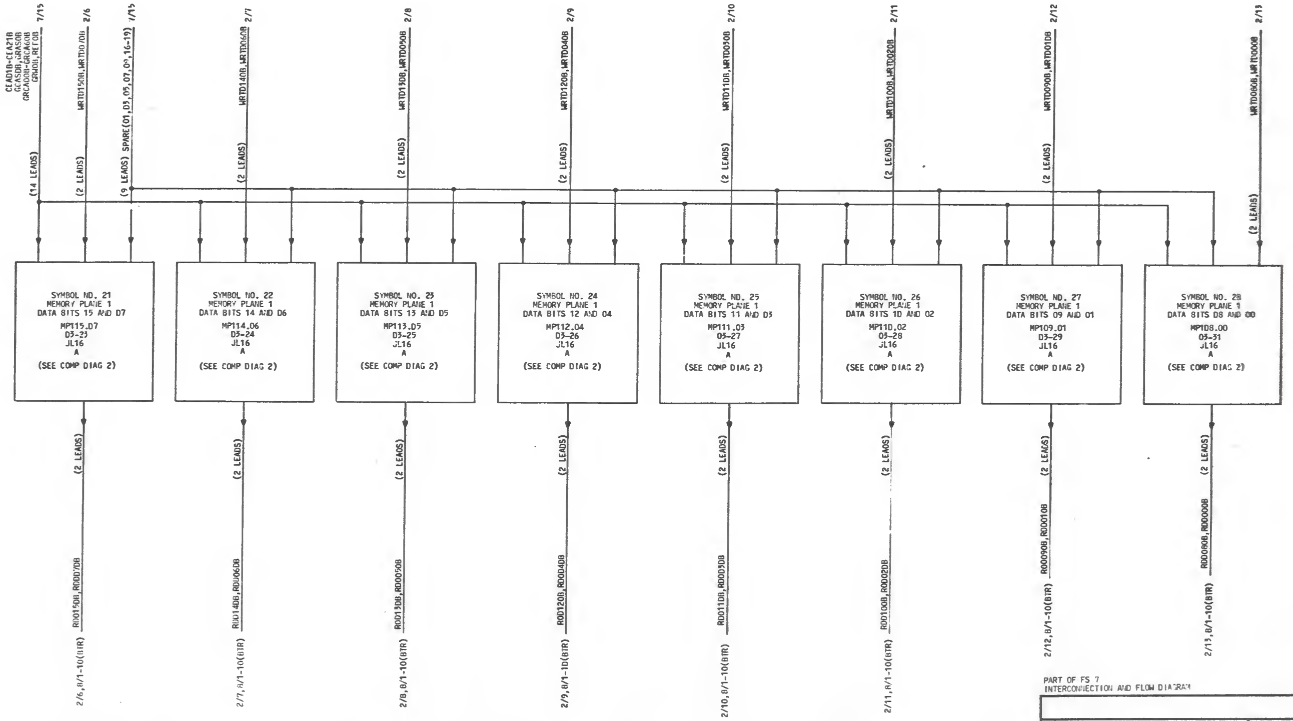
PART OF FS 7
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

		DWG SIZE	ISSUE
		6S	
BELL LABORATORIES	SD-1C902-03		B7AC

PART OF FS 7

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



PART OF FS 7
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
65

BELL LABORATORIES

SD-1C902-03

B7

PART OF FS 7

MEMORY

SYMBOL NO. 1

FANOUT BOARD 0

DESIG	EQPT LOC	CODE	ELEM IDENT	UPT
F0B0	03-02	JK25	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		218			REFCK1	2H4
APER0A	0	212	5/5 8/1-10(BTR)		TAPER0	3H3
AP17.81A	I	303	2/17		AP17.811	2A8
AP7.01A	I	204	2/1		AP7.011	3A9
A001A	I	010	2/13		A0011	3A7
A011A	I	112	2/12		A0111	3A7
A021A	I	011	2/11		A0211	3A6
A031A	I	111	2/10		A0311	3A0
A041A	I	207	2/9		A0411	3A6
A051A	I	012	2/8		A0511	3A6
A061A	I	306	2/7		A0611	3A5
A071A	I	208	2/6		A0711	3A5
A081A	I	309	2/13		A0811	3A2
A091A	I	307	2/12		A0911	3A0
A101A	I	305	2/11		A1011	3A1
A111A	I	206	2/10		A1111	3A5
A121A	I	110	2/9		A1211	3A4
A131A	I	308	2/8		A1311	3A4
A141A	I	209	2/7		A1411	3A4
A151A	I	016	2/6		A1511	3A1
	I	219	2/6	202		
A161A	I	117	2/1		A1611	3A1
A171A	I	019	2/14		A1711	3A3
CEA01A	0	102	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	CEA01	3G8
CEA11A	0	009	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	CEA11	3H7
CEA21A	0	109	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	CEA21	3H7
CS0A	I	201	1/1		RAS10	2A4
F0BMT1A	I	215	6/6	202		
	I	216	6/6		TS111	2A6
GCAS0A	0	310	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GCAS0	2H6
GRAS0A	0	003	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRAS0	2H4
GRCA00A	0	007	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRCA00	3H4
GRCA10A	C	105	7/2,7/3 7/4,7/5	202	GRCA10	3H5

SYMBOL NO. 1 (CONT)

FANOUT BOARD 0

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
F0B0	03-02	JK25	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14			
GRCA20A	0	006	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRCA20	3H5
GRCA30A	0	103	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRCA30	3H4
GRCA40A	0	101	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRCA40	3H1
GRCA50A	0	002	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRCA50	3H1
GRCA60A	0	106	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRCA60	3H0
GRW0A	0	001	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRW0	2H0
MSELA	0	115			MSEL00	3H2
	I	113			SEL011	2A2
MSEL00	0	311	5/5 8/1-10(BTR)		MSEL0	2H1
REFMD0A	I	214	1/1		REFI0	2A3
REF0A	0	108	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	REF0	2H3
RSEL01	0	013	5/5 8/1-10(BTR)		RSEL1	2H3
RW0A	I	300	1/2		RWI0	2A0
SELRC0A	0	210	5/3 8/1-10(BTR)		SELRC0	2H5
SELRW0A	0	205	5/3 8/1-10(BTR)		SELRW0	2H1
SPARE00	GRD	104	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRD	2G8
SPARE02	GRD	004	7/2,7/3 7/4,7/5	202	GRD	2G8

SYMBOL NO. 1 (CONT)

FANOUT BOARD 0

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
F0B0	03-02	JK25	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14			
SPARE04	GRD	008	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRD	2G8
SPARE06	GRD	005	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRD	2G8
SPARE08	GRD	107	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202	GRD	2G8
SPARE10	I	313	8/11-24(BTR)			
SPARE12	I	318	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202		
SPARE13	I	317	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202		
SPARE14	I	316	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202		
SPARE15	I	315	7/2,7/3 7/4,7/5 7/6,7/7 7/8,7/9 7/10,7/11 7/12,7/13 7/14	202		
SPARE20	I	314	8/11-24(BTR)			
SPARE21	I	312	8/11-24(BTR)			
UNSLER0A	0	211	5/3 8/1-10(BTR)		UNSELER0	2H5
0GRDM0DA	GRD	0GD		202	GRD	3H9
	GRD	2GD		202	GRD	3H9
	GRD	200		202	GRD	3H9
	GRD	304				
	GRD	319		202	GRD	3H9
5V	PWR	000		202	+5	3H9
	PWR	119		202	+5	3H9

SYMBOL NO. 2

MEMORY PLANE 0
DATA BITS PH AND PL

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP0PH.PL	03-17	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA011	2G0
CEA11A	I	009	7/1	202	CEA111	2G0
CEA21A	I	209	7/1	202	CEA211	2E0
GCAS0A	I	014	7/1	202	CAS10	2F0
GRAS0A	I	003	7/1	202	RAS10	2E0
GRCA00A	I	007	7/1	202	RCA010	2H0
GRCA10A	I	205	7/1	202	RCA110	2G0
GRCA20A	I	006	7/1	202	RCA210	2G0
GRCA30A	1	203	7/1	202	RCA310	2G0
GRCA40A	1	201	7/1	202	RCA410	2G0
GRCA50A	I	002	7/1	202	RCA510	2F0
GRCA60A	I	206	7/1	202	RCA610	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDDPH0A	0	212	2/1 8/1-10(BTR)		ROD10	2G9
RODPL0A	0	214	2/1 8/1-10(BTR)		ROD00	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	1	016	7/1	202		
SPARE15	1	015	7/1	202		
WRTDPH0A	I	216	2/1		WRTD110	2A9
WRTDPL0A	I	218	2/1		WRTD010	2A8
0GRDM00A	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 1 2

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
C2	1

BELL LABORATORIES | SD-1C902-03 | B7CA

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PART OF FS 7

MEMORY

SYMBOL NO. 3

MEMORY PLANE 0
DATA BITS 23 AND 19

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP023.19	03-16	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD190A	0	214	2/2 8/1-10(BTR)		RDD00	2G9
RDD230A	0	212	2/2 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD190A	I	218	2/2		WRTD0I0	2A8
WRT0230A	I	216	2/2		WRTD1I0	2A9
0GRDM0DA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 4

MEMORY PLANE 0
DATA BITS 22 AND 18

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP022.18	03-14	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	200
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GPW0A	I	001	7/1	202	RWI0	2H0
RD0180A	0	214	2/3 8/1-10(BTR)		RDD00	2G9
RDD220A	0	212	2/3 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRT0180A	I	218	2/3		WRTD0I0	2A8
WRT0220A	I	216	2/3		WRTD1I0	2A9
0GRDM0DA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 5

MEMORY PLANE 0
DATA BITS 21 AND 17

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP021.17	03-13	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD170A	0	214	2/4 8/1-10(BTR)		RDD00	2G9
RDD210A	0	212	2/4 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD170A	I	218	2/4		WRTD0I0	2A8
WRTD210A	I	216	2/4		WRTD1I0	2A9
0GRDM0DA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 6

MEMORY PLANE 0
DATA BITS 20 AND 16

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP020.16	03-12	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD160A	0	214	2/5 8/1-10(BTR)		RDD00	2G9
R0D200A	0	212	2/5 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD160A	I	218	2/5		WRTD0I0	2A8
WRTD200A	I	216	2/5		WRTD1I0	2A9
0GRDM0DA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 3 4 5 6

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
CZ	1

BELL LABORATORIES
SD-1C902-03
B7CB

PRINTED IN U. S. A.

PART OF FS 7

MEMORY

SYMBOL NO. 7

MEMORY PLANE 0
DATA BITS 15 AND 07

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP015.07	03-11	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GPCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD070A	D	214	2/6 8/1-10(BTR)		RDD00	2G9
RDD150A	0	212	2/6 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD070A	I	218	2/6		WRTD0I0	2A8
WRTD150A	I	216	2/6		WRTD1I0	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 8

MEMORY PLANE 0
DATA BITS 14 AND 06

DESIG	E. LOC	CODE	ELEM IDENT	OPT
MP014.06	03-10	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD060A	0	214	2/7 8/1-10(BTR)		RDD00	2G9
RDD140A	D	212	2/7 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD060A	I	218	2/7		WRTD0I0	2A8
WRTD140A	I	216	2/7		WRTD1I0	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 9

MEMORY PLANE 0
DATA BITS 13 AND 05

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP013.05	03-09	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD050A	D	214	2/8 8/1-10(BTR)		RDD00	2G9
RDD130A	D	212	2/8 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD050A	I	218	2/8		WRTD0I0	2A8
WRTD130A	I	216	2/8		WRTD1I0	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 10

MEMORY PLANE 0
DATA BITS 12 AND 04

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP012.04	03-08	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD040A	D	214	2/9 8/1-10(BTR)		RDD00	2G9
RDD120A	D	212	2/9 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD040A	I	218	2/9		WRTD0I0	2A8
WRTD120A	I	216	2/9		WRTD1I0	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 7 8 9 10

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE CZ	IS 1
BELL LABORATORIES	SD-1C902-03	B7C

PRINTED IN U. S. A.

PART OF FS 7

MEMORY

SYMBOL NO. 11

MEMORY PLANE 0
DATA BITS 11 AND 03

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP011.03	03-07	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
	FS INFO				CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD030A	D	214	2/10 8/1-10(BTR)		RDD00	2G9
RDD110A	D	212	2/10 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD030A	I	218	2/10		WRTD0I0	2A8
WRTD110A	I	216	2/10		WRTD1ID	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 12

MEMORY PLANE 0
DATA BITS 10 AND 02

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP010.02	03-06	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
	FS INFO				CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	200
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD020A	D	214	2/11 8/1-10(BTR)		RDD00	2G9
RDD100A	D	212	2/11 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD020A	I	218	2/11		WRTD0I0	2A8
WRTD100A	I	216	2/11		WRTD1ID	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 13

MEMORY PLANE 0
DATA BITS 09 AND 01

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP009.01	03-05	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
	FS INFO				CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD010A	D	-214	2/12 8/1-10(BTR)		RDD00	2G9
RDD090A	D	212	2/12 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD010A	I	218	2/12		WRTD0I0	2A8
WRTD090A	I	216	2/12		WRTD1I0	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 14

MEMORY PLANE 0
DATA BITS 08 AND 00

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP008.00	03-04	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
	FS INFO				CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01A	I	202	7/1	202	CEA0I1	2D0
CEA11A	I	009	7/1	202	CEA1I1	2C0
CEA21A	I	209	7/1	202	CEA2I1	2C0
GCAS0A	I	014	7/1	202	CASI0	2F0
GRAS0A	I	003	7/1	202	RASI0	2E0
GRCA00A	I	007	7/1	202	RCA0I0	2H0
GRCA10A	I	205	7/1	202	RCA1I0	2G0
GRCA20A	I	006	7/1	202	RCA2I0	2G0
GRCA30A	I	203	7/1	202	RCA3I0	2G0
GRCA40A	I	201	7/1	202	RCA4I0	2G0
GRCA50A	I	002	7/1	202	RCA5I0	2F0
GRCA60A	I	206	7/1	202	RCA6I0	2F0
GRW0A	I	001	7/1	202	RWI0	2H0
RDD000A	D	214	2/13 8/1-10(BTR)		RDD00	2G9
RDD080A	D	212	2/13 8/1-10(BTR)		RDD10	2G9
REF0A	I	208	7/1	202	REFI0	2E0
SPARE00	GRD	204	7/1	202	GRD	3B0
SPARE02	GRD	004	7/1	202	GRD	3B0
SPARE04	GRD	008	7/1	202	GRD	3B0
SPARE06	GRD	005	7/1	202	GRD	3B0
SPARE08	GRD	207	7/1	202	GRD	3B0
SPARE12	I	018	7/1	202		
SPARE13	I	017	7/1	202		
SPARE14	I	016	7/1	202		
SPARE15	I	015	7/1	202		
WRTD000A	I	218	2/13		WRTD0I0	2A8
WRTD080A	I	216	2/13		WRTD1I0	2A9
0GRDMODA	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 11 12 13 14

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE C2	ISS 1
BELL LABORATORIES	SD-1C902-03	B7C

PRINTED IN U. S. A.

PART OF FS 7

MEMORY

SYMBOL NO. 15

FANOUT BOARD 1

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
F0B1	03-33	JK25	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
		115			MSEL00	3HZ
		218			REFCK1	2H4
APER0B	0	212	5/5 8/1-10(BTR)		TAPER0	3H
AP17.81B	I	303	2/17		AP17.8I1	2A8
AP7.01B	I	204	2/1		AP7.0I1	3A9
	I	302	2/1	202		
A001B	I	010	2/13		A00I1	3A7
A011B	I	112	2/12		A01I1	3A7
A021B	I	011	2/11		A02I1	3A6
A031B	I	111	2/10		A03I1	3A0
A041B	I	207	2/9		A04I1	3A6
A051B	I	012	2/8		A05I1	3A6
A061B	I	306	2/7		A06I1	3A5
A071B	I	208	2/6		A07I1	3A5
	I	215	2/6	202		
A081B	I	309	2/13		A08I1	3A2
A091B	I	307	2/12		A09I1	3A0
A101B	I	305	2/11		A10I1	3A1
A111B	I	206	2/10		A11I1	3A5
	I	304	2/10	202		
A121B	I	110	2/9		A12I1	3A4
	I	213	2/9	202		
A131B	I	308	2/8		A13I1	3A4
A141B	I	209	2/7		A14I1	3A4
A151B	I	016	2/6		A15I1	3A1
A161B	I	117	2/1		A16I1	3A1
A171B	I	019	2/14		A17I1	3A3
CEA01B	0	102	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	CEA01	3G8
CEA11B	0	009	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	CEA11	3H7
CEA21B	0	109	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	CEA21	3H7
CS0B	I	201	1/1		RASI0	2A4
FCBMT1B	I	216	6/6		TS1I1	2A6
GCAS0B	0	310	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GCAS0	2H6
GRAS0B	0	003	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRAS0	2H4
GRCA00B	0	007	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25	202	GRCA00	3H4

SYMBOL NO. 15 (CONT)

FANOUT BOARD 1

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
F0B1	03-33	JK25	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
			7/26,7/27 7/28			
GRCA10B	0	105	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRCA10	3H5
GRCA20B	0	006	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRCA20	3H5
GRCA30B	0	103	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRCA30	3H4
GRCA40B	0	101	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRCA40	3H1
GRCA50B	0	002	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRCA50	3H1
GRCA60B	0	106	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRCA60	3H0
GROA	GRD	319	9/1		GRD	3H9
GRW0B	0	001	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRW0	2H0
MSELB	I	019			A17I1	3A3
	I	113			SEL0I1	2A2
MSEL10	0	311	5/5 8/1-10(BTR)		MSEL0	2H1
PEFMD0B	I	214	1/1		REFI0	2A3
REF0B	0	108	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	REF0	2H3
PSEL11	0	013	5/5 8/1-10(BTR)		RSEL1	2H3
PW0B	I	300	1/2		RWI0	2A0
SELRC0B	0	210	5/3 8/1-10(BTR)		SELRC0	2H5
SELRW0B	0	205	5/3 8/1-10(BTR)		SELRW0	2H1
SPARE01	GRD	104	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23	202	GRD	2G8

SYMBOL NO. 15 (CONT)

FANOUT BOARD 1

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
F0B1	03-33	JK25	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
			7/24,7/25 7/26,7/27 7/28			
SPARE03	GRD	004	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRD	2G8
SPARE05	GRD	008	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRD	2G8
SPARE07	GRD	005	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRD	2G8
SPARE09	GRD	107	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202	GRD	2G8
SPARE11	I	313	8/11-24(BTR)			
SPARE16	I	318	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202		
SPARE17	I	317	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202		
SPARE18	I	316	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202		
SPARE19	I	315	7/16,7/17 7/18,7/19 7/20,7/21 7/22,7/23 7/24,7/25 7/26,7/27 7/28	202		
SPARE22	I	314				
SPARE23	I	312				
UNSLER0B	0	211	5/3 8/1-10(BTR)		UNSELER0	2H5
0GRDM0DB	GRD	0GD		202	GRD	3H9
	GRD	2GD		202	GRD	3H9
	GRD	200		202	GRD	3H9
	GRD	319		202	GRD	3H9
5V	PWR	000		202	+5	3H9
	PWR	119		202	+5	3H9

SYMBOL NO. 16

MEMORY PLANE 1
DATA BITS PH AND PL

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP1PH.PL	03-18	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		FS INFO			CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CASI0	2F0
GRAS0B	I	003	7/15	202	RASI0	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDDPH0B	0	212	2/1 8/1-10(BTR)		RDD10	2G9
RDDPL0B	0	214	2/1 8/1-10(BTR)		RDD00	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTDPH0B	I	216	2/1		WRTD1I0	2A9
WRTDPL0B	I	218	2/1		WRTD0I0	2A8
0GRDM0DB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 15 16

MAIN STORE CONTROLLER AND MEMORY		DWG SIZE C2	ISSUE 1
BELL LABORATORIES	SD-1C902-03		B7CE

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03/06/73

PART OF FS 7

MEMORY

SYMBOL NO. 17

MEMORY PLANE 1
DATA BITS 23 AND 19

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP123.19	03-19	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CASI0	2F0
GRAS0B	I	003	7/15	202	RASI0	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDD190B	D	214	2/2 8/1-10(BTR)		RDD00	2G9
RDD230B	D	212	2/2 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD190B	I	218	2/2		WRTD0I0	2A8
WRTD230B	I	216	2/2		WRTD1I0	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 18

MEMORY PLANE 1
DATA BITS 22 AND 18

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP122.18	03-20	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CASI0	2F0
GRAS0B	I	003	7/15	202	RASI0	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDD180B	D	214	2/3 8/1-10(BTR)		RDD00	2G9
RDD220B	D	212	2/3 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD180B	I	218	2/3		WRTD0I0	2A8
WRTD220B	I	216	2/3		WRTD1I0	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 19

MEMORY PLANE 1
DATA BITS 21 AND 17

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP121.17	03-21	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CASI0	2F0
GRAS0B	I	003	7/15	202	RASI0	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDD170B	D	214	2/4 8/1-10(BTR)		RDD00	2G9
RDD210B	D	212	2/4 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD170B	I	218	2/4		WRTD0I0	2A8
WRTD210B	I	216	2/4		WRTD1I0	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 20

MEMORY PLANE 1
DATA BITS 20 AND 16

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP120.16	03-22	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CASI0	2F0
GRAS0B	I	003	7/15	202	RASI0	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDD160B	D	214	2/5 8/1-10(BTR)		RDD00	2G9
RDD200B	D	212	2/5 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD160B	I	218	2/5		WRTD0I0	2A8
WRTD200B	I	216	2/5		WRTD1I0	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 17 18 19 20

MAIN STORE CONTROLLER AND MEMORY

	DWG SIZE C2	ISSUE 1
BELL LABORATORIES	SD-1C902-03	B7CF

PRINTED IN U. S. A.

03/06/

PART OF FS 7

MEMORY

SYMBOL NO. 21

MEMORY PLANE 1
DATA BITS 15 AND 07

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP115.07	03-23	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA011	2D0
CEA11B	I	009	7/15	202	CEA111	2C
CEA21B	I	209	7/15	202	CEA211	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA010	2H0
GRCA10B	I	205	7/15	202	RCA110	2G0
GRCA20B	I	006	7/15	202	RCA210	2G0
GRCA30B	I	203	7/15	202	RCA310	2G0
GRCA40B	I	201	7/15	202	RCA410	2G0
GRCA50B	I	002	7/15	202	RCA510	2F0
GRCA60B	I	206	7/15	202	RCA610	2F0
GRW0B	I	001	7/15	202	RW10	2H0
RDD070B	0	214	2/6 8/1-10(BTR)		RDD00	2G9
RDD150B	D	212	2/6 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REF10	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD070B	I	218	2/6		WRTD010	2A8
WRTD150B	I	216	2/6		WRTD110	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 22

MEMORY PLANE 1
DATA BITS 14 AND 06

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP114.06	03-24	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA011	2D0
CEA11B	I	009	7/15	202	CEA111	2C0
CEA21B	I	209	7/15	202	CEA211	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA010	2H0
GRCA10B	I	205	7/15	202	RCA110	2G0
GRCA20B	I	006	7/15	202	RCA210	2G0
GRCA30B	I	203	7/15	202	RCA310	2G0
GRCA40B	I	201	7/15	202	RCA410	2G0
GRCA50B	I	002	7/15	202	RCA510	2F0
GRCA60B	I	206	7/15	202	RCA610	2F0
GRW0B	I	001	7/15	202	RW10	2H0
RDD060B	0	214	2/7 8/1-10(BTR)		RDD00	2G9
RDD140B	D	212	2/7 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REF10	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD060B	I	218	2/7		WRTD010	2A8
WRTD140B	I	216	2/7		WRTD110	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 23

MEMORY PLANE 1
DATA BITS 13 AND 05

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP113.05	03-25	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA011	2D0
CEA11B	I	009	7/15	202	CEA111	2C0
CEA21B	I	209	7/15	202	CEA211	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA010	2H0
GRCA10B	I	205	7/15	202	RCA110	2G0
GRCA20B	I	006	7/15	202	RCA210	2G0
GRCA30B	I	203	7/15	202	RCA310	2G0
GRCA40B	I	201	7/15	202	RCA410	2G0
GRCA50B	I	002	7/15	202	RCA510	2F0
GRCA60B	I	206	7/15	202	RCA610	2F0
GRW0B	I	001	7/15	202	RW10	2H0
RDD050B	0	214	2/8 8/1-10(BTR)		RDD00	2G9
RDD130B	0	212	2/8 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REF10	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD050B	I	218	2/8		WRTD010	2A8
WRTD130B	I	216	2/8		WRTD110	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 24

MEMORY PLANE 1
DATA BITS 12 AND 04

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP112.04	03-26	JL16	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA011	2D0
CEA11B	I	009	7/15	202	CEA111	2C0
CEA21B	I	209	7/15	202	CEA211	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA010	2H0
GRCA10B	I	205	7/15	202	RCA110	2G0
GRCA20B	I	006	7/15	202	RCA210	2G0
GRCA30B	I	203	7/15	202	RCA310	2G0
GRCA40B	I	201	7/15	202	RCA410	2G0
GRCA50B	I	002	7/15	202	RCA510	2F0
GRCA60B	I	206	7/15	202	RCA610	2F0
GRW0B	I	001	7/15	202	RW10	2H0
RDD040B	0	214	2/9 8/1-10(BTR)		RDD00	2G9
RDD120B	D	212	2/9 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REF10	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD040B	I	218	2/9		WRTD010	2A8
WRTD120B	I	216	2/9		WRTD110	2A9
0GRDMODB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 21 22 23 24

MAIN STORE CONTROLLER AND MEMORY

		DWG SIZE CZ	ISSUE 1
BELL LABORATORIES	SD-1C902-03		B7CG

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03/06/78

PART OF FS 7

MEMORY

SYMBOL NO. 25

MEMORY PLANE 1
DATA BITS 11 AND 03

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP111.03	03-27	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	1	009	7/15	202	CEA1I1	2C
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDD030B	0	214	2/10 8/1-10(BTR)		RDD00	2G9
RDD110B	D	212	2/10 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD030B	I	218	2/10		WRTD0I0	2A8
WRTD110B	I	216	2/10		WRTD1I0	2A9
0GRDM0DB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 26

MEMORY PLANE 1
DATA BITS 10 AND 02

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP110.02	03-28	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2FD
GRW0B	I	001	7/15	202	RWI0	2H0
RDD020B	D	214	2/11 8/1-10(BTR)		RDD00	2G9
RDD100B	0	212	2/11 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFI0	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRT0020B	I	218	2/11		WRTD0I0	2A8
WRTD100B	I	216	2/11		WRTD1I0	2A9
0GR0M0DB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 27

MEMORY PLANE 1
DATA BITS 09 AND 01

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP109.01	03-29	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	1	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3I0	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	I	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRW0B	I	001	7/15	202	RWI0	2H0
RDD010B	0	214	2/12 8/1-10(BTR)		RDD00	2G9
RDD090B	0	212	2/12 8/1-10(BTR)		RDD10	2G9
REF0B	I	208	7/15	202	REFID	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	D04	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD010B	I	218	2/12		WRTD0I0	2A8
WRTD090B	I	216	2/12		WRTD1I0	2A9
0GRDM0DB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3B0
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

SYMBOL NO. 28

MEMORY PLANE 1
DATA BITS 08 AND 00

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
MP108.DD	03-31	JL16	A	

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
			FS INFO		CP INFO	
-5V	PWR	019		202	-5	3H0
CEA01B	I	202	7/15	202	CEA0I1	2D0
CEA11B	I	009	7/15	202	CEA1I1	2C0
CEA21B	I	209	7/15	202	CEA2I1	2C0
GCAS0B	I	014	7/15	202	CAS10	2F0
GRAS0B	I	003	7/15	202	RAS10	2E0
GRCA00B	I	007	7/15	202	RCA0I0	2H0
GRCA10B	I	205	7/15	202	RCA1I0	2G0
GRCA20B	I	006	7/15	202	RCA2I0	2G0
GRCA30B	I	203	7/15	202	RCA3ID	2G0
GRCA40B	I	201	7/15	202	RCA4I0	2G0
GRCA50B	1	002	7/15	202	RCA5I0	2F0
GRCA60B	I	206	7/15	202	RCA6I0	2F0
GRDA	GRD	219	9/1		GRD	3B0
GRW0B	I	001	7/15	202	RWID	2H0
RDD000B	0	214	2/13 8/1-10(BTR)		RDD00	2G9
RDD080B	0	212	2/13 8/1-10(BTR)		RDD10	2G9
REF0B	1	208	7/15	202	REFID	2E0
SPARE01	GRD	204	7/15	202	GRD	3B0
SPARE03	GRD	004	7/15	202	GRD	3B0
SPARE05	GRD	008	7/15	202	GRD	3B0
SPARE07	GRD	005	7/15	202	GRD	3B0
SPARE09	GRD	207	7/15	202	GRD	3B0
SPARE16	I	018	7/15	202		
SPARE17	I	017	7/15	202		
SPARE18	I	016	7/15	202		
SPARE19	I	015	7/15	202		
WRTD000B	I	218	2/13		WRTD0I0	2A8
WRT0080B	I	216	2/13		WRTD1I0	2A9
0GRDM0DB	GRD	0GD		202	GRD	3B0
	GRD	2GD		202	GRD	3B0
	GRD	200		202	GRD	3B0
	GRD	213		202	GRD	3B0
	GRD	215		202	GRD	3BC
	GRD	217		202	GRD	3B0
	GRD	219		202	GRD	3B0
12V	PWR	000		202	+12	3G0
5V	PWR	011		202	+5	3B0
	PWR	211		202	+5	3B0

PART OF FS 7
SYMBOL(S) 25 26 27 28

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
C2	1

BELL LABORATORIES
SD-1C902-03
B7CH

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03/06/78

PART OF FS 7

MEMORY

COMPOSITE DIAGRAM 2

PHYSICAL LAYOUT

NOTES:

1. THESE CIRCUIT PACKS ARE REMOVED FOR A MAIN STORE USING A 16 BIT DATA WORD.
2. A MODULE IS DEFINED AS 128K WORDS.

EVEN NUMBERED MEMORY MODULE

		DATA BITS												
		0	1	2	3	4	5	6	7	16	17	18	19	PL
	FØB	8	9	10	11	12	13	14	15	20	21	22	23	PH
SYM NO.	1	14	13	12	11	10	9	8	7	6	5	4	3	2
	03-02	03-04	03-05	03-06	03-07	03-08	03-09	03-10	03-11	03-12	03-13	03-14	03-16	03-17
	JK25	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16

(SEE NOTE 1)

ODD NUMBERED MEMORY MODULE

	PL	19	18	17	16	7	6	5	4	3	2	1	0	
	PH	25	22	21	20	15	14	13	12	11	10	9	8	FØB
SYM NO.	16	17	18	19	20	21	22	23	24	25	26	27	28	15
	03-18	03-19	03-20	03-21	03-22	03-23	03-24	03-25	03-26	03-27	03-28	03-29	03-31	03-33
	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JK25

(SEE NOTE 1)

MEMORY PLANE LAYOUT
FRONT VIEW

MODULE 0	MODULE 1

MEMORY MODULE LAYOUT (NOTE 2)
FRONT VIEW

PART OF FS 7
COMPOSITE DIAGRAM 2

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE 6S

ISSUE 1

BELL LABORATORIES

SD-1C902-03

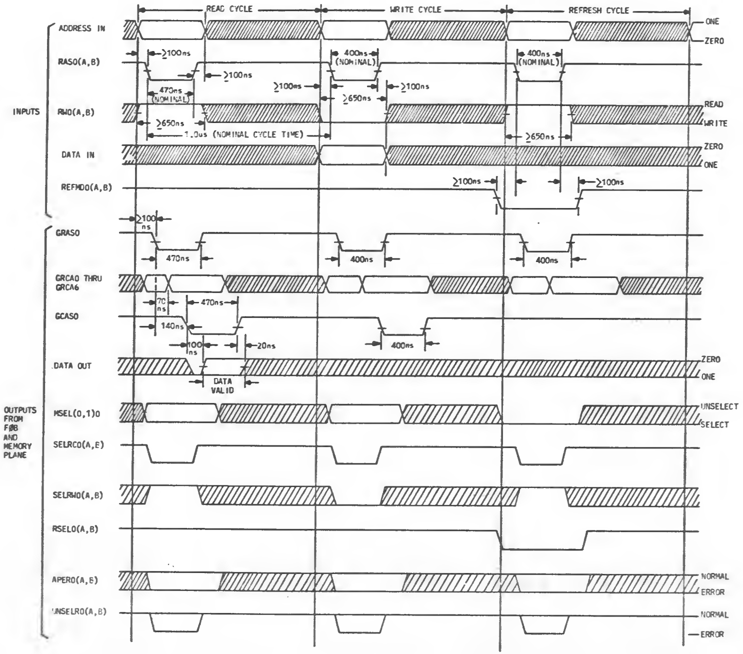
8738

PART OF FS 7

MEMORY

COMPOSITE DIAGRAM 1

TYPICAL TIMING RELATIONSHIPS



NOTES:

1. BECAUSE OF THE ASYNCHRONOUS NATURE OF THE MASM CIRCUIT, MOST OF THE TIMING AND LEVEL CONSTRAINTS WILL BE DETERMINED BY SPECIFIC APPLICATION REQUIREMENTS. THE INFORMATION PRESENTED HERE IS INTENDED AS A TYPICAL FUNCTIONAL GUIDE ONLY.
2. TIMING MEASUREMENTS ARE REFERENCED TO 1.3V POINTS.
3. LOW INPUT VOLTAGE LEVEL = $0V \leq E \leq 0.4V$.
4. HIGH INPUT VOLTAGE LEVEL = $2.4V \leq E \leq 5.0V$.
5. INPUT RISE AND FALL TIMES ARE ≤10ns.
6. OUTPUTS ARE OPEN COLLECTOR. CHARACTERISTICS ARE DETERMINED BY THE PULLUP NETWORK USED.
7. SHADED AREAS ▨ REPRESENT "DON'T CARE" STATES.

PART OF FS 7
COMPOSITE DIAGRAM 1

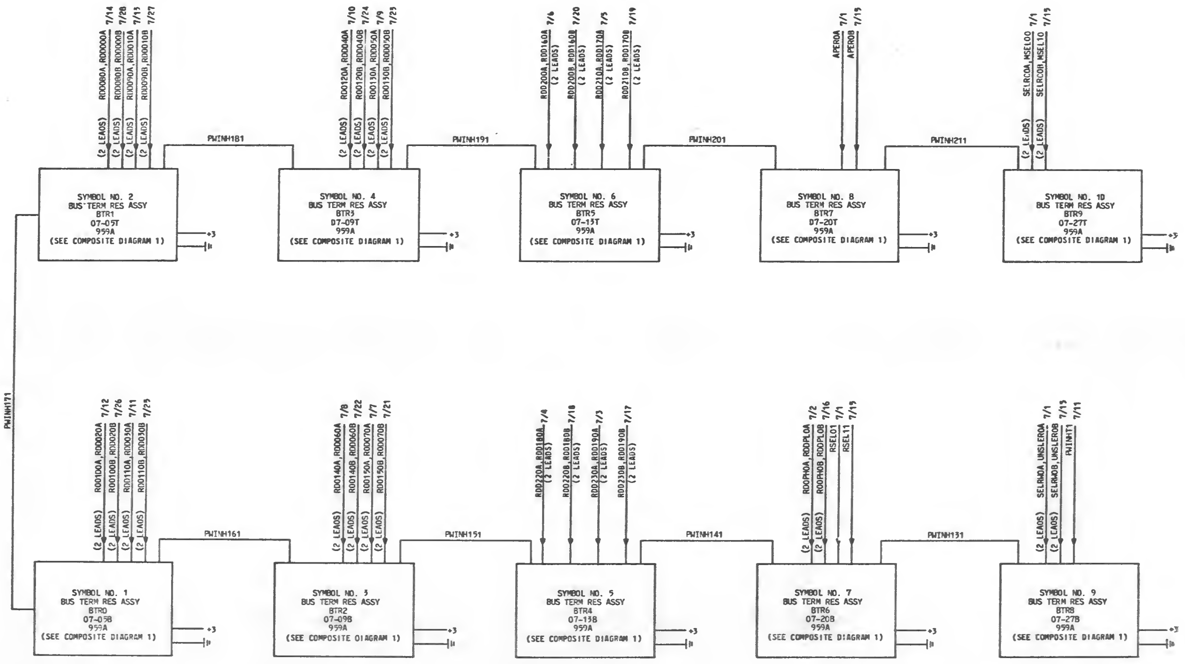
MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
65	1

BELL LABORATORIES | SD-1C902-03 | 87GA

PART OF FS 8

TERMINATIONS
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 8
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

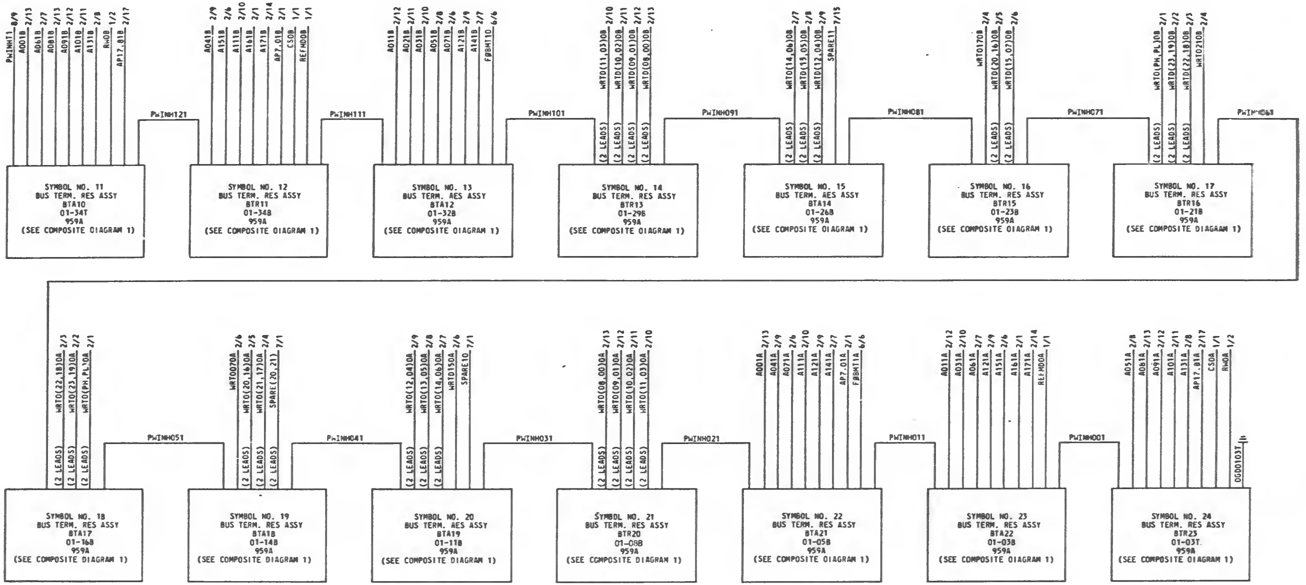
DWG SIZE 65
ISSUE 1

BELL LABORATORIES
SD-1C902-03
BBAA

PRINTED IN U.S.A.

PART OF FS 8

TERMINATIONS
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 8
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE 6S

ISSUE 1

BELL LABORATORIES

SD-1C902-03

88AB

PART OF FS 8

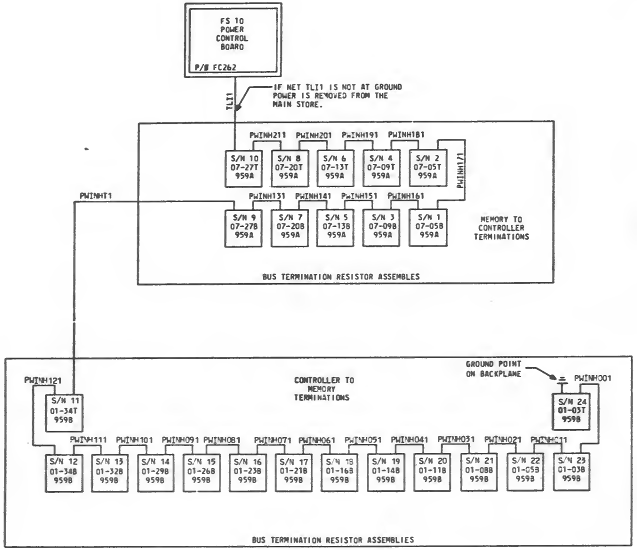
TERMINATIONS

COMPOSITE DIAGRAM I

BUS TERMINATION RESISTORS

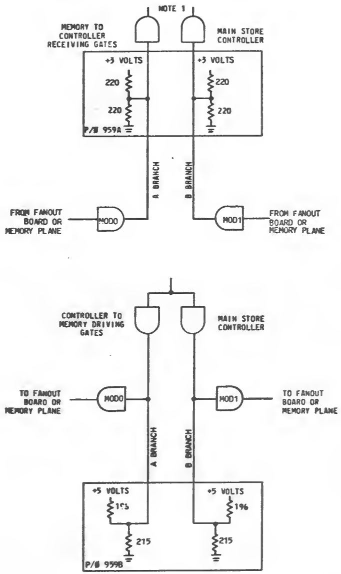
NOTES:

1. TYPICAL SIGNAL PATH FOR DRIVING BOTH TO AND FROM THE CONTROLLER AND THE MEMORY SHOWING THE BUS TERMINATION RESISTORS.
2. THE BUS TERMINATION RESISTOR ASSEMBLIES ARE SHOWN AS CLOSE AS POSSIBLE TO THEIR TRUE PHYSICAL LOCATION FROM A REAR VIEW OF THE MAIN STORE.
3. S/N DENOTES SYMBOL NUMBER.



BUS TERMINATION RESISTOR ASSEMBLY
POWER INTERLOCK

NOTE 2

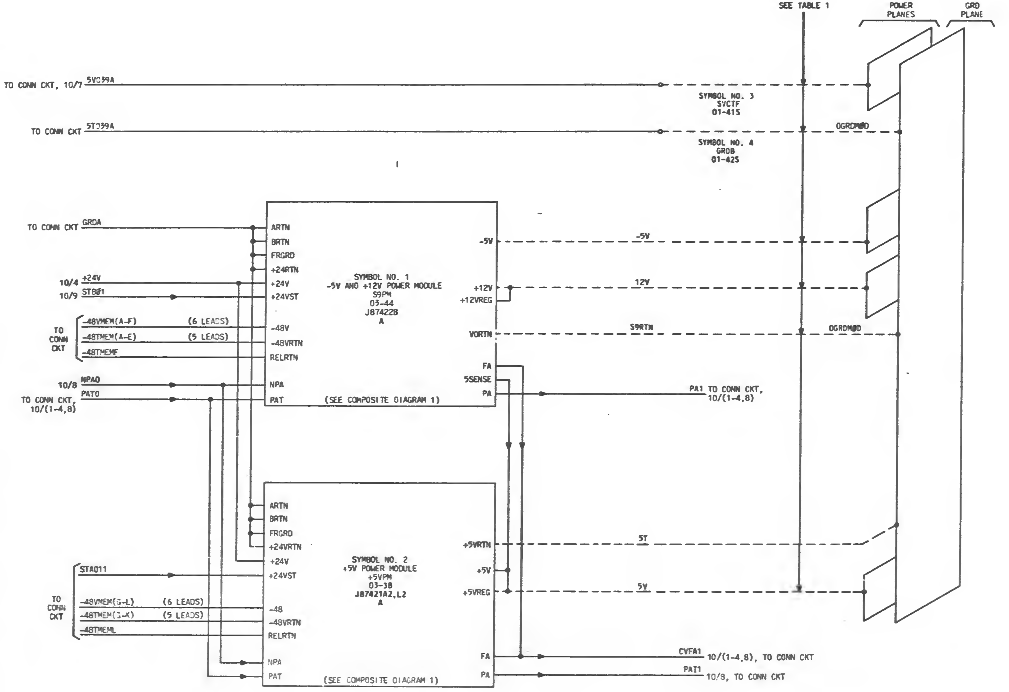

NOTE 1
MEMORY TO CONTROLLER RECEIVING GATES
MAIN STORE CONTROLLER
+3 VOLTS
+3 VOLTS
220
220
220
220
P/# 959A
A BRANCH
B BRANCH
FROM FANOUT BOARD OR MEMORY PLANE
MOD0
MOD1
FROM FANOUT BOARD OR MEMORY PLANE
CONTROLLER TO MEMORY DRIVING GATES
MAIN STORE CONTROLLER
TO FANOUT BOARD OR MEMORY PLANE
MOD0
MOD1
TO FANOUT BOARD OR MEMORY PLANE
A BRANCH
B BRANCH
+5 VOLTS
+5 VOLTS
196
196
215
215
P/# 959B

PART OF FS 8
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY ②	DWG SIZE 6S	ISS
BELL LABORATORIES	SD-1C902-03	88

PART OF FS 9

MEMORY POWER
INTERCONNECTION AND FLOW DIAGRAM



POWER DISTRIBUTION TABLE 1

CIRCUIT PACK	EQPT LOC	GRD PLANE	5V	-5V	12V
		TERM	TERM	TERM	TERM
JK25	03-02	200,319, 0G0,2G0	000,119		
JL16	03-04	200, 0G0,2G0, 213,215, 217,219	011,211	019	000
	03-05				
	03-06				
	03-07				
	03-08				
	03-09				
	03-10				
	03-11				
	03-12				
	03-13				
	03-14				
	03-16				
	03-17				
	03-18				
	03-19				
	03-20				
	03-21				
	03-22				
	03-23				
	03-24				
	03-25				
	03-26				
	03-27				
	03-28				
	03-29				
JL16	03-31	200, 0G0,2G0, 213,215, 217,219	011,211	019	000
JK25	03-33	200,319, 0G0,2G0	000,119		
J87421A	03-38	201-207 300-307	001-007 100-107		
J87422B	03-44	201-205 300-305		001-003 101-103	005-007 105-107

PART OF FS 9
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE
AS

BELL LABORATORIES SD-1C902-03

PART OF FS 9

MEMORY POWER

SYMBOL NO. 1

-5V AND +12V POWER MODULE

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
S9PM	03-44	J87422B	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	012	10/4		+24V	
	PWR	112	10/4		+24V	
-48TMEMA	I	116	TO CONN CKT		-48VRTN	
-48TMEMB	I	115	TO CONN CKT		-48VRTN	
-48TMEMC	I	114	TO CONN CKT		-48VRTN	
-48TMEMD	I	016	TO CONN CKT		-48VRTN	
-48TMEME	I	015	TO CONN CKT		-48VRTN	
-48TMEMF	I	014	TO CONN CKT		RELRTN	
-48VMEMA	I	119	TO CONN CKT		-48V	
-48VMEMB	I	118	TO CONN CKT		-48V	
-48VMEMC	I	117	TO CONN CKT		-48V	
-48VMEMD	I	019	TO CONN CKT		-48V	
-48VMEME	I	018	TO CONN CKT		-48V	
-48VMEMF	I	017	TO CONN CKT		-48V	
-5V	PWR	001		202	-5V	
	PWR	002		202	-5V	
	PWR	003		202	-5V	
	PWR	101		202	-5V	
	PWR	102		202	-5V	
	PWR	103		202	-5V	
CVFA1	OT	314	9/2		FA	
GRDA	GRD	200	7/15,7/28 9/2 TO CONN CKT	202	FRGRD	
	GRD	0GD	9/1		FRGRD	
	GRD	013	9/1		ARTN	
	GRD	111	9/1		+24RTN	
	GRD	113	9/1		ARTN	
	GRD	2GD	9/1		FRGRD	
	GRD	208	9/1		BRTN	
	GRD	319	9/1		FRGRD	
NPA0	I	315	10/8		NPA	
PAT0	I	312	9/2		PAT	
PA1	OT	313	10/1,10/2 10/3,10/4	204	PA	
	I		10/8 TO CONN CKT			
STB01	I	219	10/9		+24VST	
S9RTN	GRD	201		202	VORTN	
	GRD	202		202	VORTN	
	GRD	203		202	VORTN	
	GRD	204		202	VORTN	
	GRD	205		202	VORTN	
	GRD	300		202	VORTN	
	GRD	301		202	VORTN	
	GRD	302		202	VORTN	
	GRD	303		202	VORTN	
	GRD	304		202	VORTN	
	GRD	305		202	VORTN	
12V	PWR	005		202	+12V	
	PWR	006		202	+12V	
	PWR	007		202	+12V	
	PWR	105		202	+12V	
	PWR	106		202	+12V	
	PWR	107		202	+12V	
	I	108			+12VREG	
5V	I	100	9/2		SSENSE	

SYMBOL NO. 2

+5V POWER MODULE

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
+5VPM	03-38	J87421A2,L2	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	012	10/4		+24V	
	PWR	112	10/4		+24V	
-48TMEMG	I	116	TO CONN CKT		-48VRTN	
-48TMEMH	I	115	TO CONN CKT		-48VRTN	
-48TMEMI	I	114	TO CONN CKT		-48VRTN	
-48TMEMJ	I	016	TO CONN CKT		-48VRTN	
-48TMEMK	I	015	TO CONN CKT		-48VRTN	
-48TMEML	I	014	TO CONN CKT		RELRTN	
-48VMEMG	I	119	TO CONN CKT		-48V	
-48VMEMH	I	118	TO CONN CKT		-48V	
-48VMEMI	I	117	TO CONN CKT		-48V	
-48VMEMJ	I	019	TO CONN CKT		-48V	
-48VMEMK	I	018	TO CONN CKT		-48V	
-48VMEML	I	017	TO CONN CKT		-48V	
CVFA1	OT	314	9/1,10/1 10/2,10/3 10/4,10/8 TO CONN CKT		FA	
GRDA	GRD	0GD	9/1		FRGRD	
	GRD	013	9/1		ARTN	
	GRD	111	9/1		+24VRTN	
	GRD	113	9/1		ARTN	
	GRD	2GD	9/1		FRGRD	
	GRD	200	9/1		FRGRD	
	GRD	208	9/1		BRTN	
	GRD	319	9/1		FRGRD	
NPA0	I	315	10/8		NPA	
PAI1	OT	313	10/8 TO CONN CKT		PA	
PAT0	I	312	9/1,10/1 10/2,10/3 10/4,10/8 TO CONN CKT		PAT	
STA011	I	011			+24VST	
	I	210	TO CONN CKT		+24VST	
5T	GRD	201		202	+5VRTN	
	GRD	202		202	+5VRTN	
	GRD	203		202	+5VRTN	
	GRD	204		202	+5VRTN	
	GRD	205		202	+5VRTN	
	GRD	206		202	+5VRTN	
	GRD	207		202	+5VRTN	
	GPD	300		202	+5VRTN	
	GRD	301		202	+5VRTN	
	GRD	302		202	+5VRTN	
	GRD	303		202	+5VRTN	
	GRD	304		202	+5VRTN	
	GRD	305		202	+5VRTN	
	GRD	306		202	+5VRTN	
	GRD	307		202	+5VRTN	
5V	I	108	9/1		+5VREG	
	PWR	001		202	+5V	
	PWR	002		202	+5V	
	PWR	003		202	+5V	
	PWR	004		202	+5V	
	PWR	005		202	+5V	
	PWR	006		202	+5V	
	PWR	007		202	+5V	
	PWR	100		202	+5V	
	PWR	101		202	+5V	
	PWR	102		202	+5V	

SYMBOL NO. 2 (CONT)

+5V POWER MODULE

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
+5VPM	03-38	J87421A2,L2	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
5V	PWR	103		202	+5V	
	PWR	104		202	+5V	
	PWR	105		202	+5V	
	PWR	106		202	+5V	
	PWR	107		202	+5V	

SYMBOL NO. 3

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
GRD	01-42S	840851786		

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
0GROMODB	GRO	GRO		202		
5T039A	GRD	GRO	TO CONN CKT			

SYMBOL NO. 4

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
5VCTF	01-41S	840851786		

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
5V039A	PWR	5VCTF	10/7 TO CONN CKT	202		

PART OF FS 9
SYMBOL(S) 1 2 3 4

MAIN STORE CONTROLLER AND MEMORY
DWG SIZE C2
BELL LABORATORIES
SD-1C902-03

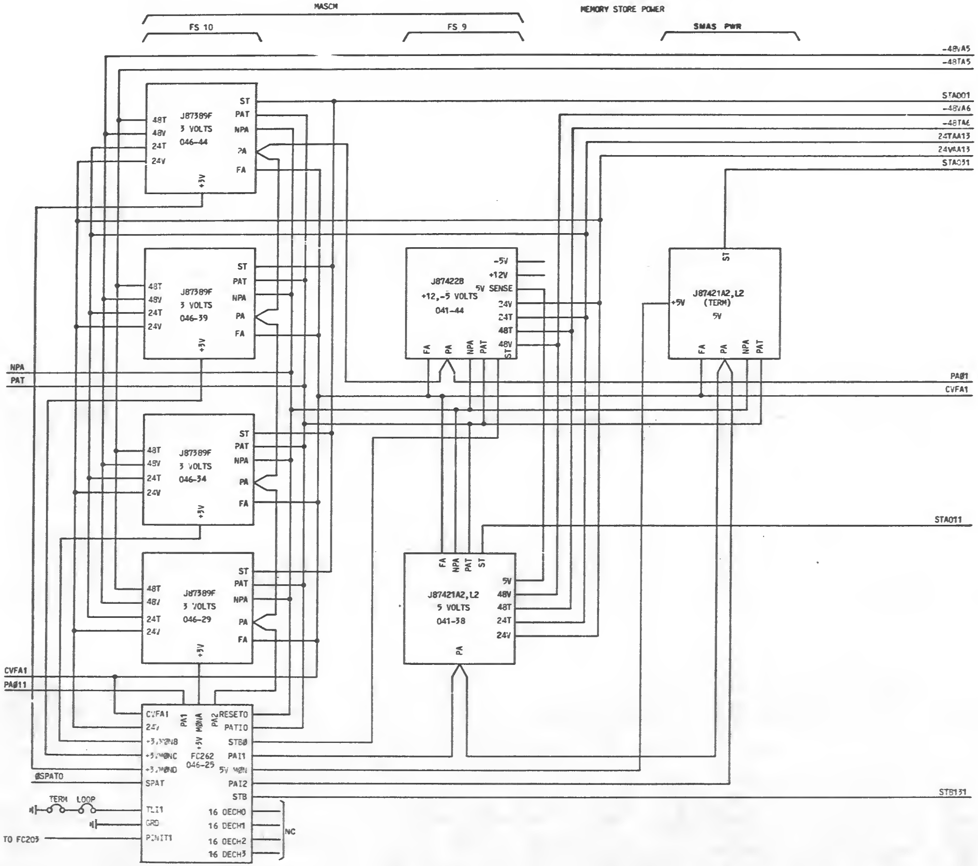
PRINTED IN U. S. A.

PART OF FS 9

MEMORY POWER
COMPOSITE DIAGRAM 1
MEMORY STORE POWER

NOTES:

1. NOT ALL CIRCUITRY ON THIS COMPOSITE IS CONTAINED IN FS
BUT A COMPLETE OVERVIEW OF THE MAIN STORE POWER IS SHOWN



PART OF FS 9
COMPOSITE DIAGRAM 1

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
65

ISSUE
1

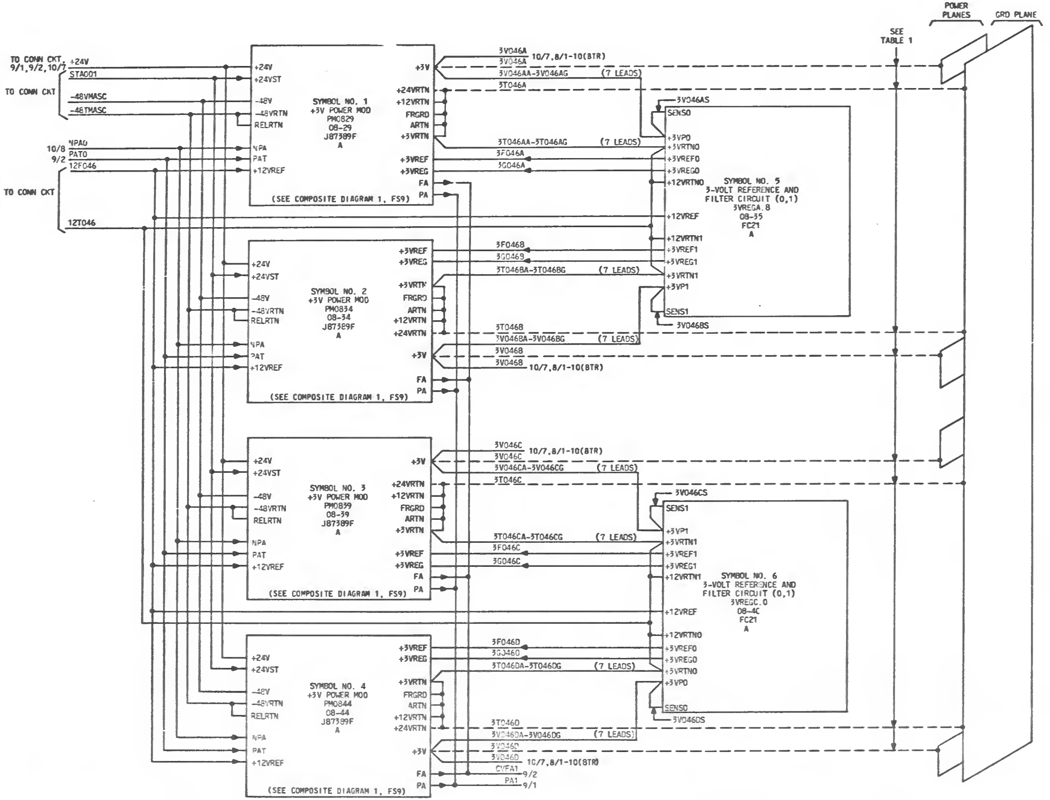
BELL LABORATORIES

SD-1C902-03

89G

PART OF FS 10

CONTROLLER POWER
INTERCONNECTION AND FLOW DIAGRAM



PART OF FS 10
INTERCONNECTION AND FLOW DIAGRAM

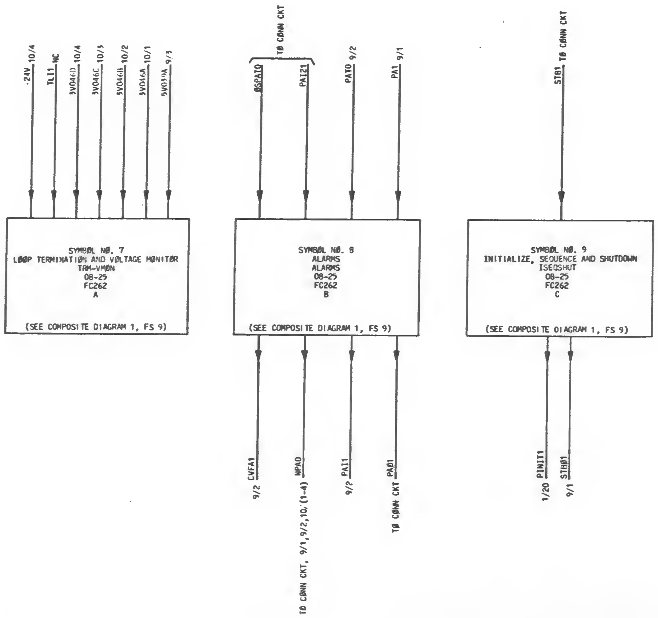
MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
ISSUE 1

BELL LABORATORIES
SD-1C902-03
BIOAA

PART OF FS 10

CØNTRØLLER PØWER
INTERCØNNECTIØN AND FLØW DIAGRAM



CIRCUIT PACK	EQPT LØC	GRD PLANE	3V046A	3V046B	3V046C	3V046D
		TERM	TERM	TERM	TERM	TERM
TS1 (NOTE 203)	08-01	0GD,2GD,200,319	000,119			
FA1060	08-02	0GD,2GD,200,319, 002,003,005,102, 105,202,205,302, 304		000,119		
↓	08-03	↓			000,119	
	08-04					000,119
	08-05		000,119			
	08-06			000,119		
	08-07				000,119	
	08-08					000,119
	08-09		000,119			
	08-10			000,119		
	08-11				000,119	
	08-12					000,119
FA1060	08-13	0GD,2GD,200,319, 002,003,005,102, 105,202,205,302, 304		000,119		
FA1067	08-14	0GD,2GD,200,319, 005,012,102,107, 114,205,212,217, 302,314,017	000,119			
FA1060	08-16	0GD,2GD,200,319, 002,003,005,102, 105,202,205,302, 304			000,119	
FA1062	08-17	0GD,2GD,200,319, 014				000,119
FA1061	08-18	0GD,2GD,200,319	000,119			
FA1066	08-19	0GD,2GD,200,319, 005,008,102,105, 107,202,205,207, 213,302,304,307		000,119		
FA1064	08-20	0GD,2GD,200,319, 005,102,107,205, 302,307			000,119	
FA1065	08-21	0GD,2GD,200,319				000,119
FA1063	08-22	0GD,2GD,200,319, 005,102,107,205, 302,307	000,119			
FC203	08-23	0GD,2GD,200,319, 001,101,201,301		000,119		
FA1071	08-24	0GD,2GD,200,319			000,119	
FC262	08-25	0GD,2GD,200,319				000,119
J87389F	08-29	0GD,2GD,200,319, 013,111,113, 201-208	100-107			
J87389F	08-34	0GD,2GD,200,319, 013,111,113, 201-208		100-107		
FC21	08-35					
J87389F	08-39	0GD,2GD,200,319, 013,111,113, 201-208			100-107	
FC21	08-40					
J87389F	08-44	0GD,2GD,200,319, 013,111,113, 201-208				100-107

PART ØF FS 10
INTERCØNNECTIØN AND FLØW DIAGRAM

MAIN STØRE CØNTRØLLER AND MEMØRY

DWG SIZE
65

ISSU
1

BELL LABORATORIES

SD-1C902-03

B10A

PART OF FS 10

CONTROLLER POWER

SYMBOL NO. 1

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0829	08-29	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	012	10/4	202	+24V	
	PWR	112	10/4	202	+24V	
-48TMASC	PWR	014		202	RELRTN	
	PWR	015		202	-48VRTN	
	PWR	016		202	-48VRTN	
	PWR	114		202	-48VRTN	
	PWR	115		202	-48VRTN	
	PWR	116		202	-48VRTN	
-48VMASC	PWR	017		202	-48V	
	PWR	018		202	-48V	
	PWR	019		202	-48V	
	PWR	117		202	-48V	
	PWR	118		202	-48V	
	PWR	119		202	-48V	
CVFA1	DT	314	9/2		FA	
NPA0	I	315	10/8		NPA	
PAT0	I	312	9/2		PAT	
PA1	DT	313	9/1	204	PA	
STA001	I	218	10/2,10/3 10/4 TD CONN CKT		+24VST	
	I	318	10/1		+24VST	
12F046	I	209	10/5		+12VREF	
3F046A	I	109	10/5		+3VREF	
3G046A	I	108	10/5		+3VREG	
3T046A	GRD	0GD		202		
	GRD	013		202	ARTN	
	GRD	111		202	+24VRTN	
	GRD	113		202	ARTN	
	GRD	2GD		202		
	GRD	200		202	FRGRD	
	GRD	201		202	+3VRTN	
	GRD	202		202	+3VRTN	
	GRD	203		202	+3VRTN	
	GRD	204		202	+3VRTN	
	GRD	205		202	+3VRTN	
	GRD	206		202	+3VRTN	
	GRD	207		202	+3VRTN	
	GRD	208		202	+12VRTN	
	GRD	319		202	FRGRD	
3T046AA	GRD	201	10/5		+3VRTN	
3T046AB	GRD	202	10/5		+3VRTN	
3T046AC	GRD	203	10/5		+3VRTN	
3T046AD	GRD	204	10/5		+3VRTN	
3T046AE	GRD	205	10/5		+3VRTN	
3T046AF	GRD	206	10/5		+3VRTN	
3T046AG	GRD	207	10/5		+3VRTN	
3V046A	PWR	100	10/7 8/1-10(BTR)	202	+3V	
	PWR	101		202	+3V	
	PWR	102		202	+3V	
	PWR	103		202	+3V	
	PWR	104		202	+3V	
	PWR	105		202	+3V	
	PWR	106		202	+3V	
	PWR	107		202	+3V	
3V046AA	PWR	100	10.5		+3V	
3V046AB	PWR	101	10/5		+3V	
3V046AC	PWR	102	10/5		+3V	
3V046AD	PWR	103	10/5		+3V	

SYMBOL NO. 1 (CONT)

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0829	08-29	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3V046AE	PWR	104	10/5		+3V	
3V046AF	PWR	105	10/5		+3V	
3V046AG	PWR	106	10/5		+3V	
3V046AS	PWR	107	10/5		+3V	

SYMBOL NO. 2

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0834	08-34	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	012	10/4	202	+24V	
	PWR	112	10/4	202	+24V	
-48TMASC	PWR	014		202	RELRTN	
	PWR	015		202	-48VRTN	
	PWR	016		202	-48VRTN	
	PWR	114		202	-48VRTN	
	PWR	115		202	-48VRTN	
	PWR	116		202	-48VRTN	
-48VMASC	PWR	017		202	-48V	
	PWR	018		202	-48V	
	PWR	019		202	-48V	
	PWR	117		202	-48V	
	PWR	118		202	-48V	
	PWR	119		202	-48V	
CVFA1	DT	314	9/2		FA	
NPA0	I	315	10/8		NPA	
PAT0	I	312	9/2		PAT	
PA1	DT	313	9/1	204	PA	
STA001	I	218	10/1		+24VST	
	I	318	10/1		+24VST	
12F046	I	209	10/5		+12VREF	
12T046	GRD	319	10/5		FRGRD	
3F046B	I	109	10/5		+3VREF	
3G046B	I	108	10/5		+3VREG	
3T046B	GRD	0GD		202		
	GRD	013		202	ARTN	
	GRD	111		202	+24VRTN	
	GRD	113		202	ARTN	
	GRD	2GD		202		
	GRD	200		202	FRGRD	
	GRD	201		202	+3VRTN	
	GRD	202		202	+3VRTN	
	GRD	203		202	+3VRTN	
	GRD	204		202	+3VRTN	
	GRD	205		202	+3VRTN	
	GRD	206		202	+3VRTN	
	GRD	207		202	+3VRTN	
	GRD	208		202	+12VRTN	
	GRD	319		202	FRGRD	

SYMBOL NO. 2 (CONT)

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0834	08-34	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3T046BA	GRD	201	10/5		+3VRTN	
3T046BB	GRD	202	10/5		+3VRTN	
3T046BC	GRD	203	10/5		+3VRTN	
3T046BD	GRD	204	10/5		+3VRTN	
3T046BE	GRD	205	10/5		+3VRTN	
3T046BF	GRD	206	10/5		+3VRTN	
3T046BG	GRD	207	10/5		+3VRTN	
3V046B	PWR	100	10/7 8/1-10(BTR)	202	+3V	
	PWR	101		202	+3V	
	PWR	102		202	+3V	
	PWR	103		202	+3V	
	PWR	104		202	+3V	
	PWR	105		202	+3V	
	PWR	106		202	+3V	
	PWR	107		202	+3V	
3V046BA	PWR	100	10/5		+3V	
3V046BB	PWR	101	10/5		+3V	
3V046BC	PWR	102	10/5		+3V	
3V046BD	PWR	103	10/5		+3V	
3V046BE	PWR	104	10/5		+3V	
3V046BF	PWR	105	10/5		+3V	
3V046BG	PWR	106	10/5		+3V	
3V046BS	PWR	107	10/5		+3V	

SYMBOL NO. 3

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0839	08-39	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	012	10/4	202	+24V	
	PWR	112	10/4	202	+24V	
-48TMASC	PWR	014		202	RELRTN	
	PWR	015		202	-48VRTN	
	PWR	016		202	-48VRTN	
	PWR	114		202	-48VRTN	
	PWR	115		202	-48VRTN	
	PWR	116		202	-48VRTN	
-48VMASC	PWR	017		202	-48V	
	PWR	018		202	-48V	
	PWR	019		202	-48V	
	PWR	117		202	-48V	
	PWR	118		202	-48V	
	PWR	119		202	-48V	
CVFA1	DT	314	9/2		FA	
NPA0	I	315	10/8		NPA	
PAT0	I	312	9/2		PAT	
PA1	DT	313	9/1	204	PA	
STA001	I	218	10/1		+24VST	
	I	318	10/1		+24VST	
12F046	I	209	10/5		+12VREF	

SYMBOL NO. 3 (CONT)

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0839	08-39	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
12T046	GRD	319	10/5		FRGRD	
3F046C	I	109	10/6		+3VREF	
3G046C	I	108	10/6		+3VREG	
3T046C	GRD	0GD		202		
	GRD	013		202	ARTN	
	GRD	111		202	+24VRTN	
	GRD	113		202	ARTN	
	GRD	2GD		202		
	GRD	200		202	FRGRD	
	GRD	201		202	+3VRTN	
	GRD	202		202	+3VRTN	
	GRD	203		202	+3VRTN	
	GRD	204		202	+3VRTN	
	GRD	205		202	+3VRTN	
	GRD	206		202	+3VRTN	
	GRD	207		202	+3VRTN	
	GRD	208		202	+12VRTN	
	GRD	319		202	FRGRD	
3T046CA	GRD	201	10/6		+3VRTN	
3T046CB	GRD	202	10/6		+3VRTN	
3T046CC	GRD	203	10/6		+3VRTN	
3T046CD	GRD	204	10/6		+3VRTN	
3T046CE	GRD	205	10/6		+3VRTN	
3T046CF	GRD	206	10/6		+3VRTN	
3T046CG	GRD	207	10/6		+3VRTN	
3V046C	PWR	100	10/7 8/1-10(BTR)	202	+3V	
	PWR	101		202	+3V	
	PWR	102		202	+3V	
	PWR	103		202	+3V	
	PWR	104		202	+3V	
	PWR	105		202	+3V	
	PWR	106		202	+3V	
	PWR	107		202	+3V	
3V046CA	PWR	100	10/6		+3V	
3V046CB	PWR	101	10/6		+3V	
3V046CC	PWR	102	10/6		+3V	
3V046CD	PWR	103	10/6		+3V	
3V046CE	PWR	104	10/6		+3V	
3V046CF	PWR	105	10/6		+3V	
3V046CG	PWR	106	10/6		+3V	
3V046CS	PWR	107	10/6		+3V	

PART OF FS 10
SYMBOL(S) 1 2 3

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE CZ
ISSUE 1

BELL LABORATORIES
SD-1C902-03
B10CA

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03/06/

PART OF FS 10

CONTROLLER POWER

SYMBOL NO. 4

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0844	08-44	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	012	9/1,9/2 10/1,10/2 10/3,10/7 TO CONN CKT	202	+24V	
	PWR	112	10/4	202	+24V	
-48TMASC	PWR	014		202	RELRTN	
	PWR	015	TO CONN CKT	202	-48VRTN	
	PWR	016		202	-48VRTN	
	PWR	114		202	-48VRTN	
	PWR	115		202	-48VRTN	
	PWR	116		202	-48VRTN	
-48VMASC	PWR	017	TO CONN CKT	202	-48V	
	PWR	018		202	-48V	
	PWR	019		202	-48V	
	PWR	117		202	-48V	
	PWR	118		202	-48V	
	PWR	119		202	-48V	
CVFA1	OT	314	9/2		FA	
NPA0	I	315	10/8		NPA	
PAT0	I	312	9/2		PAT	
PA1	OT	313	9/1	204	PA	
STA001	I	218	10/1		+24VST	
	I	318	10/1		+24VST	
12F046	I	209	10/5		+12VREF	
3F046D	I	109	10/6		+3VREF	
3G046D	I	108	10/6		+3VREG	
3T046D	GRD	0GD		202		
	GRD	013	TO CONN CKT	202	ARTN	
	GRD	111		202	+24VRTN	
	GRD	113		202	ARTN	
	GRD	2GD		202		
	GRD	200		202	FRGRD	
	GRD	201		202	+3VRTN	
	GRD	202		202	+3VRTN	
	GRD	203		202	+3VRTN	
	GRD	204		202	+3VRTN	
	GRD	205		202	+3VRTN	
	GRD	206		202	+3VRTN	
	GRD	207		202	+3VRTN	
	GRD	208		202	+12VRTN	
	GRD	319		202	FRGRD	
3T046DA	GRD	201	10/6		+3VRTN	
3T0460B	GRD	202	10/6		+3VRTN	
3T0460C	GRD	203	10/6		+3VRTN	
3T04600	GRD	204	10/6		+3VRTN	
3T046DE	GRD	205	10/6		+3VRTN	
3T046DF	GRD	206	10/6		+3VRTN	
3T046DG	GRD	207	10/6		+3VRTN	
3V0460	PWR	100	10/7 8/1-10(BTR)	202	+3V	
	PWR	101		202	+3V	
	PWR	102		202	+3V	
	PWR	103		202	+3V	
	PWR	104		202	+3V	
	PWR	105		202	+3V	
	PWR	106		202	+3V	
	PWR	107		202	+3V	
3V046DA	PWR	100	10/6		+3V	
3V0460B	PWR	101	10/6		+3V	
3V046DC	PWR	102	10/6		+3V	
3V046DD	PWR	103	10/6		+3V	

SYMBOL NO. 4 (CONT)

+3V POWER MOD

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
PM0844	08-44	J87389F	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3V046DE	PWR	104	10/6		+3V	
3V046DF	PWR	105	10/6		+3V	
3V046DG	PWR	106	10/6		+3V	
3V046DS	PWR	107	10/6		+3V	

SYMBOL NO. 5

3-VOLT REFERENCE AND FILTER CIRCUIT (0,1)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
3VREGA.B	08-35	FC21	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		000			+3VP0	
		019			+3VP1	
		100			+3VRTN0	
		119			+3VP1	
		219			+3VP1	
		300			+3VRTN0	
12F046	OI	210	10/6 10/1,10/2 10/3,10/4 TO CONN CKT		+12VREF	
	OI	109	10/5		+12VREF	
12T046	GRD	200	10/6,10/2 10/3,10/6 TO CONN CKT		+3VRTN0	
	OT	108	10/5		+12VRTN0	
	OT	211	10/5		+12VRTN1	
	GRD	0GD	10/5			
	GRD	2GD	10/5			
	GRD	319	10/5		+3VRTN1	
3F046A	O	009	10/1		+3VREF0	
3F046B	O	310	10/2		+3VREF1	
3G046A	O	008	10/1		+3VREG0	
3G046B	O	311	10/2		+3VREG1	
3T046AA	GRD	101	10/1		+3VRTN0	
	GRD	301	10/1		+3VRTN0	
3T046AB	GRD	102	10/1		+3VRTN0	
	GRD	302	10/1		+3VRTN0	
3T046AC	GRD	103	10/1		+3VRTN0	
	GRD	303	10/1		+3VRTN0	
3T046AD	GRD	104	10/1		+3VRTN0	
	GRD	304	10/1		+3VRTN0	
3T046AE	GRD	105	10/1		+3VRTN0	
	GRD	305	10/1		+3VRTN0	
3T046AF	GRD	106	10/1		+3VRTN0	
	GRD	306	10/1		+3VRTN0	
3T046AG	GRD	107	10/1		+3VRTN0	
	GRD	307	10/1		+3VRTN0	
3T046BA	GRD	112	10/2		+3VRTN1	
	GRD	312	10/2		+3VRTN1	
3T046BB	GRD	113	10/2		+3VRTN1	
	GRD	313	10/2		+3VRTN1	

SYMBOL NO. 5 (CONT)

3-VOLT REFERENCE AND FILTER CIRCUIT (0,1)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
3VREGA.B	08-35	FC21	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3T046BC	GRD	114	10/2		+3VRTN1	
3T046BC	GRD	314	10/2		+3VRTN1	
3T046BD	GRD	115	10/2		+3VRTN1	
	GRD	315	10/2		+3VRTN1	
3T046BE	GRD	116	10/2		+3VRTN1	
	GRD	316	10/2		+3VRTN1	
3T046BF	GRD	117	10/2		+3VRTN1	
	GRD	317	10/2		+3VRTN1	
3T046BG	GRD	118	10/2		+3VRTN1	
	GRD	318	10/2		+3VRTN1	
3V046AA	PWR	001	10/1		+3VP0	
	PWR	201	10/1		+3VP0	
3V046AB	PWR	002	10/1		+3VP0	
	PWR	202	10/1		+3VP0	
3V046AC	PWR	003	10/1		+3VP0	
	PWR	203	10/1		+3VP0	
3V046AD	PWR	004	10/1		+3VP0	
	PWR	204	10/1		+3VP0	
3V046AE	PWR	005	10/1		+3VP0	
	PWR	205	10/1		+3VP0	
3V046AF	PWR	006	10/1		+3VP0	
	PWR	206	10/1		+3VP0	
3V046AG	PWR	007	10/1		+3VP0	
	PWR	207	10/1		+3VP0	
3V046AS	I	309	10/1		SENS0	
3V046BA	PWR	012	10/2		+3VP1	
	PWR	212	10/2		+3VP1	
3V046BB	PWR	013	10/2		+3VP1	
	PWR	213	10/2		+3VP1	
3V046BC	PWR	014	10/2		+3VP1	
	PWR	214	10/2		+3VP1	
3V046BD	PWR	015	10/2		+3VP1	
	PWR	215	10/2		+3VP1	
3V046BE	PWR	016	10/2		+3VP1	
	PWR	216	10/2		+3VP1	
3V046BF	PWR	017	10/2		+3VP1	
	PWR	217	10/2		+3VP1	
3V046BG	PWR	018	10/2		+3VP1	
	PWR	218	10/2		+3VP1	
3V046BS	I	010	10/2		SENS1	

PART OF FS 10
SYMBOL(S) 4 5

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE CZ
ISSUE 1

BELL LABORATORIES
SD-1C902-03
B10

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PART OF FS 10

CONTROLLER POWER

SYMBOL NO. 6

3-VOLT REFERENCE AND FILTER CIRCUIT (0,1)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
3VREGC.0	08-40	FC21	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
		000			+3VP0	
		019			+3VP1	
		100			+3VRTN0	
		119			+3VP1	
		219			+3VP1	
		300			+3VRTN0	
12F046	OI	109	10/5		+12VREF	
	OI	210	10/5		+12VREF	
12T046	OT	108	10/5		+12VRTN0	
	OT	211	10/5		+12VRTN1	
	GRD	0GD	10/5			
	GRD	2GD	10/5			
	GRD	200	10/5		+3VRTN0	
	GRD	319	10/5		+3VRTN1	
3F046C	O	310	10/3		+3VREF1	
3F046D	O	009	10/4		+3VREF0	
3G046C	O	311	10/3		+3VREG1	
3G046D	O	008	10/4		+3VREG0	
3T046CA	GRD	112	10/3		+3VRTN1	
	GRD	312	10/3		+3VRTN1	
3T046CB	GRD	113	10/3		+3VRTN1	
	GRD	313	10/3		+3VRTN1	
3T046CC	GRD	114	10/3		+3VRTN1	
	GRD	314	10/3		+3VRTN1	
3T046CD	GRD	115	10/3		+3VRTN1	
	GRD	315	10/3		+3VRTN1	
3T046CE	GRD	116	10/3		+3VRTN1	
	GRD	316	10/3		+3VRTN1	
3T046CF	GRD	117	10/3		+3VRTN1	
	GRD	317	10/3		+3VRTN1	
3T046CG	GRD	118	10/3		+3VRTN1	
	GRD	318	10/3		+3VRTN1	
3T046DA	GRD	101	10/4		+3VRTN0	
	GRD	301	10/4		+3VRTN0	
3T046DB	GRD	102	10/4		+3VRTN0	
	GRD	302	10/4		+3VRTN0	
3T046DC	GRD	103	10/4		+3VRTN0	
	GRD	303	10/4		+3VRTN0	
3T046DD	GRD	104	10/4		+3VRTN0	
	GRD	304	10/4		+3VRTN0	
3T046DE	GRD	105	10/4		+3VRTN0	
	GRD	305	10/4		+3VRTN0	
3T046DF	GRD	106	10/4		+3VRTN0	
	GRD	306	10/4		+3VRTN0	
3T046DG	GRD	107	10/4		+3VRTN0	
	GRD	307	10/4		+3VRTN0	
3V046CA	PWR	012	10/3		+3VP1	
	PWR	212	10/3		+3VP1	
3V046CB	PWR	013	10/3		+3VP1	
	PWR	213	10/3		+3VP1	
3V046CC	PWR	014	10/3		+3VP1	
	PWR	214	10/3		+3VP1	
3V046CD	PWR	015	10/3		+3VP1	
	PWR	215	10/3		+3VP1	
3V046CE	PWR	016	10/3		+3VP1	
	PWR	216	10/3		+3VP1	
3V046CF	PWR	017	10/3		+3VP1	
	PWR	217	10/3		+3VP1	
3V046CG	PWR	018	10/3		+3VP1	
	PWR	218	10/3		+3VP1	

SYMBOL NO. 6 (CONT)

3-VOLT REFERENCE AND FILTER CIRCUIT (0,1)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
3VREGC.0	08-40	FC21	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3V046CS	I	010	10/3		SENS1	
3V046DA	PWR	001	10/4		+3VP0	
	PWR	201	10/4		+3VP0	
3V046DB	PWR	002	10/4		+3VP0	
	PWR	202	10/4		+3VP0	
3V046DC	PWR	003	10/4		+3VP0	
	PWR	203	10/4		+3VP0	
3V046DD	PWR	004	10/4		+3VP0	
	PWR	204	10/4		+3VP0	
3V046DE	PWR	005	10/4		+3VP0	
	PWR	205	10/4		+3VP0	
3V046DF	PWR	006	10/4		+3VP0	
	PWR	206	10/4		+3VP0	
3V046DG	PWR	007	10/4		+3VP0	
	PWR	207	10/4		+3VP0	
3V046DS	I	309	10/4		SENS0	

SYMBOL NO. 7

LOOP TERMINATION AND VOLTAGE MONITOR

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
TRM-VMON	08-25	FC262	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
+24V	PWR	117	10/4		+24V	
	PWR	118	10/4		+24V	
TLI1	I	003			TLI1	
3T046D	GRD	0GD		202	GRD	
	GRD	2GD		202	GRD	
	GRD	200		202	GRD	
	GRD	319		202	GRD	
3V046A	I	302	10/1		3VMONA	
3V046B	I	303	10/2		3VMONB	
3V046C	I	304	10/3		3VMONC	
3V046D	I	305	10/4		3VMOND	
	PWR	000		202	+3V	
	PWR	119		202	+3V	
5V039A	I	308	9/4		5VMON	

SYMBOL NO. 8

ALARMS

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ALARMS	08-25	FC262	B	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
CVFA1	CT	102	9/2		CVFA1	
NPA0	C	103	9/1,9/2 10/1,10/2 10/3,10/4 TO CONN CKT		RESET0	
OSPAT0	I	109	TO CONN CKT		SPAT	
PAI1	CT	105	9/2		PAI1	
PAI21	I	106	TO CONN CKT		PAI2	
PAO1	C	108	TO CONN CKT		PA1	
PAT0	I	104	9/2		PATI0	
PA1	I	107	9/1	204	PA2	

SYMBOL NO. 9

INITIALIZE.SEQUENCE AND SHUTDOWN

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
ISEQSHUT	08-25	FC262	C	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
PINIT1	O	001	1/20		PINITI1	
STBO1	CT	112	10/9		STBO	
	CT	111			STBO	
	I		9/1			
STB1	I	114	TO CONN CKT		STB	
	I	115			STB	

SYMBOL NO. 10

(SEE NOTE 203)

DESIG	EQPT LOC	CODE	ELEM IDENT	OPT
TS1	08-01	(NOTE 203)	A	

FS INFO					CP INFO	
LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
3T046A	GRD	0GD		202		
	GRD	2GD		202		
	GRD	200		202		
	GRD	319		202		
3V046A	PWR	000		202		
	PWR	119		202		

PART OF FS 10
SYMBOL(S) 6 7 8 9 10

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
C2

BELL LABORATORIES
SD-1C902-03

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APP FIG. 1

CIRCUIT PACK

EQPT LOC	08-01		08-02		08-03		08-04		08-05		08-06		08-07	
DESIG	TS1													
CODE	(NOTE 203)		FA1060		FA1060		FA1060		FA1060		FA1060		FA1060	
OPTION														
ELEM IDENT	CKT		CKT		CKT		CKT		CKT		CKT		CKT	
CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM
A		10/10	BSC08.00	1/18	BSC09.01	1/17	BSC10.02	1/16	BSC11.03	1/15	BSC12.04	1/14	BSC13.05	1/13
B			A.D08.00	2/13	A.D09.01	2/12	A.D10.02	2/11	A.D11.03	2/10	A.D12.04	2/9	A.D13.05	2/8
C			REF08.00	3/13	REF09.01	3/12	REF10.02	3/11	REF11.03	3/10	REF12.04	3/9	REF13.05	3/8
D			WPRSEG0	4/11	WPRSEG1	4/10	WPRSEG2	4/9	WPRSEG3	4/8	WPRSEG4	4/7	WPRSEG5	4/6
E			ERR08.00	5/20	ERR09.01	5/19	ERR10.02	5/18	ERR11.03	5/17	ERR12.04	5/16	ERR13.05	5/15
F			DGA08.00	6/20	DGA09.01	6/19	DGA10.02	6/18	DGA11.03	6/17	DGA12.04	6/16	DGA13.05	6/15

EQPT LOC	08-08		08-09		08-16		08-17		08-18		08-19		EQPT LOC
DESIG													DESIG
CODE	FA1060		FA1060		FA1060		FA1062		FA1071		FA1066		CODE
OPTION													OPTION
ELEM IDENT	CKT		CKT		CKT		CKT		CKT		CKT		ELEM IDENT
CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A	BSC14.06	1/12	BSC15.07	1/11	BSCPH.PL	1/6	CM.WLDRV	1/5	CNTTIMGN	1/1	ACPH.PDR	2/17	A
B	A.D14.06	2/7	A.D15.07	2/6	A.DPH.PL	2/1	A19.MPH	2/15	ABIT18	2/16	NMPFADCK	5/5	B
C	REF14.06	3/7	REF15.07	3/6	REF16.PL	3/5	DPREFCNT	3/2	REFCNTRL	3/3	RFMCCDA	6/6	C
D	WPRSEG6	4/5	WPRSEG7	4/4	WPRSPAR	4/3	TAPSELDA	6/7	SELSFTCK	5/1	REFCOUNT	3/1	D
E	ERR14.06	5/14	ERR15.07	5/13	ISOPH.PL	5/8	WPSEGDEC	4/2	CTSSDA	6/4			E
F	DGA14.06	6/14	DGA15.07	6/13	DCAPH.PL	6/8	CNTLDPLC	5/2					F

CIRCUIT PACK

EQPT LOC	08-20		08-21		08-22		08-23		08-24		08-25		08-35		08-40		EQPT LOC
DESIG					IOSR.BUF								3VREGA.B		3VREGC.D		DESIG
CODE	FA1064		FA1065		FA1063		FC203		FA1068		FC262		FC21		FC21		CODE
OPTION																	OPTION
ELEM IDENT	CKT		CKT		CKT		CKT		CKT		CKT		CKT		CKT		ELEM IDENT
CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A	IOORDDEC	6/3	CREG.DEC	1/2		6/2	IOINTFCE	6/1	CTLSGRPT	1/3	TRM-VMON	10/7		10/5		10/6	A
B	IODECHK	5/7	WPMODDEC	4/1			ABIT17	2/14	SDDP.ERA	5/4	ALARMS	10/8					B
C			TMWPDPHC	5/3					ISO.WRMD	6/21	ISEQSHUT	10/9					C
D			T.BDDA	6/5			REFA17	3/4			CTLSGDLY	1/4					D
E							CLK.PINT	1/20									E
F																	F

BTR ASSY

OPTION	DESIG	FS/SYM	CODE
	BTP0	8/1	959A
	BTR1	8/2	959A
	BTR2	8/3	959A
	BTR3	8/4	959A
	BTR4	8/5	959A
	BTR5	8/6	959A
	BTR6	8/7	959A
	BTR7	8/8	959A
	BTR8	8/9	959A
	BTR9	8/10	959A
	BTR10	8/11	959B
	BTR11	8/12	959B
	BTR12	8/13	959B
	BTR13	8/14	959B
	BTR14	8/15	959B
	BTR15	8/16	959B
	BTR16	8/17	959B
	BTR17	8/18	959B
	BTR18	8/19	959B
	BTR19	8/20	959B
	BTR20	8/21	959B
	BTR21	8/22	959B

BTR ASSY (CONT)

OPTION	DESIG	FS/SYM	CODE
	BTR22	8/23	959B
	BTR23	8/24	959B

POWER MODULE

OPTION	DESIG	FS/SYM	CODE
	+5VPM	9/2	J87421A2,L2
	PM0829	10/1	J87389F
	PM0834	10/2	J87389F
	PM0839	10/3	J87389F
	PM0844	10/4	J87389F
	S9PM	9/1	J87422B

ISSUE
1

MAIN STORE CONTROLLER AND MEMORY

BELL TELEPHONE LABORATORIES
INCORPORATED

DWG SIZE
E2

SD-1C902-03-C1

APP FIG. 2

CIRCUIT PACK

EQPT LOC	03-02	03-04	03-05	03-06	03-07	03-08	03-09	03-10	03-11	03-17	EQPT LOC
DESIG	F080	MP008.00	MP009.01	MP010.02	MP011.03	MP012.04	MP013.05	MP014.06	MP015.07	MPOPH.PL	DESIG
CODE	JK25	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	JL16	CODE
OPTION											OPTION
ELEM IDENT	CKT	CKT	CKT	CKT	CKT	CKT	CKT	CKT	CKT	CKT	ELEM IDENT

CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A		7/1		7/14		7/13		7/12		7/11		7/10		7/9		7/8		7/7		7/2	A
B																					B
C																					C
D																					D
E																					E
F																					F

APP FIG. 3

CIRCUIT PACK

EQPT LOC	03-12	03-13	03-14	03-16	EQPT LOC
DESIG	MP020.16	MP021.17	MP022.18	MP023.19	DESIG
CODE	JL16	JL16	JL16	JL16	CODE
OPTION					OPTION
ELEM IDENT	CKT	CKT	CKT	CKT	ELEM IDENT

CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A		7/6		7/5		7/4		7/3	A
B									B
C									C
D									D
E									E
F									F

ISSUE
1

MAIN STORE CONTROLLER AND MEMORY

BELL TELEPHONE LABORATORIES
INCORPORATED

DWG SIZE
C2

SD-1C902-03-C2

SD-1C902-03-C2

APP FIG. 4

CIRCUIT PACK

EQPT LOC	03-18		03-23		03-24		03-25		03-26		03-27		03-28		03-29		03-31		03-33		EQPT LOC
DESIG	MP1PH.PL		MP115.07		MP114.06		MP113.05		MP112.04		MP111.03		MP110.02		MP109.01		MP108.00		F081		DESIG
CODE	JL16		JL16		JL16		JL16		JL16		JL16		JL16		JL16		JL16		JK25		CODE
OPTION																					OPTION
ELEM IDENT	CKT		CKT		CKT		CKT		CKT		CKT		CKT		CKT		CKT		CKT		ELEM IDENT
CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A		7/16		7/21		7/22		7/23		7/24		7/25		7/26		7/27		7/28		7/15	A
B																					B
C																					C
D																					D
E																					E
F																					F

APP FIG. 5

CIRCUIT PACK

EQPT LOC	03-19		03-20		03-21		03-22		EQPT LOC
DESIG	MP123.19		MP122.18		MP121.17		MP120.16		DESIG
CODE	JL16		JL16		JL16		JL16		CODE
OPTION									OPTION
ELEM IDENT	CKT		CKT		CKT		CKT		ELEM IDENT
CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A		7/17		7/18		7/19		7/20	A
B									B
C									C
D									D
E									E
F									F

APP FIG. 6

CIRCUIT PACK

EQPT LOC	08-10		08-11		08-12		08-13		08-14		EQPT LOC
DESIG											DESIG
CODE	FA1060		FA1060		FA1060		FA1060		FA1067		CODE
OPTION											OPTION
ELEM IDENT	CKT		CKT		CKT		CKT		CKT		ELEM IDENT
CKT	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	DESIG	FS/SYM	CKT
A	BSC20.16	1/10	BSC21.17	1/9	BSC22.18	1/8	BSC23.19	1/7	ZBDPARBD	1/19	A
B	DZB20.16	2/5	DZB21.17	2/4	DZB22.18	2/3	DZB23.19	2/2	ZBPISODA	6/22	B
C	NCR20.16	3/17	NCR21.17	3/16	NCR22.18	3/15	NCR23.19	3/14	ZBPARCNV	2/18	C
D	NCW20.16	4/15	NCW21.17	4/14	NCW22.18	4/13	NCW23.19	4/12	ZBPARCHK	5/6	D
E	ISO20.16	5/12	ISO21.17	5/11	ISO22.18	5/10	ISO23.19	5/9			E
F	DGA20.16	6/12	DGA21.17	6/11	DGA22.18	6/10	DGA23.19	6/9			F

MAIN STORE CONTROLLER AND MEMORY

BELL TELEPHONE LABORATORIES
INCORPORATED

DWG SIZE
C2

SD-1C902-03-

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER
+24V	3/4	+24	APP FIG. 1
-48:MASC	3	-48	
-48:MEM(A-L)*	3	-48	

BATTERY SYMBOL	VOLTAGE RANGE
+24	20.75 TO 26.25
-48	42.75 TO 52.5

* UNIT LEADS SHALL BE MECHANICALLY INTERCONNECTED ON USERS CONNECTING CIRCUIT VIA 942G TYPE BERG CONNECTOR OR EQUIVALENT.

102.

FEATURE OR OPTION		PROVIDE		
		APP FIG	APP OR DRG	QUANTITY
MAIN STORE CONTROLLER CKT AND POWER MODS INCLUDING CONTROLLER AND MEMORY WIRING		1		1 PER CKT
MAIN CONTROLLER CAPACITY (16K MEMORY DEVICES)	131,072 WORDS BY 18 BITS	2		1 PER CKT
	131,072 WORDS BY 26 BITS	2,3,6		1 PER CKT
	262,144 WORDS BY 18 BITS	2,4		1 PER CKT
	262,144 WORDS BY 26 BITS	2-6		1 PER CKT

103.

RECORD OF APP FIGURES WIRING AND APPARATUS CHANGES

CHANGED ON ISSUE	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	A&M	MD

CIRCUIT NOTES (CONT):

104. TO PROVIDE ADDRESS IDENTITY FOR 128K MEMORY MODULES USING 16K MEMORY DEVICES, PROVIDE 30 GA, D3-TYPE MODULE SELECT STRAPS AT THE ASSOCIATED FANOUT BOARD TERMINALS AS SHOWN BELOW.

MODULE	WIRE FANOUT BOARD TERMINALS
0	113 TO 115
1	113 TO 019

EQUIPMENT NOTES:

201. UNLESS OTHERWISE SPECIFIED, IF A NET HAS A TOTAL NET WIRING DISTANCE GREATER THAN 7.0 INCHES, NET SEGMENTS WITHIN A WIRING DISTANCE GREATER THAN 6.0 INCHES SHALL REQUIRE A 30 GAUGE TWISTED PAIR, C2 OR D3-TYPE WIRING WHICH TERMINATES ON COAXIAL TERMINATING FIELD TERMINALS OR DIRECTLY ON THE APPARATUS. ALL OTHER NET SEGMENTS SHALL BE 30 GAUGE AUTOMATICALLY INSTALLED D4-TYPE WIRING.

202. WIRING SHALL BE PRINTED WIRING PATHS LOCATED IN MULTILAYER PRINTED WIRING BOARDS, ED-4C091-30 OR ED-4C015-30.

203. CIRCUIT PACK POSITION 08-01 IS USED AS A TERMINAL STRIP FOR CABLING TO THE DIAGNOSTIC FIELD TEST SET. NO CIRCUIT PACK SHALL BE MOUNTED IN THIS POSITION. TERMINALS 200,319,CGD AND 2GD ARE CONNECTED TO GROUND VIA THE MULTILAYER PRINTED WIRING BOARD (MLPWB). TERMINALS 000 AND 119 ARE CONNECTED TO POWER VIA THE MLPWB. SEE POWER DISTRIBUTION TABLES.

204. NET PA1 SHALL FORM A SERIES CHAIN THROUGH THE UNIT TERMINATING ON CIRCUIT PACK FC262.

205. CIRCUIT PACK CAN BE MOUNTED IN THIS POSITION. TERMINALS 200,319,0GD AND 2GD ARE CONNECTED TO GROUND VIA THE MLPWB. TERMINALS 000 AND 119 ARE CONNECTED TO POWER (+5) VIA THE MLPWB.

MAIN STORE CONTROLLER AND MEMORY

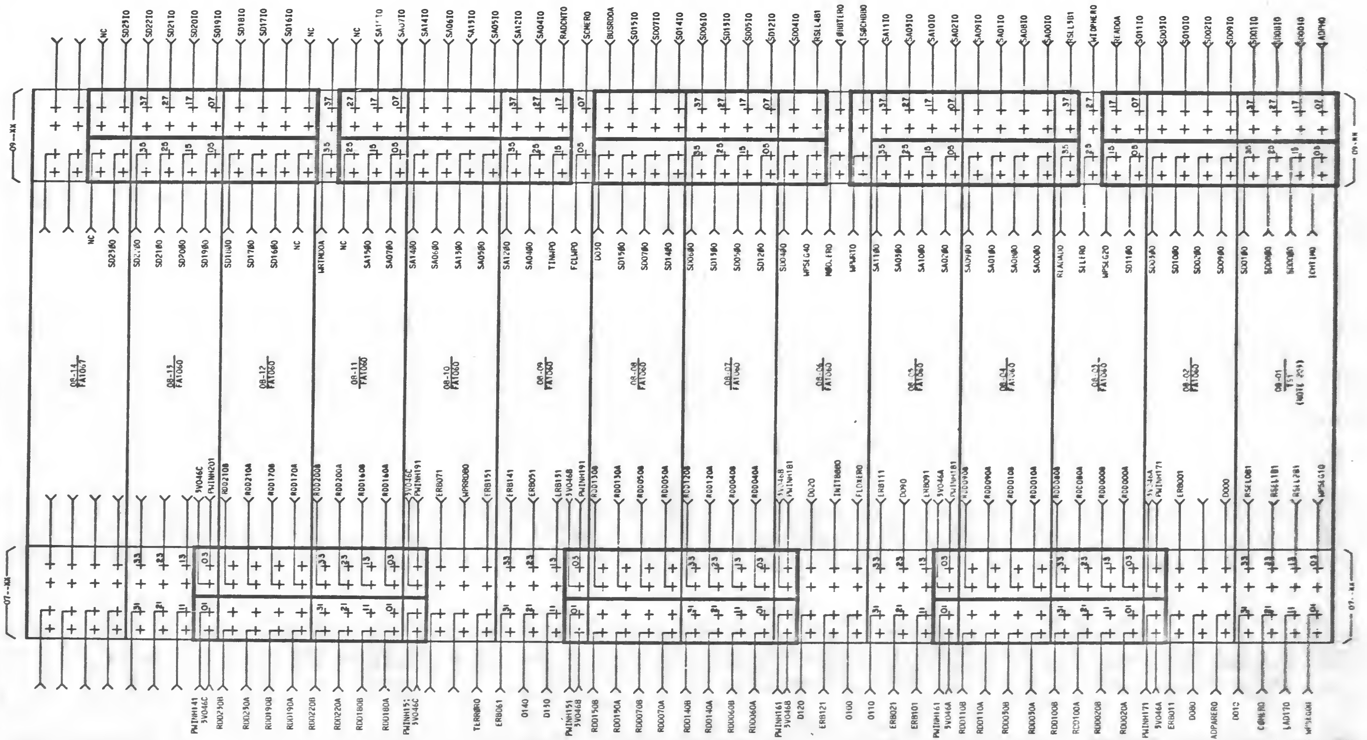
DWG SIZE
6S

BELL LABORATORIES
SD-1C902-03

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
VALUES PRECEDED BY THE SYMBOL + (PLUS) OR – (MINUS)
ARE IN VOLTS.

302.



(REAR VIEW)

MAIN STORE CONTROLLER AND MEMORY

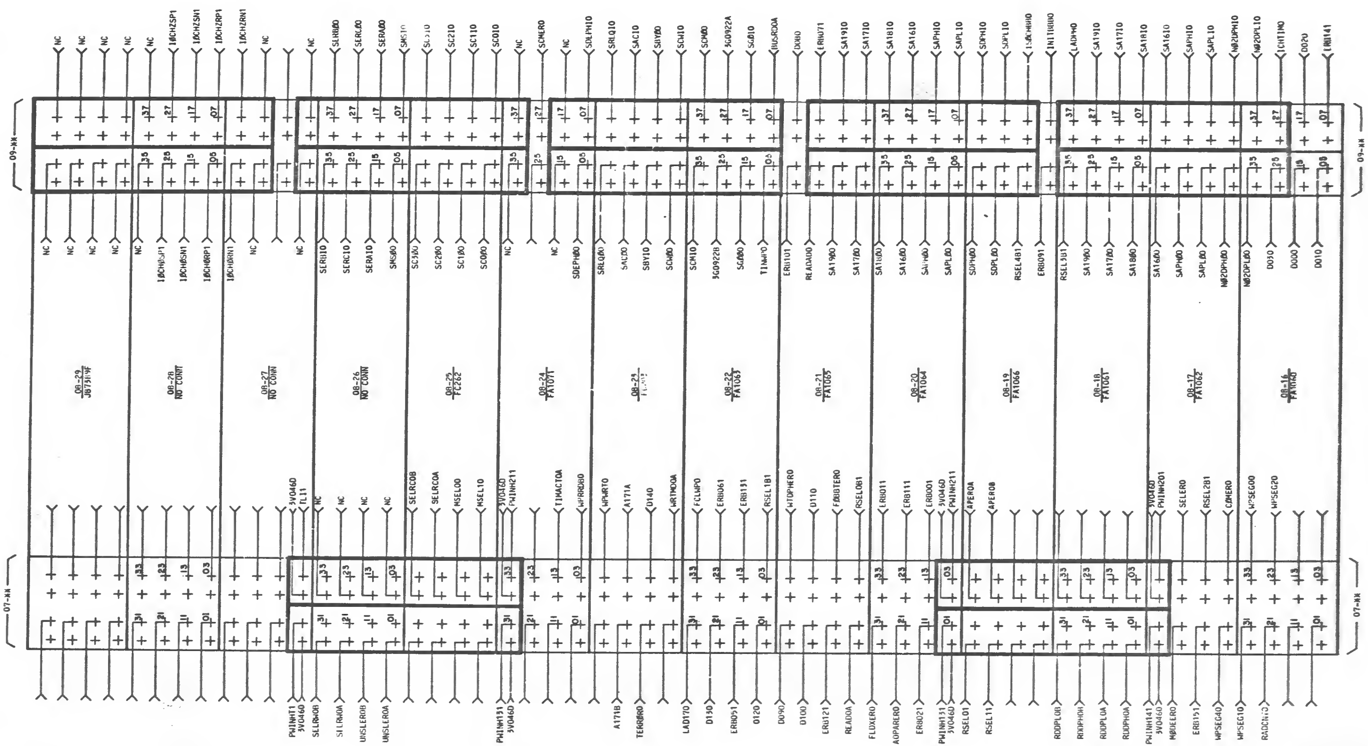
DWG SIZE
6S

BELL LABORATORIES

SD-1C902-03

INFORMATION NOTES: (CONT)

302. (CONT)



(REAR VIEW)

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE 6S

ISSUE 1

BELL LABORATORIES

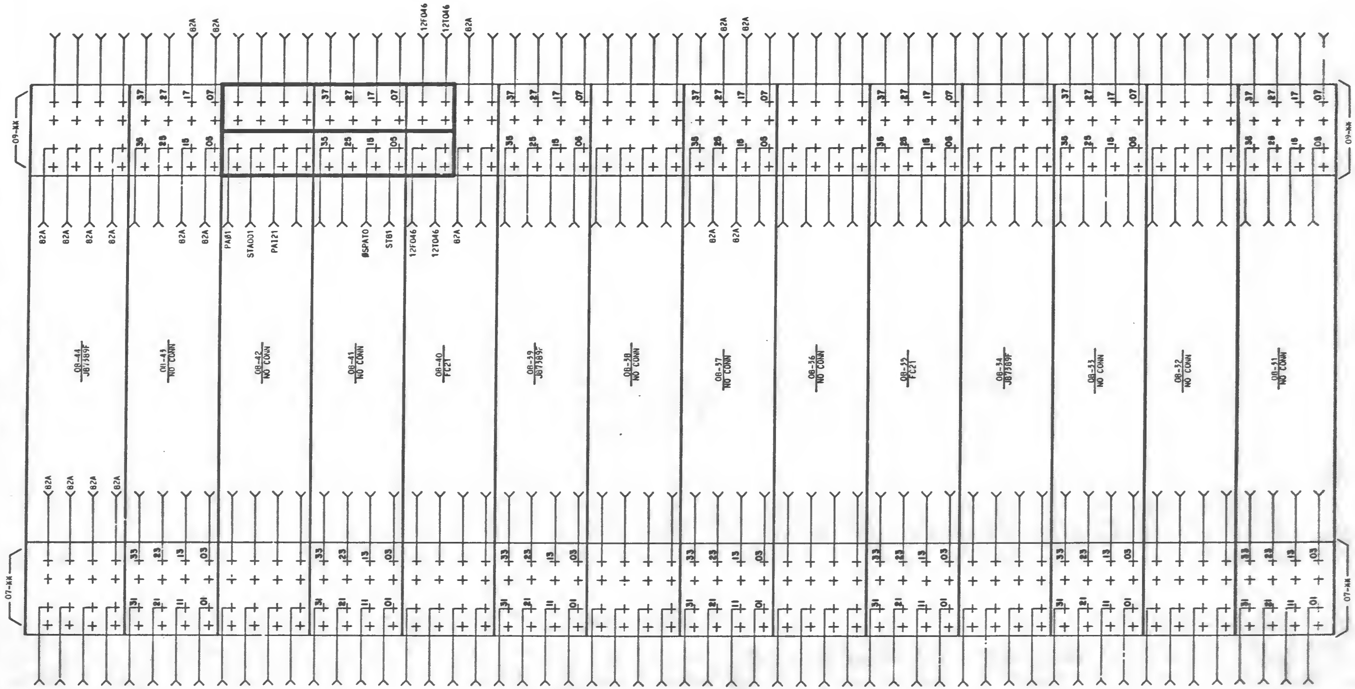
SD-1C902-03

03

8282-110173

INFORMATION NOTES: (CONT)

302. (CONT)



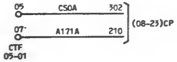
(REAR VIEW)

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE 65	ISSUE 1
BELL LABORATORIES	SD-1C902-03	D4

CAD NOTES:

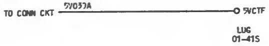
1. THE FOLLOWING SHOWS THE SYMBOLIC EQUIVALENT OF THE TABULAR REPRESENTATION:

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	WIRE METHOD	SYM	TERMINAL	LEAD DESIG	TO TERMINATION	TERMINAL	OPT	NOTE
....	....	....	....	05-01	CTF	(NOTE 1)	....	....	
	CSOA			05	CSOA	08-18 CP	302		
	A171A			07	A171A	08-23 CP	210		



2. THE FOLLOWING SHOWS THE SYMBOLIC EQUIVALENT OF THE TABULAR REPRESENTATION:

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	WIRE METHOD	SYM	TERMINAL	LEAD DESIG	TO TERMINATION	TERMINAL	OPT	NOTE
	5VCTF			01-41S	LUG	(NOTE 2)	....	....	
TO CONN CKT	5V039A			5VCTF	5V039A				



MAIN STORE CONTROLLER AND MEMORY ②

DWG SIZE	ISSUE
6S	1

BELL LABORATORIES

SD-1C902-03

G81

PRINTED IN U.S.A.

CAD 1
UNIT SYMBOL

ELEMENT IDENTIFIER
A

MAIN STORE BUS

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
NO2DPHIO	OI	09-17-07	08-14-305	2/18	
NO2DPLIO	OI	09-16-37	08-14-207	2/18	
SACIO	I	09-23-27	08-24-214	1/3	
SAPHIO	I	09-20-17	08-19-011	2/17	
SAPHIO	I	09-17-27	08-19-011	2/17	
SAPLIO	I	09-20-07	08-16-018	2/1	
SAPLIO	I	09-17-17	08-16-018	2/1	
SA00IO	I	09-04-07	08-02-018	2/13	
SA01IO	I	09-04-27	08-03-018	2/12	
SA02IO	I	09-05-07	08-04-018	2/11	
SA03IO	I	09-05-27	08-05-018	2/10	
SA04IO	I	09-09-27	08-06-018	2/9	
SA05IO	I	09-10-07	08-07-018	2/8	
SA06IO	I	09-10-27	08-08-018	2/7	
SA07IO	I	09-11-07	08-09-018	2/6	
SA08IO	I	09-04-17	08-02-317	2/13	
SA09IO	I	09-04-37	08-03-317	2/12	
SA10IO	I	09-05-17	08-04-317	2/11	
SA11IO	I	09-05-37	08-05-317	2/10	
SA12IO	I	09-09-37	08-06-317	2/9	
SA13IO	I	09-10-17	08-07-317	2/8	
SA14IO	I	09-10-37	08-08-317	2/7	
SA15IO	I	09-11-17	08-09-317	2/6	
SA16IO	I	09-20-27	08-16-317	2/1	
SA16IO	I	09-17-37	08-16-317	2/1	
SA17IO	I	09-21-07	08-23-005	2/14	
SA17IO	I	09-18-17	08-23-005	2/14	
SA18IO	I	09-20-37	08-18-213	2/16	
SA18IO	I	09-18-07	08-18-213	2/16	
SA19IO	I	09-21-17	08-17-118	2/15	
SA19IO	I	09-18-27	08-17-118	2/15	
SBY00	OI	09-23-17	08-24-106	1/3	
SCM00	OI	09-22-37	08-24-202	1/3	
SCWIO	I	09-23-07	08-24-207	1/3	
SC0IO	I	09-25-07	08-21-219	1/2	
SC1IO	I	09-25-17	08-21-103	1/2	
SC2IO	I	09-25-27	08-21-216	1/2	
SC3IO	I	09-25-37	08-21-218	1/2	
SDEPHIO	OI	09-24-07	08-14-306	2/18	
SDPHIO	OI	09-19 37	08-16-013	2/1	
SDPLIO	OI	09-19-27	08-16-314	2/1	
SD00IO	OI	09-01-17	08-02-314	2/13	
SD01IO	OI	09-01-37	08-03-314	2/12	
SD02IO	OI	09-02-17	08-04-314	2/11	
SD03IO	OI	09-02-37	08-05-314	2/10	
SD04IO	OI	09-06-37	08-06-314	2/9	
SD05IO	OI	09-07-17	08-07-314	2/8	
SD06IO	OI	09-07-37	08-08-314	2/7	
SD07IO	OI	09-08-17	08-09-314	2/6	
SD08IO	OI	09-01-27	08-02-013	2/13	
SD09IO	OI	09-02-07	08-03-013	2/12	
SD10IO	OI	09-02-27	08-04-013	2/11	
SD11IO	OI	09-03-07	08-05-013	2/10	
SD12IO	OI	09-07-07	08-06-013	2/9	
SD13IO	OI	09-07-27	08-07-013	2/8	
SD14IO	OI	09-08-07	08-08-013	2/7	
SD15IO	OI	09-08-27	08-09-013	2/6	
SD16IO	OI	09-12-17	08-10-314	2/5	
SD17IO	OI	09-12-27	08-11-314	2/4	
SD18IO	OI	09-12 37	08-12-314	2/3	
SD19IO	OI	09-13-07	08-13-314	2/2	
SD20IO	OI	09-13-17	08-10-013	2/5	
SD21IO	OI	09-13-27	08-11-013	2/4	
SD22IO	OI	09-13-37	08-12-013	2/3	
SD23IO	OI	09-14-07	08-13-013	2/2	
SERA00	O	09-26-17	08-24-117	5/4	

ELEMENT IDENTIFIER (CONT)
A

MAIN STORE BUS

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
SERB00	O	09-26-37	08-21-013	5/3	
SERC00	OI	09-26-27	08-24-002	5/4	
SG0IO	I	09-22-17	08-18-116	1/1	
SMSIO	I	09-26-07	08-17-312	1/5	
SREQIO	I	09-23-37	08-23-006	1/20	
3G0922A	G	09-22-27			

ELEMENT IDENTIFIER
B

GROWTH MAIN STORE BUS

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
NO2DPH00	OI	09-17-05	08-14-216	2/18	
NO2DPL00	OI	09-16-35	08-14-209	2/18	
SAC00	O	09-23-25	08-24-016	1/3	
SAPH00	O	09-17-25	08-19-007	2/17	
SAPH00	O	09-20-15	08-19-007	2/17	
SAPL00	O	09-17-15	08-16-112	2/1	
SAPL00	O	09-20-05	08-16-112	2/1	
SA0000	O	09-04-05	08-02-112	2/13	
SA0100	O	09-04-25	08-03-112	2/12	
SA0200	O	09-05-05	08-04-112	2/11	
SA0300	O	09-05-25	08-05-112	2/10	
SA0400	O	09-09-25	08-06-112	2/9	
SA0500	O	09-10-05	08-07-112	2/8	
SA0600	O	09-10-25	08-08-112	2/7	
SA0700	O	09-11-05	08-09-112	2/6	
SA0800	O	09-04-15	08-02-312	2/13	
SA0900	O	09-04-35	08-03-312	2/12	
SA1000	O	09-05-15	08-04-312	2/11	
SA1100	O	09-05-35	08-05-312	2/10	
SA1200	O	09-09-35	08-06-312	2/9	
SA1300	O	09-10-15	08-07-312	2/8	
SA1400	O	09-10-35	08-08-312	2/7	
SA1500	O	09-11-15	08-09-312	2/6	
SA1600	O	09-17-35	08-16-312	2/1	
SA1600	O	09-20-25	08-16-312	2/1	
SA1700	O	09-18-15	08-23-203	2/14	
SA1700	O	09-21-05	08-23-203	2/14	
SA1800	O	09-18-05	08-18-015	2/16	
SA1800	O	09-20-35	08-18-015	2/16	
SA1900	O	09-18-25	08-17-211	2/15	
SA1900	O	09-21-15	08-17-211	2/15	
SBYIO	IO	09-23-15	08-24-115	1/3	
SCMIO	IO	09-22-35	08-24-111	1/3	
SCW00	O	09-23-05	08-24-014	1/3	
SC000	O	09-25-05	08-21-105	1/2	
SC100	O	09-25-15	08-21-213	1/2	
SC200	O	09-25-25	08-21-312	1/2	
SC300	O	09-25-35	08-21-205	1/2	
SDEPH00	OI	09-24-05	08-14-015	2/18	
SDPH00	OI	09-19-35	08-16-213	2/1	
SDPL00	OI	09-19-25	08-16-113	2/1	
SD0000	OI	09-01-15	08-02-113	2/13	
SD0100	OI	09-01-35	08-03-113	2/12	
SD0200	OI	09-02-15	08-04-113	2/11	
SD0300	OI	09-02-35	08-05-113	2/10	
SD0400	OI	09-06-35	08-06-113	2/9	
SD0500	OI	09-07-15	08-07-113	2/8	
SD0600	OI	09-07-35	08-08-113	2/7	
SD0700	OI	09-08-15	08-09-113	2/6	

ELEMENT IDENTIFIER (CONT)
B

GROWTH MAIN STORE BUS

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
SD0800	OI	09-01-25	08-02-213	2/13	
SD0900	OI	09-02-05	08-03-213	2/12	
SD1000	OI	09-02-25	08-04-213	2/11	
SD1100	OI	09-03-05	08-05-213	2/10	
SD1200	OI	09-07-05	08-06-213	2/9	
SD1300	OI	09-07-25	08-07-213	2/8	
SD1400	OI	09-08-05	08-08-213	2/7	
SD1500	OI	09-08-25	08-09-213	2/6	
SD1600	OI	09-12-15	08-10-113	2/5	
SD1700	OI	09-12-25	08-11-113	2/4	
SD1800	OI	09-12-35	08-12-113	2/3	
SD1900	OI	09-13-05	08-13-113	2/2	
SD2000	OI	09-13-15	08-10-213	2/5	
SD2100	[illegible]	09-13-25	08-11-213	2/4	
SD2200	OI	09-13-35	08-12-213	2/3	
SD2300	OI	09-14-05	08-13-213	2/2	
SERAIO	I	09-26-15	08-24-318	5/4	
SERBIO	I	09-26-35	08-21-111	5/3	
SERCIO	IO	09 26-25	08-24-309	5/4	
SG000	O	09-22-15	08-24-010	1/3	
SMS00	O	09-26-05	08-17-112	1/5	
SREQ00	O	09-23-35	08-23-302	1/20	
3G0922B	G	09-22-25			

ELEMENT IDENTIFIER
C

MAIN STORE I/O

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
IOCHORN1	I	09-27-35	08-23-218	6/1	
IOCHORP1	I	09-28-05	08-23-318	6/1	
IOCHOSN1	O	09-28-15	08-23-213	6/1	
IOCHOSP1	O	09-28-25	08-23-313	6/1	
IOCHZRN1	I	09-27-37	08-23-316	6/1	
IOCHZRP1	I	09-28-07	08-23-317	6/1	
IOCHZSN1	O	09-28-17	08-23-214	6/1	
IOCHZSP1	O	09-28-27	08-23-314	6/1	

MAIN STORE CONTROLLER AND MEMORY	DWG SIZE C2	ISSUE 1
BELL LABORATORIES	SD-1C902-03	GBZ

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CAD 1
UNIT SYMBOL

ELEMENT IDENTIFIER
D

MAS POWER

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
+24V	P	08-44-012	08-44-012	10/4	
+24V	P	08-44-112	08-44-012	10/4	
-48TMASC	P	08-44-114	08-44-015	10/4	
-48TMASC	P	08-44-015	08-44-015	10/4	
-48TMASC	P	08-44-115	08-44-015	10/4	
-48TMASC	P	08-44-116	08-44-015	10/4	
-48TMASC	P	08-44-014	08-44-015	10/4	
-48TMASC	P	08-44-016	08-44-015	10/4	
-48TMEMA	I	03-44-116	03-44-116	9/1	
-48TMEMB	I	03-44-115	03-44-115	9/1	
-48TMEMC	I	03-44-114	03-44-114	9/1	
-48TMEMD	I	03-44-016	03-44-016	9/1	
-48TMEME	I	03-44-015	03-44-015	9/1	
-48TMEMF	I	03-44-014	03-44-014	9/1	
-48TMEMG	I	03-38-116	03-38-116	9/2	
-48TMEMH	I	03-38-115	03-38-115	9/2	
-48TMEMI	I	03-38-114	03-38-114	9/2	
-48TMEMJ	I	03-38-016	03-38-016	9/2	
-48TMEMK	I	03-38-015	03-38-015	9/2	
-48TMEML	I	03-38-014	03-38-014	9/2	
-48VMASC	P	08-44-019	08-44-017	10/4	
-48VMASC	P	08-44-117	08-44-017	10/4	
-48VMASC	P	08-44-118	08-44-017	10/4	
-48VMASC	P	08-44-119	08-44-017	10/4	
-48VMASC	P	08-44-017	08-44-017	10/4	
-48VMASC	P	08-44-018	08-44-017	10/4	
-48VMEMA	I	03-44-119	03-44-119	9/1	
-48VMEMB	I	03-44-118	03-44-118	9/1	
-48VMEMC	I	03-44-117	03-44-117	9/1	
-48VMEMD	I	03-44-019	03-44-019	9/1	
-48VMEME	I	03-44-018	03-44-018	9/1	
-48VMEMF	I	03-44-017	03-44-017	9/1	
-48VMEMG	I	03-38-119	03-38-119	9/2	
-48VMEMH	I	03-38-118	03-38-118	9/2	
-48VMEMI	I	03-38-117	03-38-117	9/2	
-48VMEMJ	I	03-38-019	03-38-019	9/2	
-48VMEMK	I	03-38-018	03-38-018	9/2	
-48VMEML	I	03-38-017	03-38-017	9/2	
C/FA1	0	03-44-314	03-38-314	9/2	
GRDA	G	03-38-319	03-44-200	9/1	
NPA0	0	03-44-315	08-25-103	10/8	
OSPAT0	I	09-41-15	08-25-109	10/8	
PAI1	0	03-38-313	03-38-313	9/2	
PAI21	I	09-42-15	08-25-106	10/8	
PA01	0	09-42-35	08-25-108	10/8	
PAT0	I	03-44-312	03-38-312	9/2	
PA1	0	03-44-313	03-44-313	9/1	
STA001	I	09-42-25	08-29-218	10/1	
STA001a	G	09-42-24			
STA011	I	03-38-210	03-38-210	9/2	
STB1	I	09-41-05	08-25-114	10/9	
STB1a	G	09-41-04			
12F046	0I	09-40-37	08-35-210	10/5	
12F046	0I	09-40-35	08-35-210	10/5	
12T046	G	09-40-27	08-35-200	10/5	
12T046	G	09-40-25	08-35-200	10/5	
3T046D	G	08-44-013	08-44-013	10/4	
3T046D	G	08-44-113	08-44-013	10/4	
5T039A	G	01-42S-GRD	01-42S-GRD	9/3	
5V039A	P	01-41S-5VCTF	01-41S-5VCTF	9/4	

ELEMENT IDENTIFIER
E

MAS SELECT

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
SADD180	0	08-18-318	08-18-318	2/16	
SADD190	0	08-18-107	08-18-107	2/16	
SAD180	0I	08-17-300	08-17-300	2/15	
SAD181	0	08-18-112	08-18-112	2/16	
SAD181	0	08-17-002	08-18-112	2/16	
SAD190	0I	08-17-311	08-17-311	2/15	
SAD191	0	08-18-307	08-17-218	2/15	
SAD191	0	08-17-218	08-17-218	2/15	
SEL00	I	08-17-101	08-17-101	1/5	
SEL01	I	08-18-109	08-18-109	1/1	
SEL10	I	08-17-011	08-17-011	1/5	
SEL11	I	08-18-210	08-18-210	1/1	

ELEMENT IDENTIFIER
F

BLIND WRITE SELECT

TERM. MODIFIER	FUNC	ACCESS TERM.	FS TERM.	LOC FS/SYM	NOTE
BLWTOP0	I	08-21-003	08-21-003	5/3	
3T0460	G	08-21-200	08-44-013	10/4	

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE C2

ISSUE 1

BELL LABORATORIES

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CAD 2

DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
	TO CONNECTION				FROM CONNECTION				
....				01-00	CTF	(NOTE 1).................			
	---			30	5V039A				
TO BTR ASSY	PWINH001			31	PWINH001				
	---			32	5V039A				
TO BTR ASSY	PWINH001			33	PWINH001				
....				01-01	CTF	(NOTE 1).................			
TO BTR ASSY	A171A			00	A171A	08-23 CP	210		
I	CSCA			02	CSCA	08-18 CP	302		
I	A161A			10	A161A	08-16 CP	201		
I	PWCA			12	RWCA	08-21 CP	100		
I	REFMOCA			20	REFMOCA	08-18 CP	100		
I	AP17.81A			22	AP17.81A	08-19 CP	201		
I	A151A			30	A151A	08-09 CP	201		
I	A101A			32	A101A	08-04 CP	201		
....				01-02	CTF	(NOTE 1).................			
TO BTR ASSY	A061A			00	A061A	08-08 CP	204		
I	A051A			02	A051A	08-07 CP	204		
I	A011A			10	A011A	08-03 CP	204		
I	A091A			12	A091A	08-03 CP	201		
I	A031A			20	A031A	08-05 CP	204		
I	A131A			22	A131A	08-07 CP	201		
I	A121A			30	A121A	08-06 CP	201		
I	A081A			32	A081A	08-02 CP	201		
....				01-03	CTF	(NOTE 1).................			
TO BTR ASSY	5V039A			00	5V039A				
I	PWINH011			01	PWINH011				
I	5V039A			02	5V039A				
I	0GD0103T			03	0GD0103T				
	---			20	5V039A				
TO BTR ASSY	PWINH011			21	PWINH011				
	---			23	0GD0103T				
TO BTR ASSY	A001A			30	A001A	08-02 CP	204		
	---			32	APEROA	03-02 CP	212		
....				01-04	CTF	(NOTE 1).................			
TO BTR ASSY	A021A			00	A021A	08-04 CP	204		
	---			02	UNSLEROA	03-02 CP	211		
TO BTR ASSY	A141A			10	A141A	08-08 CP	201		
	---			12	SELRDOA	03-02 CP	210		
TO BTR ASSY	A071A			20	A071A	08-09 CP	204		
	---			22	SELPWOA	03-02 CP	205		
TO BTR ASSY	A041A			30	A041A	08-06 CP	204		
....				01-05	CTF	(NOTE 1).................			
TO BTR ASSY	A111A			00	A111A	08-05 CP	201		
I	AP7.01A			10	AP7.01A	08-16 CP	204		
	---			12	RDD080A	03-04 CP	212		
TO BTR ASSY	FOBMT1A			20	FOBMT1A	08-19 CP	300		
	---			22	RDD000A	03-04 CP	214		
TO BTR ASSY	5V039A			30	5V039A				
I	PWINH021			31	PWINH021				
....				01-06	CTF	(NOTE 1).................			
	---			10	5V039A				
TO BTR ASSY	PWINH021			11	PWINH021				
I	WRTD080A			20	WRTD080A	08-02 CP	303		
	---			22	RDD090A	03-05 CP	212		
TO BTR ASSY	WRTD000A			30	WRTD000A	08-02 CP	300		
	---			32	RDD010A	03-05 CP	214		
....				01-07	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD090A			00	WRTD090A	08-03 CP	303		
	---			02	RDD100A	03-06 CP	212		
TO BTR ASSY	WRTD010A			10	WRTD010A	08-03 CP	300		
	---			12	RDD020A	03-06 CP	214		
TO BTR ASSY	WRTD100A			20	WRTD100A	08-04 CP	303		
	---			22	RDD110A	03-07 CP	212		
TO BTR ASSY	WRTD020A			30	WRTD020A	08-04 CP	300		
	---			32	RDD030A	03-07 CP	214		

CAD 2

(CONT'D)

DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
	TO CONNECTION				FROM CONNECTION				
....				01-08	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD110A			00	WRTD110A	08-05 CP	303		
	---			02	RDD120A	03-08 CP	212		
TO BTR ASSY	WRTD030A			10	WRTD030A	08-05 CP	300		
	---			12	RDD040A	03-08 CP	214		
TO BTR ASSY	5V039A			20	5V039A				
I	PWINH031			21	PWINH031				
....				01-09	CTF	(NOTE 1).................			
	---			00	5V039A				
TO BTR ASSY	PWINH031			01	PWINH031				
I	WRTD120A			10	WRTD120A	08-06 CP	303		
	---			12	RDD130A	03-09 CP	212		
TO BTR ASSY	WRTD040A			20	WRTD040A	08-06 CP	300		
	---			22	RDD050A	03-09 CP	214		
TO BTR ASSY	WRTD130A			30	WRTD130A	08-07 CP	303		
	---			32	RDD140A	03-10 CP	212		
....				01-10	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD050A			00	WRTD050A	08-07 CP	300		
	---			02	RDD060A	03-10 CP	214		
TO BTR ASSY	WRTD140A			10	WRTD140A	08-08 CP	303		
	---			12	RDD150A	03-11 CP	212		
TO BTR ASSY	SPARE10			20	SPARE10	03-02 CP	313		
	---			22	RDD070A	03-11 CP	214		
TO BTR ASSY	WRTD060A			30	WRTD060A	08-08 CP	300		
	---			32	RDD200A	03-12 CP	212		
....				01-11	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD150A			00	WRTD150A	08-09 CP	303		
	---			02	RDD160A	03-12 CP	214		
TO BTR ASSY	5V039A			10	5V039A				
I	PWINH041			11	PWINH041				
	---			30	5V039A				
TO BTR ASSY	PWINH041			31	PWINH041				
....				01-12	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD070A			00	WRTD070A	08-09 CP	300		
	---			02	RDD210A	03-13 CP	212		
TO BTR ASSY	SPARE20			10	SPARE20	03-02 CP	314		
	---			12	RDD170A	03-13 CP	214		
TO BTR ASSY	WRTD200A			20	WRTD200A	08-10 CP	303		
	---			22	RDD220A	03-14 CP	212		
TO BTR ASSY	SPARE21			30	SPARE21	03-02 CP	312		
	---			32	RDD180A	03-14 CP	214		
....				01-13	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD160A			00	WRTD160A	08-10 CP	300		
	---			02	RDD230A	03-16 CP	212		
	---			12	RDD190A	03-16 CP	214		
TO BTR ASSY	WRTD210A			20	WRTD210A	08-11 CP	303		
	---			22	RDDPHCA	03-17 CP	212		
TO BTR ASSY	WRTD170A			30	WRTD170A	08-11 CP	300		
	---			32	RDDPLOA	03-17 CP	214		
....				01-14	CTF	(NOTE 1).................			
TO BTR ASSY	5V039A			00	5V039A				
I	PWINH051			01	PWINH051				
	---			20	5V039A				
TO BTR ASSY	PWINH051			21	PWINH051				
I	WRTD220A			30	WRTD220A	08-12 CP	303		
....				01-15	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD180A			00	WRTD180A	08-12 CP	300		
	---			12	SPARE28				
	---			22	SPARE34				
TO BTR ASSY	WRTD230A			30	WRTD230A	08-13 CP	303		
	---			32	SPARE26				

CAD 2

(CONT'D)

DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
	TO CONNECTION				FROM CONNECTION				
....				01-16	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD190A			00	WRTD190A	08-13 CP	300		
	---			02	SPARE32				
TO BTR ASSY	WRTDPHCA			10	WRTDPHCA	08-16 CP	303		
	---			12	SPARE24				
TO BTR ASSY	WRTDPLOA			20	WRTDPLOA	08-16 CP	300		
	---			22	SPARE30				
TO BTR ASSY	5V039A			30	5V039A				
I	PWINH061			31	PWINH061				
....				01-18	CTF	(NOTE 1).................			
	---			30	5V039A				
TO BTR ASSY	PWINH061			31	PWINH061				
....				01-19	CTF	(NOTE 1).................			
TO BTR ASSY	WRTDPH0B			00	WRTDPH0B	08-16 CP	103		
	---			02	RDDPH0B	03-18 CP	212		
TO BTR ASSY	WRTDPL0B			10	WRTDPL0B	08-16 CP	100		
	---			12	RDDPL0B	03-18 CP	214		
TO BTR ASSY	WRTD230B			20	WRTD230B	08-13 CP	103		
	---			22	RDD230B	03-19 CP	212		
TO BTR ASSY	WRTD190B			30	WRTD190B	08-13 CP	100		
	---			32	RDD190B	03-19 CP	214		
....				01-20	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD220B			00	WRTD220B	08-12 CP	103		
	---			02	RDD220B	03-20 CP	212		
	---			12	RDD180B	03-20 CP	214		
TO BTR ASSY	WRTD180B			20	WRTD180B	08-12 CP	100		
	---			22	RDD210B	03-21 CP	212		
TO BTR ASSY	WRTD210B			30	WRTD210B	08-11 CP	103		
	---			32	RDD170B	03-21 CP	214		
....				01-21	CTF	(NOTE 1).................			
TO BTR ASSY	5V039A			00	5V039A				
I	PWINH071			01	PWINH071				
	---			20	5V039A				
TO BTR ASSY	PWINH071			21	PWINH071				
I	WRTD170B			30	WRTD170B	08-11 CP	100		
	---			32	RDD200B	03-22 CP	212		
....				01-22	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD200B			00	WRTD200B	08-10 CP	103		
	---			02	RDD160B	03-22 CP	214		
	---			12	RDD150B	03-23 CP	212		
TO BTR ASSY	WRTD160B			20	WRTD160B	08-10 CP	100		
	---			22	RDD070B	03-23 CP	214		
	---			32	RDD140B	03-24 CP	212		
....				01-23	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD150B			00	WRTD150B	08-09 CP	103		
	---			02	RDD060B	03-24 CP	214		
	---			12	RDD130B	03-25 CP	212		
TO BTR ASSY	WRTD070B			20	WRTD070B	08-09 CP	100		
	---			22	RDD050B	03-25 CP	214		
TO BTR ASSY	5V039A			30	5V039A				
I	PWINH081			31	PWINH081				

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE	ISSUE
CZ	1

BELL LABORATORIES

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CAD 2
(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				01-24	CTF	(NOTE 1).................			
	---			10	5V039A				
TO BTR ASSY	PWINH081			11	PWINH081				
I	WRTD140B			20	WRTD140B	08-08 CP	103		
	---			22	RDD120B	03-26 CP	212		
TO BTR ASSY	WRTD060B			30	WRTD060B	08-08 CP	100		
	---			32	RDD040B	03-26 CP	214		
....				01-25	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD130B			00	WRTD130B	08-07 .P	103		
	---			02	RDD110B	03-27 CP	212		
	---			12	RDD030B	03-27 CP	214		
TO BTR ASSY	WRTD050B			20	WRTD050B	08-07 CP	100		
	---			22	RDD100B	03-28 CP	212		
TO BTR ASSY	SPARE11			30	SPARE11	03-33 CP	313		
	---			32	RDD020B	03-28 CP	214		
....				01-26	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD120B			00	WRTD120B	08-06 CP	103		
	---			02	RDD090B	03-29 CP	212		
TO BTR ASSY	WRTD040B			10	WRTD040B	08-06 CP	100		
	---			12	RDD010B	03-29 CP	214		
TO BTR ASSY	5V039A			20	5V039A				
I	PWINH091			21	PWINH091				
....				01-27	CTF	(NOTE 1).................			
	---			00	5V039A				
TO BTR ASSY	PWINH091			01	PWINH091				
I	WRTD110B			10	WRTD110B	08-05 CP	103		
I	WRTD030B			20	WRTD030B	08-05 CP	100		
I	WRTD100B			30	WRTD100B	08-04 CP	103		
	---			32	SPARE29				
....				01-28	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD020B			00	WRTD020B	08-04 CP	100		
	---			02	SPARE35				
TO BTR ASSY	WRTD090B			10	WRTD090B	08-03 CP	103		
	---			12	SPARE27				
TO BTR ASSY	WRTD010B			20	WRTD010B	08-03 CP	100		
	---			22	SPARE33				
TO BTR ASSY	WRTD080B			30	WRTD080B	08-02 CP	103		
	---			32	SPARE25				
....				01-29	CTF	(NOTE 1).................			
TO BTR ASSY	WRTD000B			00	WRTD000B	08-02 CP	100		
	---			02	SPARE31				
TO BTR ASSY	5V039A			10	5V039A				
I	PWINH101			11	PWINH101				
	---			30	5V039A				
TO BTR ASSY	PWINH101			31	PWINH101				
....				01-30	CTF	(NOTE 1).................			
TO BTR ASSY	FOBMT1B			00	FOBMT1B	08-19 CP	100		
I	A011B			10	A011B	08-03 CP	004		
	---			12	SELRW0B	03-33 CP	205		
TO BTR ASSY	A031B			20	A031B	08-05 CP	004		
	---			22	SELRC0B	03-33 CP	210		
TO BTR ASSY	A121B			30	A121B	08-06 CP	001		
	---			32	UNSLER0B	03-33 CP	211		
....				01-31	CTF	(NOTE 1).................			
TO BTR ASSY	A141B			00	A141B	08-08 CP	001		
	---			02	RDD080B	03-31 CP	212		
TO BTR ASSY	A071B			10	A071B	08-09 CP	004		
	---			12	RDD000B	03-31 CP	214		
TO BTR ASSY	A051B			20	A051B	08-07 CP	004		
	---			22	APER0B	03-33 CP	212		
TO BTR ASSY	A021B			30	A021B	08-04 CP	004		

CAD 2
(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				01-32	CTF	(NOTE 1).................			
TO BTR ASSY	5V039A			00	5V039A				
I	PWINH111			01	PWINH111				
	---			20	5V039A				
TO BTR ASSY	PWINH121			21	PWINH121				
	---			22	5V039A				
TO BTR ASSY	PWINH121			23	PWINH121				
I	A041B			30	A041B	08-06 CP	004		
I	A001B			32	A001B	08-02 CP	004		
....				01-33	CTF	(NOTE 1).................			
TO BTR ASSY	A111B			00	A111B	08-05 CP	001		
I	A081B			02	A081B	08-02 CP	001		
I	AP7.01B			10	AP7.01B	08-16 CP	004		
I	A131B			12	A131B	08-07 CP	001		
I	CS0B			20	CS0B	08-18 CP	300		
I	A091B			22	A091B	08-03 CP	001		
I	REFMD0B			30	REFMD0B	08-18 CP	001		
I	A061B			32	A061B	08-08 CP	004		
....				01-34	CTF	(NOTE 1).................			
TO BTR ASSY	A151B			00	A151B	08-09 CP	001		
I	A101B			02	A101B	08-04 CP	001		
I	A161B			10	A161B	08-16 CP	001		
I	AP17.81B			12	AP17.81B	08-19 CP	101		
I	A171B			20	A171B	08-23 CP	311		
I	RW0B			22	RW0B	08-21 CP	300		
I	5V039A			30	5V039A				
I	PWINH111			31	PWINH111				
I	5V039A			32	5V039A				
I	PWINHT1			33	PWINHT1				
....				01-35	CTF	(NOTE 1).................			
	---			20	SPARE23	03-33 CP	312		
	---			30	SPARE22	03-33 CP	314		
....				05-01	CTF	(NOTE 1).................			
	---			05	CS0A	08-18 CP	302		
	---			07	A171A	08-23 CP	210		
	---			15	RW0A	08-21 CP	100		
	---			17	A161A	08-16 CP	201		
	---			25	AP17.81A	08-19 CP	201		
	---			27	REFMD0A	08-18 CP	100		
	---			35	A101A	08-04 CP	201		
	---			37	A151A	08-09 CP	201		
....				05-02	CTF	(NOTE 1).................			
	---			05	A051A	08-07 CP	204		
	---			07	A061A	08-08 CP	204		
	---			15	A091A	08-03 CP	201		
	---			17	A011A	08-03 CP	204		
	---			25	A131A	08-07 CP	201		
	---			27	A031A	08-05 CP	204		
	---			35	A081A	08-02 CP	201		
	---			37	A121A	08-06 CP	201		
....				05-03	CTF	(NOTE 1).................			
	---			35	APER0A	03-02 CP	212		
	---			37	A001A	08-02 CP	204		
....				05-04	CTF	(NOTE 1).................			
	---			05	UNSLER0A	03-02 CP	211		
	---			07	A021A	08-04 CP	204		
	---			15	SELRC0A	03-02 CP	210		
	---			17	A141A	08-08 CP	201		
	---			25	SELRW0A	03-02 CP	205		
	---			27	A071A	08-09 CP	204		
	---			37	A041A	08-06 CP	204		

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(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				05-05	CTF	(NOTE 1).................			
	---			07	A111A	08-05 CP	201		
	---			15	RDD080A	03-04 CP	212		
	---			17	AP7.01A	08-16 CP	204		
	---			25	RDD000A	03-04 CP	214		
	---			27	FOBMT1A	08-19 CP	300		
....				05-06	CTF	(NOTE 1).................			
	---			25	RDD090A	03-05 CP	212		
	---			27	WRTD080A	08-02 CP	303		
	---			35	RDD010A	03-05 CP	214		
	---			37	WRTD000A	08-02 CP	300		
....				05-07	CTF	(NOTE 1).................			
	---			05	RDD100A	03-06 CP	212		
	---			07	WRTD090A	08-03 CP	303		
	---			15	RDD020A	03-06 CP	214		
	---			17	WRTD010A	08-03 CP	300		
	---			25	RDD110A	03-07 CP	212		
	---			27	WRTD100A	08-04 CP	303		
	---			35	RDD030A	03-07 CP	214		
	---			37	WRTD020A	08-04 CP	300		
....				05-08	CTF	(NOTE 1).................			
	---			05	RDD120A	03-08 CP	212		
	---			07	WRTD110A	08-05 CP	303		
	---			15	RDD040A	03-08 CP	214		
	---			17	WRTD030A	08-05 CP	300		
....				05-09	CTF	(NOTE 1).................			
	---			15	RDD130A	03-09 CP	212		
	---			17	WRTD120A	08-06 CP	303		
	---			25	RDD050A	03-09 CP	214		
	---			27	WRTD040A	08-06 CP	300		
	---			35	RDD140A	03-10 CP	212		
	---			37	WRTD130A	08-07 CP	303		
....				05-10	CTF	(NOTE 1).................			
	---			05	RDD060A	03-10 CP	214		
	---			07	WRTD050A	08-07 CP	300		
	---			15	RDD150A	03-11 CP	212		
	---			17	WRTD140A	08-08 CP	303		
	---			25	RDD070A	03-11 CP	214		
	---			27	SPARE10	03-02 CP	313		
	---			35	RDD200A	03-12 CP	212		
	---			37	WRTD060A	08-08 CP	300		
....				05-11	CTF	(NOTE 1).................			
	---			05	RDD160A	03-12 CP	214		
	---			07	WRTD150A	08-09 CP	303		

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE C2

ISSUE 1

BELL LABORATORIES

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(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				05-12	CTF	(NOTE 1).................			
	---			05	RDD210A	03-13 CP	212		
	---			07	WRTD070A	08-09 CP	300		
	---			15	RDD170A	03-13 CP	214		
	---			17	SPARE20	03-02 CP	314		
	---			25	RDD220A	03-14 CP	212		
	---			27	WRT0200A	08-10 CP	303		
	---			35	RDD180A	03-14 CP	214		
	---			37	SPARE21	03-02 CP	312		
....				05-13	CTF	(NOTE 1).................			
	---			05	RDD230A	03-16 CP	212		
	---			07	WRT0160A	08-10 CP	300		
	---			15	RDD190A	03-16 CP	214		
	---			25	RDDPH0A	03-17 CP	212		
	---			27	WRTD210A	08-11 CP	303		
	---			35	RDDPL0A	03-17 CP	214		
	---			37	WRTD170A	08-11 CP	300		
....				05-14	CTF	(NOTE 1).................			
	---			35	SPARE28				
	---			37	WRTD220A	08-12 CP	303		
....				05-15	CTF	(NOTE 1).................			
	---			05	SPARE34				
	---			07	WRTD180A	08-12 CP	300		
	---			15	SPARE26				
	---			25	SPARE32				
	---			35	SPARE24				
	---			37	WRTD230A	08-13 CP	303		
....				05-16	CTF	(NOTE 1).................			
	---			05	SPARE30				
	---			07	WRTD190A	08-13 CP	300		
	---			15	MSEL00	08-19 CP	219		
	---			17	WRTDPH0A	08-16 CP	303		
	---			25	RSEL01	08-19 CP	103		
	---			27	WRTDPL0A	08-16 CP	300		
....				05-19	CTF	(NOTE 1).................			
	---			05	RDDPH0B	03-18 CP	212		
	---			07	WRTDPH0B	08-16 CP	103		
	---			15	RDDPL0B	03-18 CP	214		
	---			17	WPTDPL0B	08-16 CP	100		
	---			25	RDD230B	03-19 CP	212		
	---			27	WRTD230B	08-13 CP	103		
	---			35	RDD190B	03-19 CP	214		
	---			37	WRTD190B	08-13 CP	100		
....				05-20	CTF	(NOTE 1).................			
	---			05	RDD220B	03-20 CP	212		
	---			07	WRTD220B	08-12 CP	103		
	---			15	RDD180B	03-20 CP	214		
	---			25	RDD210B	03-21 CP	212		
	---			27	WRTD180B	08-12 CP	100		
	---			35	RDD170B	03-21 CP	214		
	---			37	WRTD210B	08-11 CP	103		
....				05-21	CTF	(NOTE 1).................			
	---			35	RDD200B	03-22 CP	212		
	---			37	WRTD170B	08-11 CP	100		
....				05-22	CTF	(NOTE 1).................			
	---			05	RDD160B	03-22 CP	214		
	---			07	WRTD200B	08-10 CP	103		
	---			15	RDD150B	03-23 CP	212		
	---			25	RDD070B	03-23 CP	214		
	---			27	WRTD160B	08-10 CP	100		
	---			35	RDD140B	03-24 CP	212		

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(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				05-23	CTF	(NOTE 1).................			
	---			05	RDD060B	03-24 CP	214		
	---			07	WRT0150B	08-09 CP	103		
	---			15	RDD130B	03-25 CP	212		
	---			25	RDD050B	03-25 CP	214		
	---			27	WRTD070B	08-09 CP	100		
....				05-24	CTF	(NOTE 1).................			
	---			25	RDD120B	03-26 CP	212		
	---			27	WRTD140B	08-08 CP	103		
	---			35	RDD040B	03-26 CP	214		
	---			37	WRTD060B	08-08 CP	100		
....				05-25	CTF	(NOTE 1).................			
	---			05	RDD110B	03-27 CP	212		
	---			07	WRTD130B	08-07 CP	103		
	---			15	RDD030B	03-27 CP	214		
	---			25	RDD100B	03-28 CP	212		
	---			27	WRTD050B	08-07 CP	100		
	---			35	RDD020B	03-28 CP	214		
	---			37	SPARE11	03-33 CP	313		
....				05-26	CTF	(NOTE 1).................			
	---			05	RDD090B	03-29 CP	212		
	---			07	WRTD120B	08-06 CP	103		
	---			15	RDD010B	03-29 CP	214		
	---			17	WRTD040B	08-06 CP	100		
....				05-27	CTF	(NOTE 1).................			
	---			15	SPARE29				
	---			17	WRTD110B	08-05 CP	103		
	---			25	SPARE35				
	---			27	WRTD030B	08-05 CP	100		
	---			35	SPARE27				
	---			37	WRT0100B	08-04 CP	103		
....				05-28	CTF	(NOTE 1).................			
	---			05	SPARE33				
	---			07	WRTD020B	08-04 CP	100		
	---			15	SPARE25				
	---			17	WRTD090B	08-03 CP	103		
	---			25	SPARE31				
	---			27	WRT0010B	08-03 CP	100		
	---			35	MSEL10	08-19 CP	318		
	---			37	WRTD080B	08-02 CP	103		
....				05-29	CTF	(NOTE 1).................			
	---			05	RSEL11	08-19 CP	203		
	---			07	WRTD000B	08-02 CP	100		
....				05-30	CTF	(NOTE 1).................			
	---			07	FOBMT1B	08-19 CP	100		
	---			15	SELRW0B	03-33 CP	205		
	---			17	A011B	08-03 CP	004		
	---			25	SELRC0B	03-33 CP	210		
	---			27	A031B	08-05 CP	004		
	---			35	UNSLER0B	03-33 CP	211		
	---			37	A121B	08-06 CP	001		
....				05-31	CTF	(NOTE 1).................			
	---			05	RDD080B	03-31 CP	212		
	---			07	A141B	08-08 CP	001		
	---			15	RDD000B	03-31 CP	214		
	---			17	A071B	08-09 CP	004		
	---			25	APER0B	03-33 CP	212		
	---			27	A051B	08-07 CP	004		
	---			35	PWINHT1	07-27 CTF	01		
	---			37	A021B	08-04 CP	004		

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(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				05-32	CTF	(NOTE 1).................			
	---			35	A001B	08-02 CP	004		
	---			37	A041B	08-06 CP	004		
....				05-33	CTF	(NOTE 1).................			
	---			05	A081B	08-02 CP	001		
	---			07	A111B	08-05 CP	001		
	---			15	A131B	08-07 CP	001		
	---			17	AP7.01B	08-16 CP	004		
	---			25	A091B	08-03 CP	001		
	---			27	CS0B	08-18 CP	300		
	---			35	A061B	08-08 CP	004		
	---			37	REFMD0B	08-18 CP	001		
....				05-34	CTF	(NOTE 1).................			
	---			05	A101B	08-04 CP	001		
	---			07	A151B	08-09 CP	001		
	---			15	AP17.81B	08-19 CP	101		
	---			17	A161B	08-16 CP	001		
	---			25	RW0B	08-21 CP	300		
	---			27	A171B	08-23 CP	311		
....				05-35	CTF	(NOTE 1).................			
	---			27	SPARE23	03-33 CP	312		
	---			37	SPARE22	03-33 CP	314		
....				07-02	CTF	(NOTE 1).................			
	---			30	3V046A				
TO BTR ASSY	PWINH171			31	PWINH171				
	---			32	3V046A				
TO BTR ASSY	PWINH171			33	PWINH171				
....				07-03	CTF	(NOTE 1).................			
TO BTR ASSY	RDD020A			00	RDD020A	03-06 CP	214		
│	RDD000A			02	RDD000A	03-04 CP	214		
│	RDD020B			10	RDD020B	03-28 CP	214		
│	RDD000B			12	RDD000B	03-31 CP	214		
│	RDD100A			20	RDD100A	03-06 CP	212		
│	RDD080A			22	RDD080A	03-04 CP	212		
│	RDD100B			30	RDD100B	03-28 CP	212		
│	RDD080B			32	RDD080B	03-31 CP	212		
....				07-04	CTF	(NOTE 1).................			
TO BTR ASSY	RDD030A			00	RDD030A	03-07 CP	214		
│	RDD010A			02	RDD010A	03-05 CP	214		
│	RDD030B			10	RDD030B	03-27 CP	214		
│	RDD010B			12	RDD010B	03-29 CP	214		
│	RDD110A			20	RDD110A	03-07 CP	212		
│	RDD090A			22	RDD090A	03-05 CP	212		
│	RDD110B			30	RDD110B	03-27 CP	212		
│	RDD090B			32	RDD090B	03-29 CP	212		
....				07-05	CTF	(NOTE 1).................			
TO BTR ASSY	3V046A			00	3V046A				
│	PWINH161			01	PWINH161				
│	3V046A			02	3V046A				
│	PWINH181			03	PWINH181				

MAIN STORE CONTROLLER AND MEMORY

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BELL LABORATORIES

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TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				07-06	CTF	(NOTE 1)..................			
	---			30	3V046B				
TO BTR ASSY	PWINH161			31	PWINH161				
	---			32	3V046B				
TO BTR ASSY	PWINH181			33	PWINH181				
....				07-07	CTF	(NOTE 1)..................			
TO BTR ASSY	RDD060A			00	RDD060A	03-10 CP	214		
│	RDD040A			02	RDD040A	03-08 CP	214		
│	RDD060B			10	RDD060B	03-24 CP	214		
│	RDD040B			12	RDD040B	03-26 CP	214		
│	RDD140A			20	RDD140A	03-10 CP	212		
│	RDD120A			22	RDD120A	03-08 CP	212		
│	RDD140B			30	RDD140B	03-24 CP	212		
│	RDD120B			32	RDD120B	03-26 CP	212		
....				07-08	CTF	(NOTE 1)..................			
TO BTR ASSY	RDD070A			00	RDD070A	03-11 CP	214		
│	RDD050A			02	RDD050A	03-09 CP	214		
│	RDD070B			10	RDD070B	03-23 CP	214		
│	RDD050B			12	RDD050B	03-25 CP	214		
│	RDD150A			20	RDD150A	03-11 CP	212		
│	RDD130A			22	RDD130A	03-09 CP	212		
│	RDD150B			30	RDD150B	03-23 CP	212		
│	RDD130B			32	RDD130B	03-25 CP	212		
....				07-09	CTF	(NOTE 1)..................			
TO BTR ASSY	3V046B			00	3V046B				
│	PWINH151			01	PWINH151				
│	3V046B			02	3V046B				
│	PWINH191			03	PWINH191				
....				07-10	CTF	(NOTE 1)..................			
	3V046C			30	3V046C				
TO BTR ASSY	PWINH151			31	PWINH151				
	3V046C			32	3V046C				
TO BTR ASSY	PWINH191			33	PWINH191				
....				07-11	CTF	(NOTE 1)..................			
TO BTR ASSY	RDD180A			00	RDD180A	03-14 CP	214		
│	RDD160A			02	RDD160A	03-12 CP	214		
│	RDD180B			10	RDD180B	03-20 CP	214		
│	RDD160B			12	RDD160B	03-22 CP	214		
│	RDD220A			20	RDD220A	03-14 CP	212		
│	RDD200A			22	RDD200A	03-12 CP	212		
│	RDD220B			30	RDD220B	03-20 CP	212		
│	RDD200B			32	RDD200B	03-22 CP	212		
....				07-12	CTF	(NOTE 1)..................			
TO BTR ASSY	RDD190A			00	RDD190A	03-16 CP	214		
│	RDD170A			02	RDD170A	03-13 CP	214		
│	RDD190B			10	RDD190B	03-19 CP	214		
│	RDD170B			12	RDD170B	03-21 CP	214		
│	RDD230A			20	RDD230A	03-16 CP	212		
│	RDD210A			22	RDD210A	03-13 CP	212		
│	RDD230B			30	RDD230B	03-19 CP	212		
│	RDD210B			32	RDD210B	03-21 CP	212		
....				07-13	CTF	(NOTE 1)..................			
TO BTR ASSY	3V046C			00	3V046C				
│	PWINH141			01	PWINH141				
│	3V046C			02	3V046C				
│	PWINH201			03	PWINH201				
....				07-17	CTF	(NOTE 1)..................			
	---			30	3V046D				
TO BTR ASSY	PWINH141			31	PWINH141				
	---			32	3V046D				
TO BTR ASSY	PWINH201			33	PWINH201				

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TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
....				07-18	CTF	(NOTE 1)..................			
TO BTR ASSY	RDDPH0A			00	RDDPH0A	03-17 CP	212		
│	RDDPL0A			10	RDDPL0A	03-17 CP	214		
│	RDDPH0B			20	RDDPH0B	03-18 CP	212		
│	RDDPL0B			30	RDDPL0B	03-18 CP	214		
....				07-19	CTF	(NOTE 1)..................			
TO BTR ASSY	RSEL11			20	RSEL11	08-19 CP	203		
│	APER0B			22	APER0B	03-33 CP	212		
│	RSEL01			30	RSEL01	08-19 CP	103		
│	APER0A			32	APER0A	03-02 CP	212		
....				07-20	CTF	(NOTE 1)..................			
TO BTR ASSY	3V046D			00	3V046D				
│	PWINH131			01	PWINH131				
│	3V046D			02	3V046D				
│	PWINH211			03	PWINH211				
....				07-24	CTF	(NOTE 1)..................			
	---			30	3V046D				
TO BTR ASSY	PWINH131			31	PWINH131				
	---			32	3V046D				
TO BTR ASSY	PWINH211			33	PWINH211				
....				07-25	CTF	(NOTE 1).............			
TO BTR ASSY	MSEL10			02	MSEL10	08-19 CP	318		
│	MSEL00			12	MSEL00	08-19 CP	219		
│	SELRC0A			22	SELRC0A	03-02 CP	210		
│	SELRC0B			32	SELRC0B	03-33 CP	210		
....				07-26	CTF	(NOTE 1)..................			
TO BTR ASSY	UNSLER0A			00	UNSLER0A	03-02 CP	211		
│	UNSLER0B			10	UNSLER0B	03-33 CP	211		
│	SELRW0A			20	SELRW0A	03-02 CP	205		
│	SELRW0B			30	SELRW0B	03-33 CP	205		
....				07-27	CTF	(NOTE 1)..................			
TO BTR ASSY	3V046D			00	3V046D				
│	PWINHT1			01	PWINHT1	05-31 CTF	35		
│	3V046D			02	3V046D				
│	TL11			03	TL11	08-25 CP	003		

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TEST CONNECTOR(SEE NOTE 203)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
.............TS1				08-01	CONNECTOR	(NOTE 1)..................			
	NC			100	3VG0801B	08-01 CONN	200		
	3VG0801B			101	3VG0801B	08-01 CONN	100		
	3VG0801B			102	3VG0801B	08-01 CONN	101		
	3VG0801B			103	3VG0801B	08-01 CONN	102		
	3VG0801B			104	3VG0801B	08-01 CONN	103		
	3VG0801B			105	3VG0801B	08-01 CONN	104		
	3VG0801B			106	3VG0801B	08-01 CONN	105		
	3VG0801B			107	3VG0801B	08-01 CONN	106		
	3VG0801B			108	3VG0801B	08-01 CONN	107		
	NC			109	3VG0801B	08-01 CONN	108		
	NC			110	3VG0801T	08-01 CONN	310		
	3VG0801T			111	3VG0801T	08-01 CONN	110		
	3VG0801T			112	3VG0801T	08-01 CONN	111		
	3VG0801T			113	3VG0801T	08-01 CONN	112		
	3VG0801T			114	3VG0801T	08-01 CONN	113		
	3VG0801T			115	3VG0801T	08-01 CONN	114		

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(CONT'D)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
.............TS1				08-01 (CONT'D)	CONNECTOR	(NOTE 1)..................			
	3VG0801T			116	3VG0801T	08-01 CONN	115		
	3VG0801T			117	3VG0801T	08-01 CONN	116		
	3VG0801T			118	3VG0801T	08-01 CONN	117		
	NC			200	3VG0801B	08-01 CONN	109		
	NC			300	3VG0801B	08-01 CONN	301		
	3VG0801B			301	3VG0801B	08-01 CONN	302		
	3VG0801B			302	3VG0801B	08-01 CONN	303		
	3VG0801B			303	3VG0801B	08-01 CONN	304		
	3VG0801B			304	3VG0801B	08-01 CONN	305		
	3VG0801B			305	3VG0801B	08-01 CONN	306		
	3VG0801B			306	3VG0801B	08-01 CONN	307		
	3VG0801B			307	3VG0801B	08-01 CONN	308		
	3VG0801B			308	3VG0801B	08-01 CONN	309		
	NC			309	3VG0801B	08-01 CONN	109		
	NC			310	3VG0801T	08-01 CONN	311		
	3VG0801T			311	3VG0801T	08-01 CONN	312		
	3VG0801T			312	3VG0801T	08-01 CONN	313		
	3VG0801T			313	3VG0801T	08-01 CONN	314		
	3VG0801T			314	3VG0801T	08-01 CONN	315		
	3VG0801T			315	3VG0801T	08-01 CONN	316		
	3VG0801T			316	3VG0801T	08-01 CONN	317		
	3VG0801T			317	3VG0801T	08-01 CONN	318		
	3VG0801T			318	3VG0801T	08-01 CONN	319		
	NC			319	3VG0801T	08-01 CONN	318		

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(FOR ED-4C091-30)

TO CONNECTION					FROM CONNECTION				
DESTINATION	LEAD DESIG	METHOD	WIRE SYM	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
.............5VCTF				01-41S	LUG	(NOTE 2)..................			
TO CONN CKT	5V039A			5VCTF	5V039A				
.............GRD				01-42S	LUG	(NOTE 2)..................			
TO CONN CKT	5T039A			GRD	0GRDMOD8				

MAIN STORE CONTROLLER AND MEMORY

DWG SIZE
C2

BELL LABORATORIES

SD-1C902-03

GE

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